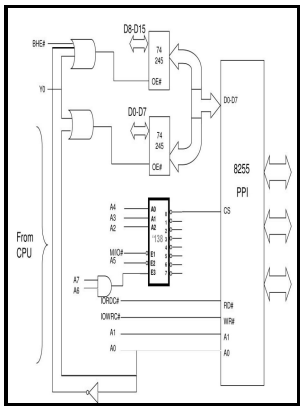
Microcomputer interfacing with the 8255 PPI chip

H. W. Sams - MICROCOMPUTER INTERFACING WITH THE 8255 PPI CHIP



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Notes: Includes index.
This edition was published in 1979

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Tags: #8255 #Programmable #Peripheral #Interface #Chip

Description of 8255 PPI

Intel 8255 is a peripheral interface PPI chip which is programmable.

MICROCOMPUTER INTERFACING WITH THE 8255 PPI CHIP

A port inside the 8255 is selected for communication by the 8085 by the address-input pins A1 and A0, as shown in the above table. A low signal at this pin shows the enabling of communication between the 8255 and the processor. Rest of the pins are allotted to the signals discussed above.

What is 8255 Programmable Peripheral Interface (PPI)? Definition, Architecture, Pin Diagram and Modes of Operation of 8255

When the microprocessor is reset, it also resets the 8255 via this connection. These pins are output pins if Port C upper is programmed as an output port.

Interfacing of 8051 with 8255 Programmable Peripheral Interface

To learn in detail about the demultiplexing process, you can check out this section on. Algorithm Consider this example where we are required to take input data from port A and port B and output it at port C.

Interfacing of 8085 with 8255 Programmable Peripheral Interface

Pin Diagram of 8255 PPI The figure below represents the 40 pin configuration of 8255 programmable peripheral interface: As we can see that pin number 27 to 34 is allotted to data bus. After a reset of 8255, all the three ports of 8255 work as input ports in mode 0, which is the simplest mode of operation.

Programmable peripheral interface 8255

Input and Output data are latched.

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