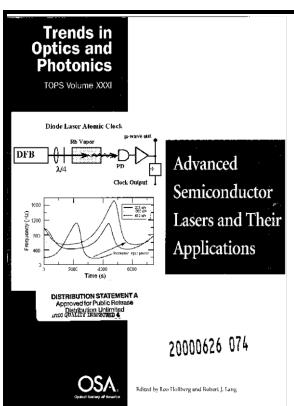


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Vertical

This metallization scheme allows flip-chip mounting junction-side down on Si subcarrier.

Solid state lasers VIII : 25

The VCSEL array chip is placed above the window in the Si subcarrier and is assembled using a self-aligned bonding technique with PbSn solder bumps. The coupling between the two monolithically grown cavities leads to two distinct Fabry-Perot modes whose separation and localization are designed in such a way that the interaction between the two modes leads to dual laser emission. Photon rate equation parameters, such as the net modal gain and the spontaneous emission into a mode, are calculated using this mode set as a basis.

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This paper focuses on 1 the characteristics of oxide-confined multi-wavelength

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VCSEL arrays, 2 the design and comparison of optimized REPD structures at 850 nm and at 980 nm, and 3 wavelength multiplexing and demultiplexing using VCSEL and REPD arrays.

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The short wavelength bottom- emitting VCSELs are fabricated by wafer fusion using an inert gas low temperature annealing process.



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