

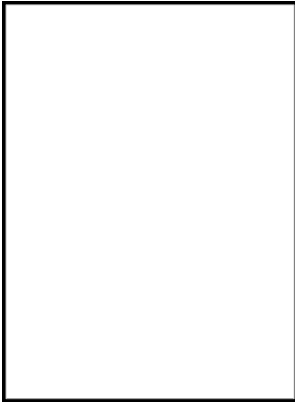
# ESD in silicon integrated circuits

J. Wiley - ESD protection techniques for high frequency integrated circuits

Description: -

-

Paleobotany -- Holocene.  
 Paleoclimatology -- Quaternary.  
 Paleoclimatology -- Northwest Territories -- Mackenzie.  
 Paleoclimatology -- Nunavut -- Keewatin.  
 Palynology -- Northwest Territories -- Mackenzie.  
 Palynology -- Nunavut -- Keewatin.  
 Death -- Religious aspects -- Catholic Church  
 Redemption  
 Forgiveness of sin  
 Universities and colleges -- United States.  
 Grove City college, Grove City, Pa. [from old catalog]  
 Ketler, Isaac Conrad, 1853-1913. [from old catalog]  
 Static eliminators.  
 Electrostatics.  
 Integrated circuits -- Protection.  
 Semiconductors -- Protection.ESD in silicon integrated circuits  
 -ESD in silicon integrated circuits  
 Notes: Includes bibliographical references and indexes.  
 This edition was published in 2002



Filesize: 54.95 MB

#Integrated #Circuits

Tags: #Amerasekera, #ESD #in #Silicon

On

Yet, few are capable of providing effective and efficient protection against ESD without latch-up. To address these issues, generally one of two alternative solutions is utilized. This is a significant problem since the current produced by an ESD is capable of destroying microchips.

## Amerasekera, ESD in Silicon Integrated Circuits

It provides guidance on the implementation of circuit protection measures. Topics include: Reproduction of ESD-type events in the lab, analysis of semiconductor devices, detailed study of design and layout, case studies of ESD problem solving. Experimental results showing improved ESD performance are included for some of these techniques.

## ESD protection techniques for high frequency integrated circuits

This comprehensive book on application-specific integrated circuits ASICs describes the latest methods in VLSI-systems design.

## ESD in silicon integrated circuits in SearchWorks catalog

In analog integrated circuits at lower frequencies, such as filters, nonlinear behaviour limits the dynamic range. The book covers both semicustom and programmable ASIC types.

## ESD protection techniques for high frequency integrated circuits

The second involves using empirical modification of the lateral structure of the Silicon Controlled Rectifier SCR, but this approach is time consuming and has limited applicability. As a consequence, ASICs and ASIC-design methods have become increasingly popular in industry for a wide range of applications. Bibliography Includes bibliographical references and index.

---

## Related Books

- [Randomisation und Aufklärung bei klinischen Studien in der Onkologie](#)
- [Diaspora, memory and identity - a search for home](#)
- [Portable Edmund Wilson](#)
- [Beiträge zur kenntnis der p-oxy-chinolin-ana-sulfonsäure.](#)
- [Wo kan Zhong wai dian ying](#)