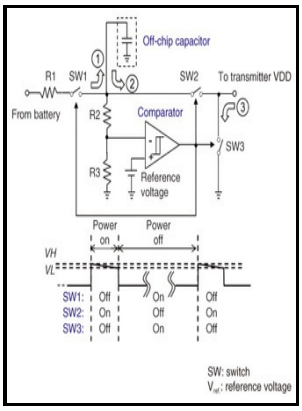
Low-power CMOS design for wireless transceivers

Kluwer Academic - CMOS IC Design for Wireless Medical and Health Care



Description: -
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Medicine -- Bibliography.
Metal oxide semiconductors, Complementary.
Low voltage integrated circuits.
Radio -- Transmitter-receivers.
Radio circuits -- Design and construction.Low-power CMOS design for wireless transceivers
-Low-power CMOS design for wireless transceivers
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Design of a low

The new evolutions in wireless communications set new requirements for the trans ceivers transmitter-receivers.

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Communication at these higher frequencies is able to deliver much greater data rates and is able to take advantage of underutilized portions of the electromagnetic spectrum. The system consumes an average current of 4. Wireless Technologies responds to the explosive growth of standard cellular radios and radically different wireless applications by presenting new architectural and circuit solutions engineers can use to solve modern design problems.

Integrated Multi

These two bands provide the largest bandwidths available for communication technologies and present many attractive opportunities for medical applications. The entire transceiver has been implemented in an area as small as 4.

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Chapter 2 gives a detailed study of the level of integration in current transceiver realization and analyzes their limitations.

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The book presents not only the state of the art technologies and solutions to tackle the critical challenges faced by the building and development of the pervasive health system but also potential impact on society at social, medical and technological level.

Power Cmos Circuit Design And Reliability Analysis For Wireless by Md Anwar Sadat

Multi-Standard CMOS Wireless Receivers: Analysis and Design is the first book on the subject of multi-standard wireless receivers.

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