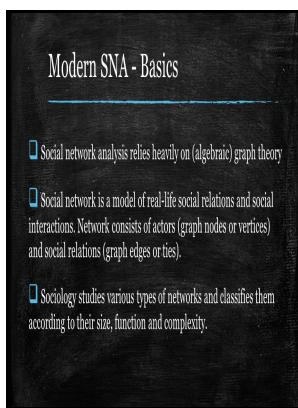


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The other two models simply had deeper flaws when compared against the evaluation criteria.

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Similar to system idle states, devices have low-power states that range from D0 to D3.

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The third port of DC1 couples off the power reflected from P1 via A1 and PC1, then feeding it to test receiver 1 RX TEST1. Low-power states for devices the SoC itself are defined by the SoC manufacturer. As hardware becomes virtualized and programmable through software, IT teams need the skills to enable integration — and the hardware needs to be able to integrate.

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