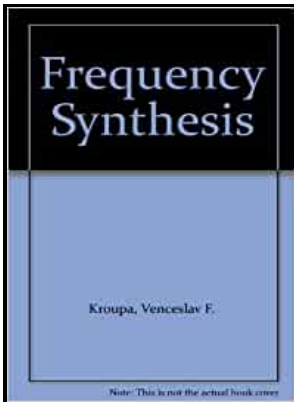


Frequency synthesis: theory, design & applications

Griffin - Frequency synthesis



Description: -

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 Notes: Includes bibliographical references and index.
 This edition was published in 1973



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What is an RF Frequency Synthesizer » Electronics Notes

Rouphael, in , 2009 This chapter presents an overview of the signal processing operation of $\Delta\Sigma$ converters and compares the performance of continuous-time versus discrete-time converters. If a sine look-up table is used, the phase accumulator computes a phase angle address for the look-up table, which outputs the digital value of amplitude—corresponding to the sine of that phase angle—to the DAC. The signals were generated in a programmable gate array FPGA and then directly synthesized by the RF DAC.

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DDS devices like the AD9833 are programmed through a high speed serial peripheral-interface SPI , and need only an external clock to generate simple sine waves.

Frequency synthesizers

He is the author of the data sheets, application notes, and technical articles for several generations of high speed DAC products. With an intermodulation products chart among its figures, a computer program that calculates the frequencies of mixer intermodulation products among its appendices, and a bibliography of more than 190 references, Frequency Synthesizers: Theory and Design continues to be an invaluable aid for engineers, managers, instructors, and students. In addition, the shutdown feature can be used to interrupt drive to the mixer.

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Field-programmable gate arrays are available in a variety of IC package types and with different numbers of pins and flip-flops. Whether providing agile sources of low-phase-noise variable-frequencies with good spurious performance for communications, or simply generating a frequency stimulus in industrial or biomedical test equipment applications, convenience, compactness, and low cost are important design considerations. Unfortunately, the standard DDS method is inherently non-phase coherent.

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