

# Fabrication and characterisation of thin film copper indium (gallium) diselenide.

Universityof Salford - Numerical Simulation of CGS/CIGS Single and Tandem Thin

Different Layers of CIGS Solar Cell			
Substrate	Mo (1-1.5 µm)	CIGS (1-2 µm)	CdS (50nm)
Glass, Metal foil, Polymers, CdS Requires substrate temperature higher than 350°	High electron affinity (~4.5 eV) permitting an ohmic contact with the CIGS. Supported by the formation of a thin $\text{MoS}_2$ interfacial layer.	$\text{CuInGaSe}_2$ is a quaternary I-II-VI semiconductor. Its band-gap varies from approximately 1.07 eV to 1.63 eV as a function of Ga content. Intrinsic p-type conduction.	N-type semiconductor forming an heterojunction with the p-type CdS. Bandgap of 2.4 eV.
No diffusion from the glass to the CIGS layer at high temperature and has a beneficial impact on the CIGS.	Good adhesion with CIGS.	Low surface recombination velocity at interface below $10^9$ cm/s was measured. Cu-rich composition prevents recombination due to In/Ga defects.	Protecting the absorber layer during sputtering of window layer. Act as buffer layer preventing interface recombination.
Flexible substrate, polyimide substrate, metallic substrate or ultra-thin glass [3-11]	Only Tungsten (W) and Tantalum (Ta) have been proven to give a similar efficiency than Mo back-contact.	Use of toxic Cd-compounds limits the development.	

Description: -

-fabrication and characterisation of thin film copper indium (gallium) diselenide.

-DX187187fabrication and characterisation of thin film copper indium (gallium) diselenide.

Notes: PhD thesis, electronic and Electrical Engineering.

This edition was published in 1995



Filesize: 33.48 MB

Tags: #Global #Thin

## CIGS solar cell

Still, all three play crucial background roles in the industry. This study used photoluminescence PL spectroscopy, photoluminescence excitation PLE spectroscopy, and cathodoluminescence CL to study radiative emissions from a variety of Cu-poor CIGS thin films. First zinc oxide layer 24 is deposited with RF sputtering at approximately 0.

## Fabrication of copper

Place, publisher, year, edition, pages2009.

## Comprehensive characterization of CIGS absorber layers grown by one

NREL built up another procedure that includes three deposition steps and created the efficiency of the current CIGS at around 20. The process of claim 5 wherein the electrodeposition proceeds at a superimposed AC voltage of approximately 0. Optimal results are obtained with a thickness of about 2  $\mu\text{m}$  and a temperature of 318 K.

## Thin Film Archives

Since their low profile can minimize air resistance and they are not significantly heavy-weight. Cu deficiency increases the hole concentration by increasing the number of electron-accepting Cu vacancies.

## CuIn1 Xgaxse2 Based Thin Film Solar Cells Volume 35 Thin Films And Nanostructures PDF Book

It is also believed that the AC voltage improves nucleation growth of the thin film by allowing additional nucleation centers to be created. After this tariff imposition, 8 GW of imported PV modules did not tariff in 2019 while the majority of these modules 4.

## One

Figure 1: Structure of a CIGS device. Global Thin-film PV module Market Analysis, By Geography 7.

## Related Books

- [Creative photography](#)
- [Nomenclator literarius recentioris theologiae catholicae theologos exhibens qui inde a Concilio trid](#)
- [Konstante i previdi](#)
- [Short walk around the Pyramids & through the world of art](#)
- [2 families - Gilbert McDonald, Edward Leo Dobbins](#)