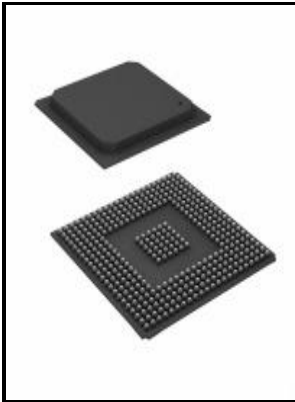


# TMS320C1X

Texas Instruments - Texas Instruments TMS320



Description: -  
-TMS320C1X  
-  
Social history in perspective  
Digital signal processing products TMS320C1X  
Notes: SPRU013C.  
This edition was published in 1991



Filesize: 63.109 MB

Tags: #5 #Assembler #Directives

## Compiler

Also the line counter is incremented, even when source statements are not listed.

## TMS320C1X datasheet

With the C28x assembler, objects that are 16 bits or larger start on a word boundary and are placed with the least significant bits at the lower address. It does not, however, set the error or warning counts, and it does not prevent the assembler from producing an object file. To improve readability, labels and comments are not shown as part of the directive syntax here.

## Compiler

Its microprocessor operates at 5 MIPS. A macro library is a collection of files that contain macro definitions.

## Source code to an IDA processor module: TMS320C10/TMS320C1X

The assembler truncates any values that are greater than eight bits. The 32-bit value is stored exponent byte first, least significant word of fraction second, and most significant word of fraction third, in the format shown in. The latest BiCMOS process technology is used, resultantly a supply current is limited as low 4.

## Source code to an IDA processor module: TMS320C10/TMS320C1X

This parameter can only be used with a top-level union. Local labels, by nature, are temporary; the.

## Source code to an IDA processor module: TMS320C10/TMS320C1X

Each value is placed in a 16-bit word by itself and is aligned on a word boundary. These directives are to be used only by the compiler; do not attempt to use these directives.

## **TMS320C1X datasheet**

The structure tag stag of a.

## **TMS320C1X datasheet**

If you do not use the. This allows you to initialize memory with pointers to variables or labels.

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