

Uccayute nilal.

- - [PATCH v3 11/14] scsi: lpfcc: Use irq_set_affinity

Description: -

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Notes: Romanized.

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Tags: #PATCH #v3 #00/14] #genirq: #Cleanup #the #usage #of #irq set affinity hint

Re: [PATCH v3 06/14] RDMA/irdma: Use irq_set_affinity_and_hint

Testing ----- In terms of testing, I have performed some basic testing on x86 to verify things such as the interrupts are evenly spread on all CPUs, hint mask is correctly set etc.

Hence, there is no need for the drivers to enforce their own affinity for the spreading of interrupts. For this, we will introduce extension to devlink to control affinity that will be used prior initialization sequence.

Re: [PATCH v2 00/14] genirq: Cleanup the usage of irq_set_affinity_hint

If I understand correctly, the mlx5 sub-function use case uses iRDMA? We would like to create devices with correct affinity from the beginning. I got a little confused from our last conversation about mlx5.

Question 2 (a) Design a causal FIR lowpass filter,

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[PATCH v3 11/14] scsi: lpfc: Use irq_set_affinity

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