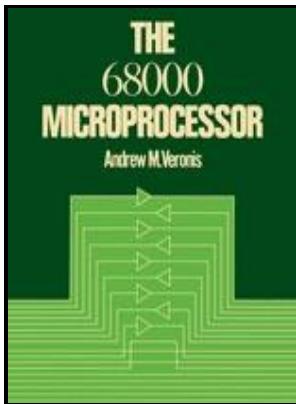


Mc68000 8- 16- 32-Bit Microprocessor Users Manual

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Motorola 68000, FLT

. SSP ; SSP—2 SSP; SR SSP ; PC MOTOROLA.

68000 MICROPROCESSOR

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For the initial reset, RESET and HALT must be asserted for at least 100 ms. . This additional information describes the internal state of the processor at the time of the bus error and is reloaded by the RTE instruction to continue the instruction that caused the error.

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