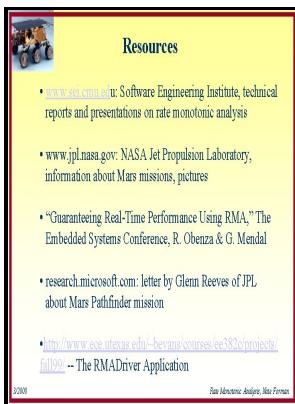


Performance analysis of real-time embedded software

Kluwer Academic - [PDF] Performance Analysis Of Real



Description: -

- Real-time data processing

Embedded computer systems -- EvaluationPerformance analysis of real-time embedded software

-Performance analysis of real-time embedded software

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Chapter 10: Performance Analysis based on the UML SPT Profile

For example, in the case of the Client-Server pattern, it is expected that the instance playing the role of client will send a request to the server, which will process it and send a reply back. Tradeoffs are well understood and rationalized. Park and Shaw developed a performance model of a 68000 that gave them good results.

Performance Analysis of Real

The following steps were performed: a designed the system with UML; b implemented the system by using reusable frameworks; c measured the system in a networked environment; d used measured resource demands to annotate the UML model; e generated the LQN model; f solved the LQN model, g compared the LQN results with overall performance measurements obtained from the real system, and h used the LQN model to gain some insight into the performance of the DES system. S EVCIK, Quantitative System Performance: Computer System Analysis Using Queueing Network Models, Prentice-Hall, Inc. Each software task is running on a processor shown as a circle.

Software performance engineering for embedded systems

Many scheduling algorithms and analysis methods guarantee schedulability based on knowledge of execution time. Each I-block has two execution times, one for a cache hit and one for a cache miss.

Performance Analysis of Real

The memory system is an even larger source of variation. Testing is the main method of proving correctness but has been shown to be a long and time consuming process.

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The performance results obtained by solving the model e. The advantage of CSM is evident when we consider adding a new performance formalism, as it is much simpler to translate from CSM to a performance model, than directly from UML. Although it had a large instruction set, it had only a simple pipeline and no cache.

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