


TMS320C1X

Texas Instruments - TMS320C10NL Texas Instruments

Fixed vs. Floating Point DSP's

- Fixed Point DSP's (Modems, Controllers, Phones...)
 - Cheaper and consume less power
 - Need special care when programming to avoid overflows
 - More dominant in the market
 - TMS320C1x, 'C2x, 'C20x, 'C24x (16 bit, for control)
 - 'C5x, 'C54x, 'C55x (16 bit, wireless phones and modems)
 - 'C62x, 'C64x (16 bit, DSL, imaging, video)
- Floating Point DSP's (DSL, Video, Imaging...)
 - Easier to program,
 - Complex architecture (more computationally demanding)
 - More expensive
 - 'C3x, 'C4x, 'C67x (32-bit, DSL, imaging, video)



Description: -
-TMS320C1X
-
Social history in perspective
Digital signal processing productsTMS320C1X
Notes: SPRU013C.
This edition was published in 1991



Filesize: 51.107 MB

Tags: #Source #code #to #an #IDA #processor #module: #TMS320C10/TMS320C1X

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The structure or union tag of a. The assembler includes the symbol in the symbol table.

5 Assembler Directives

If a global symbol is defined more than once, the linker issues a multiple-definition error. See for more information on macros and macro libraries.

Source code to an IDA processor module: TMS320C10/TMS320C1X

This redefines any existing opcodes or macros with the same name. They do not allocate memory.

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There are variants without ARMs, and without DSPs.

Source code to an IDA processor module: TMS320C10/TMS320C1X

NOLIST Do not include the converted assembly code in any listing file generated for the containing assembly file default. This means that the allocated space does not cross a page boundary 64 words unless its size is greater than a page, in which case the allocated space starts on a page boundary.

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. W limits the listing of

TMS320C1X datasheet

This example shows how the. It is designed to accomodate either a low impedance dynamic microphone or an electret microphone that can be

supplied from the circuits own DC - supply.

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