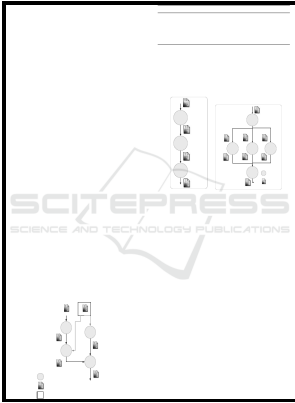


Parallel scheduling of concurrent VLSI simulation modules onto a multiprocessor

- - Parallel Simulation



Description: -

-Parallel scheduling of concurrent VLSI simulation modules onto a multiprocessor

-Parallel scheduling of concurrent VLSI simulation modules onto a multiprocessor

Notes: Thesis (Ph.D.) - Loughborough University of Technology, 1991.

This edition was published in 1991



Filesize: 43.46 MB

Tags: #Parallel #Sequencing #and #Assembly #Line #Problems

Parallel Programming Environments

Scheduling events in hardware design language simulation 2004-09-20 2019-12-10 The Mathworks, Inc.

Sushil Prasad

Since each co-simulator maintains the states of its sub-tasks independently, the disclosed schemes provide true offloading of the simulator and are therefore highly scalable. Conversely if the turnaround time for jobs is longer, intelligent users may think more carefully about the jobs they submit rather than randomly submitting jobs and hoping that one of them works out.

US9672065B2

On multiprocessor machines, each processor has its own scheduler with its own runqueue.

US6466898B1

Executing the at least one sub-task may include synchronizing two or more sub-tasks, which are executed by two or more respective co-simulators, using the at least one sub-task executed by the simulator.

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