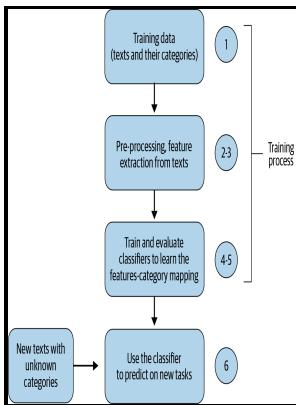


Automatic parallelization - new approaches to code generation, data distribution, and performance prediction

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Notes: Includes bibliographical references and index.
This edition was published in 1994



Filesize: 10.37 MB

Tags: #An #Automatic #Instruction

Automatic Parallelization

. International Conference on Computational Science 2006 ICCS 2006 , Reading, UK, Lecture Notes in Computer Science, Springer-Verlag, 2006. .

Automatically estimating network contention of parallel programs

Most applications perform expensive stack walking to recover contexts. Pluto transforms C programs from source to source for coarse-grained parallelism and data locality simultaneously.

Automatically estimating network contention of parallel programs

A Compile-Time Data Locality Optimization Framework for NUCA Chip Multiprocessors. Parallel Prophet models many realistic features of parallel programs: unbalanced workload, multiple critical sections, nested and recursive parallelism, and specific thread scheduling paradigms, which are hard to model in previous approaches. The compiler identifies these language constructs and analyzes the marked code for parallelization.

Autotuning for Automatic Parallelization on Heterogeneous Systems

Workshop on High Performance Computation on Very Large Data Sets, part of HPCN Europe 99, pp. We propose a technique to encode the current calling context of any point during an execution.

CiteSeerX — Citation Query Automatic parallelization in the polytope model

More specifically, we have developed a tool that automatically. With Parallel Prophet, programmers simply insert annotations that describe the parallel behavior of the serial program

[PDF] Predicting the Performance of Automatically Scalable Computation (ASC)

. Connectionist Models Summer School, pp. Some of them even make the optimal choices within their framework.

CiteSeerX — Automatic Parallelization

If such optimization is not possible for all references, then temporal localization of reused data sequences is made at some outer loops level. In contrast to dynamic scripting languages, our technique is used for automatic parallelization of machine code. Similarly, we target SMP by a static automatic parallelization method, but on the machine level using more fine-grained ILP.

Effective automatic computation placement and dataallocation for parallelization of regular programs

The increase is noticeable until 8 cores, where it is around 4.

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