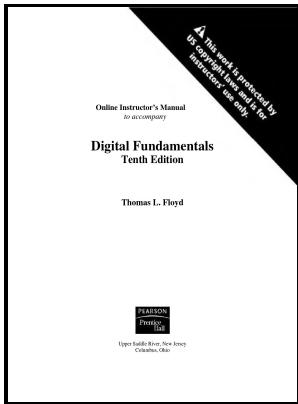


Digital fundamentals - experiments and concepts with CPLDs

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Digital Fundamentals

Provides Digital System Applications in most chapters, illustrating how basic logic functions can be applied in real-world situations; many use VHDL to implement a system. This book offers an easy-to-read, easy-to-follow approach to digital fundamentals through the use of Complex Programmable Logic Devices CPLDs. Coverage begins with the basic logic gates used to perform arithmetic operations, and proceeds up through sequential logic and memory circuits used to interface to modern PCs.

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implementations are also included, which allow the readers to gain a solid understanding of a circuit's real potential and limitations, and to develop a realistic perspective on the practical design of actual integrated circuits.

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