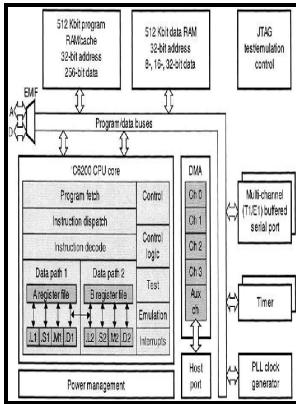


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Page, Walter Hines, -- 1855-1918.

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Notes: Includes bibliographical references and indexes.

This edition was published in 1999



Filesize: 52.39 MB

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CAD4X Laboratory

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Evaluation of CORDIC Algorithms for FPGA Design

From January 1998 until May 1999 Andreas visited the Department of Electrical Engineering and Computer Science at U.

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