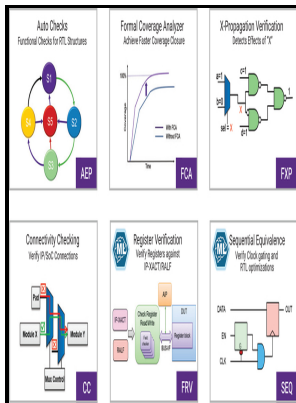# Power optimization and synthesis at behavioral and system levels using formal methods

Kluwer - Power Optimization and Synthesis at Behavioral and System Levels Using Formal Methods (Hardcover)

Description: -
-
Integrated circuits -- Very large scale integration -- Computer-aided design.
Low voltage integrated circuits -- Computer-aided design.Power optimization and synthesis at behavioral and system levels using formal methods
-Power optimization and synthesis at behavioral and system levels using formal methods
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Tags: #A #formal #approach #for #high #level #synthesis #of #linear #analog #systems

Power Optimization and Synthesis at Behavioral and System Levels Using Formal Methods

Unfortunately, determining the input patterns e. For example, if a computation requires five add operators that are independent of each other, it could be done in one cycle if there were five adders available, or 5 cycles if there was just one adder available.

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Formal Methods in High

Some of them are, for example, 1 the extraction of common sub-expressions; 2 the conversion of multi-dimension arrays to a single-level array; 3 constant propagation; 4 the use of a shifter in place of constant multipliers; 5 loop unrolling, loop folding, pipelining, function inline expansion, dead code elimination and tree balancing. Tuttle and Lenore Zuck We show how to verify the correctness of transactional memory implementations with a model checker.

Power Optimization and Synthesis at Behavioral and System Levels Using Formal Methods

Many researchers and CAD tool developers are working on high-level power modeling and estimation, as well as power-constrained high-level synthesis and optimization. Consequently, we also study partial trigger querying, which returns a non empty subset of the solution, and is more feasible. PPIs: Pseudo-Primary Inputs, which are defined as the outputs of memory elements e.

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