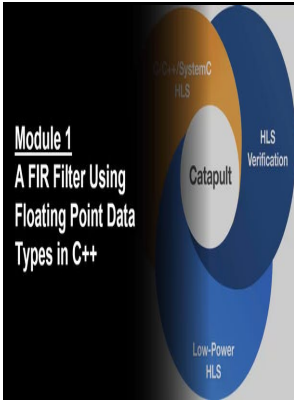# Power optimization and synthesis at behavioral and system levels using formal methods

Kluwer - Power Optimization and Synthesis at Behavioral and System Levels Using Formal Methods

Description: -
-
Integrated circuits -- Very large scale integration -- Computer-aided design.
Low voltage integrated circuits -- Computer-aided design.Power optimization and synthesis at behavioral and system levels using formal methods
-Power optimization and synthesis at behavioral and system levels using formal methods
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Formal Methods in High

Fault models and fault simulation Strategies for digital circuits and analog circuits. In strategy D, we forced HLS tool to use 3 control steps in addition to the reset state while in strategy E we let HLS tool minimize number of control steps. In typical implementations, genetic algorithms search for this optimal solution until a specified termination criterion is met.

Power Optimization and Synthesis at Behavioral and System Levels Using Formal Methods (Hardcover)

To obtain accurate estimates for large high-density integrated circuit designs, a highly efficient circuit simulator should be utilized to drive the evolution process.

The xPilot System

This can be a task without further information of the circuit e. Power-Optimal Module Allocation and Binding -- 3.

The xPilot System

They are not intended to be exhaustive or to limit the invention to the precise forms disclosed, and obviously many modifications and variations are possible in light of the above teaching. Here v 1 and v 2 denote the input vectors applied in clock cycle I and II, respectively. That is, there would be a long path that violated timing at the desired clock frequency.

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