

Uccayutē nilal.

- - Uccayutē nilal : kathakal / Vatsala. = ■■■■■■■■■■ ■■■■ ெ .

Description: -
-Uccayutē nilal.
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Notes: Romanized.
This edition was published in 1976



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Tags: #Re: #[PATCH #v2 #00/14] #genirq: #Cleanup #the #usage #of #irq_set_affinity_hint

Re: [PATCH v3 06/14] RDMA/irdma: Use irq_set_affinity_and_hint

I got a little confused from our last conversation about mlx5.

[PATCH v3 11/14] scsi: lpfc: Use irq_set_affinity

Re: [PATCH v3 06/14] RDMA/irdma: Use irq_set_affinity_and_hint

Hence, there is no need for the drivers to enforce their own affinity for the spreading of interrupts. The issue that this commit was trying to solve is to allow the drivers to enforce their affinity mask to distribute the interrupts across the CPUs such that they don't always end up on CPU0. IIUC mlx5 sub-function use case uses irdma? Not sure about that, but I think that only mlx5 implements SIOV model.

[PATCH v3 11/14] scsi: lpfc: Use irq_set_affinity

Hence, I will wait for this week if I don't get anything I will just post another version by making the change in this driver. Currently, netdev users who don't want irqbalance are digging into their procsfs, reconfigure affinity on already existing devices and hope for the best. Hence, there is no need for the drivers to enforce their own affinity for the spreading of interrupts.

Re: [PATCH v3 06/14] RDMA/irdma: Use irq_set_affinity_and_hint

Testing ----- In terms of testing, I have performed some basic testing on x86 to verify things such as the interrupts are evenly spread on all CPUs, hint mask is correctly set etc.

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