

Uccayute nilal.

Description: -

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Notes: Romanized.

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Tags: #Re: #PATCH #v2 #00/14] #genirq: #Cleanup #the #usage #of #irq set affinity hint

Re: [PATCH v3 06/14] RDMA/irdma: Use irq_set_affinity and hint

I got a little confused from our last conversation about mlx5.

[PATCH v3 11/14] scsi: lpfc: Use irq_set_affinity

Re: [PATCH v3 06/14] RDMA/irdma: Use irq_set_affinity and hint

Hence, there is no need for the drivers to enforce their own affinity for the spreading of interrupts. The issue that this commit was trying to solve is to allow the drivers to enforce their affinity mask to distribute the interrupts across the CPUs such that they don't always end up on CPU0. IIUC mlx5 sub-function use case uses rdma? Not sure about that, but I think that only mlx5 implements SIOV model.

[PATCH v3 11/14] scsi: lpfc: Use irq_set_affinity

Hence, I will wait for this week if I don't get anything I will just post another version by making the change in this driver. Currently, netdev users who don't want irqbalance are digging into their procfs, reconfigure affinity on already existing devices and hope for the best. Hence, there is no need for the drivers to enforce their own affinity for the spreading of interrupts.

Re: [PATCH v3 06/14] RDMA/irdma: Use irq_set_affinity and hint

Testing ----- In terms of testing, I have performed some basic testing on x86 to verify things such as the interrupts are evenly spread on all CPUs, hint mask is correctly set etc.

Hence, there is no need for the drivers to enforce their own affinity for the spreading of interrupts.

Re: [PATCH v2 00/14] genirq: Cleanup the usage of irq_set_affinity_hint

In this patch-set, I have done that for a few drivers and will hopefully try to move the remaining of them in the coming days. For this, we will introduce extension to devlink to control affinity that will be used prior initialization sequence.

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The same goes for other sub-functions of that PCI device, like RDMA, vdpa e. In any case, I will make the change and re-post.

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