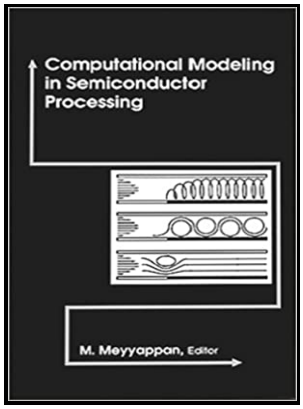
Computational modeling in semiconductor processing

Artech House - Open Source Software for Materials and Process Modeling



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The Artech House materials science libraryComputational modeling in semiconductor processing
Notes: Includes bibliographical references and index.
This edition was published in 1995

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Computational modeling in semiconductor manufacturing. Edited by M. Meyyappan, Artech House, Norwood, MA, 1995, 363 pp., $89.00 (hardcover), Aiche Journal

The individual components of IBM's CS solution include: Source Mask Optimization IBM has partnered with Mentor Graphics on a new resolution enhancement technique to enable cost-effective printing of two dimensional patterns for the 22nm semiconductor technology generation.

Open Source Software for Materials and Process Modeling

A Software Tool for Rapid Flow and Thermal Design of Electronics Cooling, Semiconductor Processing, and General Flow Systems MacroFlow uses the Flow Network Modeling FNM technique for quick and accurate prediction of the flow and thermal behavior of engineering systems.

Computational Electronics

Working with engineering design automation EDA suppliers, IBM will be providing new design enablement solutions for a seamless transition. A team of mathematicians, biomedical informaticians, and hospital staff will generate publicly shared data and software.

Computational Modeling

Summary Reactor-scale, feature-scale, and fast-reactor-scale modeling are three approaches that can be leveraged for different aspects of semiconductor processing. The approach aims to empower people with severe paralysis and provide an interface to safely learn to control robotic assistants.

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