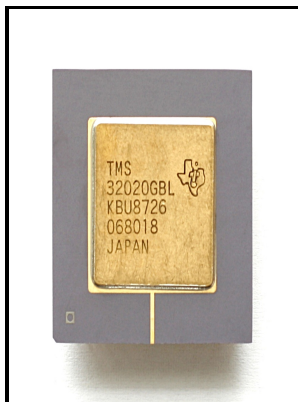


TMS320C1X

Texas Instruments - TMS320C10NL Texas Instruments



Description: -
-TMS320C1X
-
Social history in perspective
Digital signal processing productsTMS320C1X
Notes: SPRU013C.
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The highly paralleled architecture and efficient instruction set provide speed and flexibility to produce a CMOS microprocessor generation capable of executing to 8.

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These devices are offered in 68-pin plastic PLCC or ceramic CER-QUAD packages.

Source code to an IDA processor module: TMS320C10/TMS320C1X

This label is absolute and equates to the present offset from the beginning of the structure.

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Ordinary assembler processing then resumes on the line following the closing %}. Local labels are not included in the symbol table. If you specify a value greater than 0 for this parameter, the assembler allocates size in words contiguously.

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