13. (1 point) For the circuit shown in Figure (7a), the plot V_o when the polarity of dc supply $V_m/2$ is reversed
Ans:

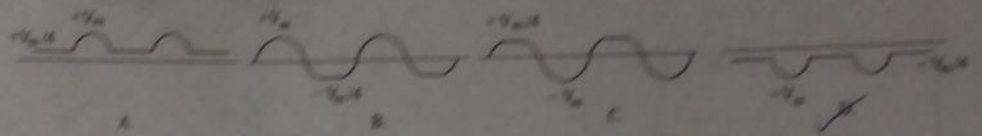


Figure 9: Answer for question 13

14. (1 point) For the circuit shown in Figure (7a), the plot V_o when the polarity of diode D is reversed
Ans:

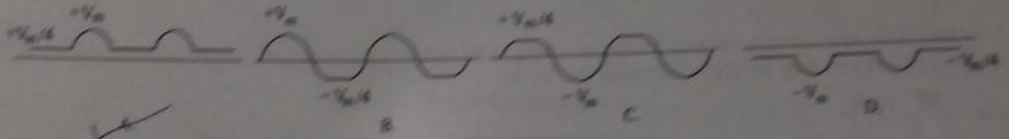
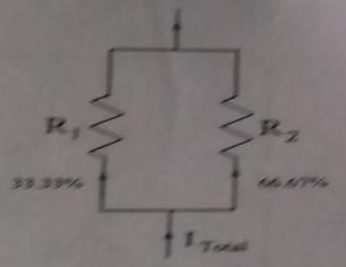


Figure 10: Answer for question 14

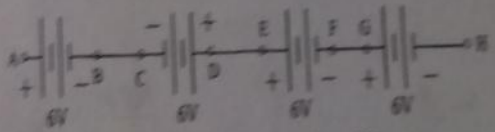
15. (1 point) For the circuit shown in Figure (7b), determine the average value of the current through the load resistor

A. 0 B. 0.79 mA C. 1.59 mA D. 3.18 mA

Figure 11



(a) Question number 16



(b) Question number 17

16. (1 point) For the circuit shown in Figure (11a), the current through the resistors are 33.33% and 66.67% of I_{Total}, approximately. The relation between R_1 and R_2 is

A. $2R_1 = R_2$ B. $3R_1 = 2R_2$ C. $R_1 = 2R_2$ D. $2R_1 = 3R_2$

17. (2 points) For the circuit shown in Figure (11b), what is the potential difference between the following nodes

A. AH = 12V
B. CG = 0V
C. FB = 0V
D. FA = −6V