ECE 4263
Lab3b
Mukul Deshpande

Msd153

I spent around 6-7 hours on this lab since something accidentally got shifted in either datapath or zipper layout and I was getting around 100 DRC errors on the datapath layout which corrupted my whole library. Thus, I had to do all part A and part B again. But it was a good experience, since now I can work on electric even in my sleep.