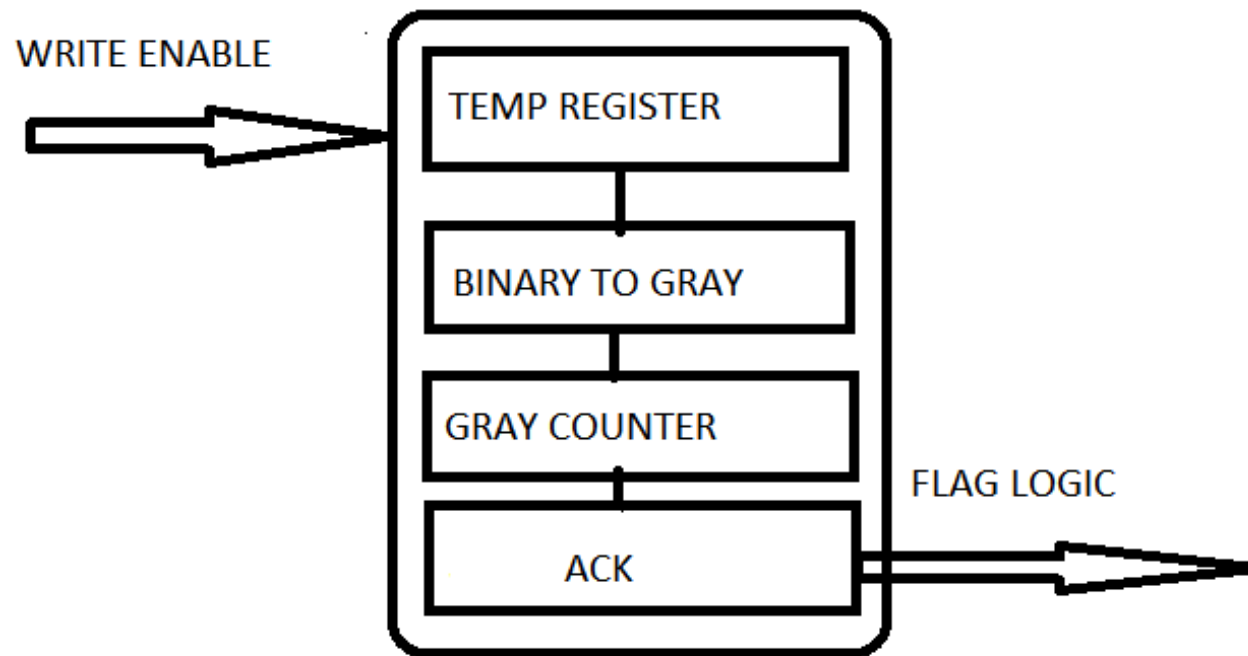
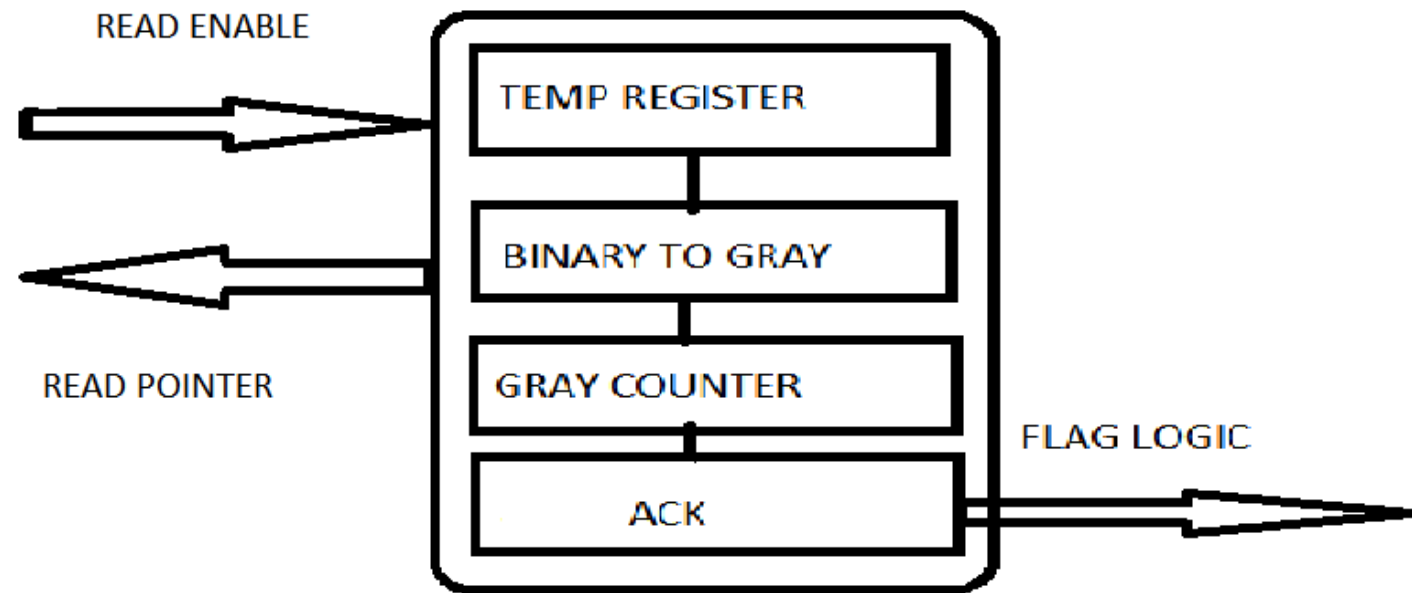


Micro-Architecture

Write Control



Read Control



Flag Logic

