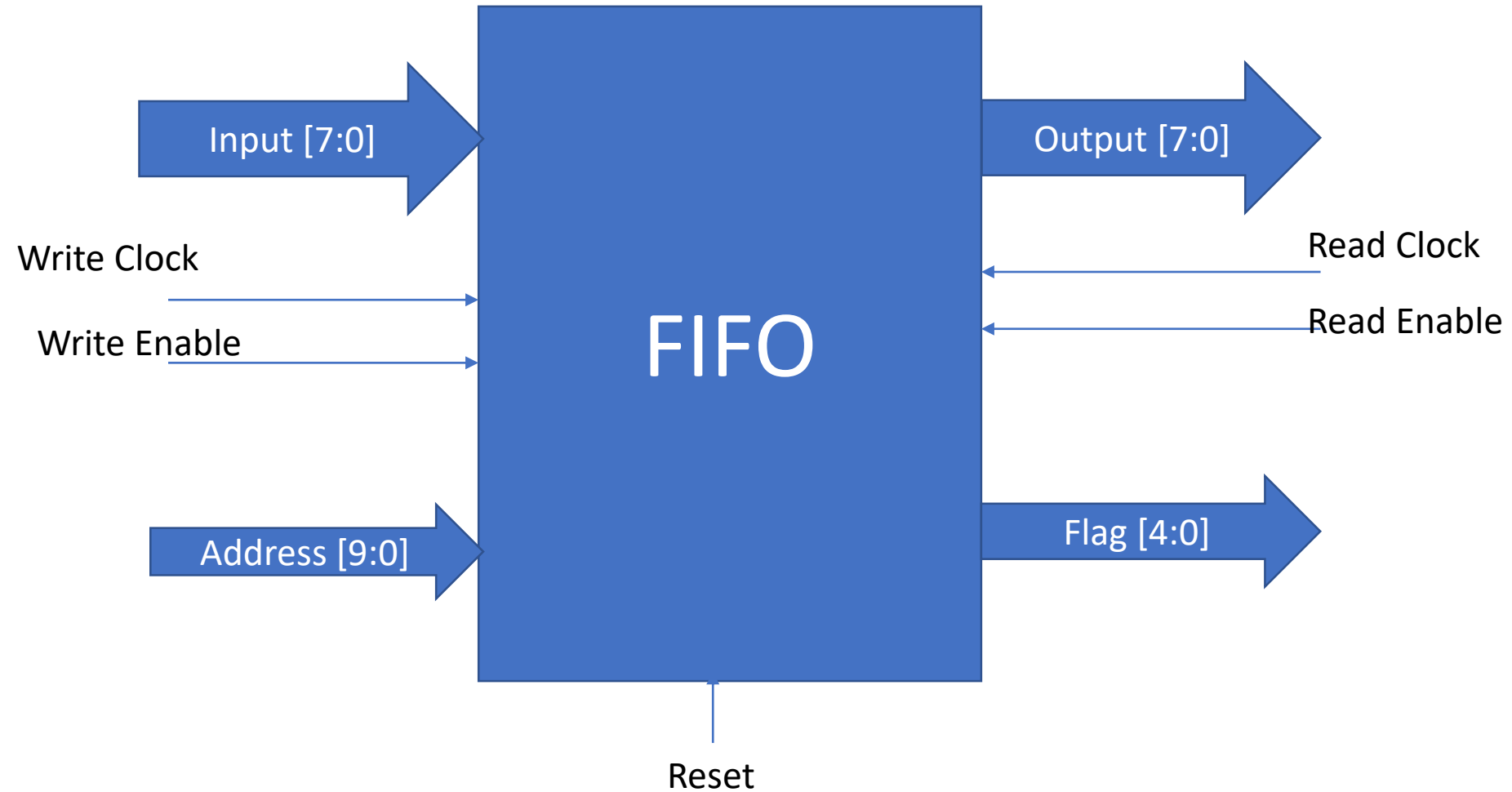


SPECIFICATIONS

- FIFO Size : 1 K bytes
- Input Data Pins: 8
- Output Data Pins: 8
- Control Input Pins: 2 (Write Enable, Read Enable)
- Write Pointer Pins: 10
- Read Pointer Pins: 10
- Flag Pins: 5 (EMPTY, FULL, HALF FULL / EMPTY, ALMOST FULL, ALMOST EMPTY)
- Reset: 1
- Write Clock (100MHz)
- Read Clock (33MHz)

PIN Diagram:



Top Level Diagram:

