J2 USB_C
VBUS
VBUS A4
CC1 A5
CC2 B5
R5 5k1
R6 5k1
GND
D− A7
D− B7
D+ A6
D+ B6
SBU1 A8
SBU2 B8
SHIELD S1
GND A1
R1 1M
GND GND

U1 USBLC6-2SC6
USB_DN
USB_DP
GND

J4 Test Points
VBUS
C2 10u
C3 100n
U4 AP2205-3.3
VIN VOUT
GND
+3V3
C4 10u
C5 100n
GND

U5 RS3236-3.3
VBUS
C6 1u
VIN VOUT
EN
GND
+3.3VADC
C10 1u
C11 10u
C12 100n
GND
Low noise, high PSSR LDO
Used for analog Vdd

J1 TAG CONNECT
+3V3
1
2 SWDIO
3 nRESET
4 SWCLK
5
6 PA15
GND

TODO
C81720 for MCU 48 pin

Voltage Supervisor, Hold MCU reset
for 150ms after power reaches 2.9V.
Prevent brownout at boot.

U2 APX803L20-29SA-7
+3V3
Vcc
RESET
GND
GND
nRESET

+3V3

U3 STM32F072RBTx
NRST 7
BOOT0 60
PA0 14
PA1 15
PA2 16
PA3
PA4
PA5
PA6
PA7
PA8
PA9
PA10
PA11 44
PA12 45
PA13 46
PA14 49
PA15 50
PF0
PF1
PD2 54
PC0 8
PC1 9
PC2 10
PC3
PC4
PC5
PC6 37
PC7 38
PC8
PC9
PC10
PC11
PC12
PC13
PC14
PC15
VBAT VDD VDD VDD VDDA VDDIO2
PB0
PB1
PB2
PB3
PB4
PB5
PB6
PB7
PB8
PB9
PB10
PB11
PB12
PB13
PB14
PB15
VSS VSSA 18 12
GND

USB_DN
USB_DP
SWDIO
SWCLK
PA15

PD_SCK
DOUT

D2 GRN
D1 GRN
R14 510
R13 510
GND
GND

+3V3
C17 100n
C16 100n
C15 100n
C14 100n
C13 100n

SW1
nRESET
C1 100n
GND
+3V3

SW2
BOOT0
R4 10k
GND
+3V3

H1 MountingHole
H2 MountingHole

J3 Loadd Cell
1 Signal+
2 Signal−
3 Excitation−
4 Excitation+
GND
+3.3VADC
R7 100
R8 100
C7 100n

U6 HX717
GND AGND 1
+3.3VADC VRO/VREF 3
+3.3VADC VFB 4
AVDD
DVDD 16
INA+ 8
INA− 7
INB+ 10
INB−
S0 6
S1 13
XI 14
DOUT 12
PD_SCK 11
AGND 5
DGND 15
GND

DOUT
PD_SCK

+3.3VADC
+3V3
GND
C8 100n
C9 100n
GND

+3V3 +3V3
R10 100
R11 100
R12 100
S1 JP2
S0 JP1
GND
GND
GND