Lecture Notes: RLC Circuits

Michael Mulhearn

January 13, 2019

Chapter 1

Direct Current Circuits and Resistance

1.1 The Electric Field

Suppose there are two particles of charge q_1 and q_2 located a distance r_{12} apart. The force on particle 2 is given by:

 $\vec{F}_{12} = k_e \frac{q_1 q_2}{r_{12}^2} \cdot \hat{r}_{12}$

where k_e is Coulomb's constant and \hat{r}_{12} is a unit vector pointing from particle 1 to particle 2. Now consider the force \vec{F} on a particle of charge Q due to N particles, which will be the sum:

$$\vec{F} = Q k_e \sum_{i=1}^{N} \frac{q_i}{r_i^2} \hat{r}_i$$

where r_i is the distance between the particle of charge Q and the ith particle, and \hat{r}_i is the unit vector pointing from the ith particle toward the charge Q. The ratio of the force F to the charge Q is independent of the charge Q, and we call this quantity the electric field:

$$\vec{E} \equiv \frac{\vec{F}}{Q}$$

The importance of this distinction is that the electric field is due solely due to the source charges, independent of the particle of charge Q experiencing the force. The electric field exists whether or not anything is there to experience a force! The force experienced by a particle of charge Q in an electric field \vec{E} is given by:

$$\vec{F} = Q\vec{E}.$$

1.2 The Electric Potential

For a conservative force the change in potential energy that results from moving a particle from x = a to x = b is defined as the negative of the work done by the conservative force:

$$U_{ab} \equiv -W_{\rm CSV} = -\int_a^b F_x \cdot dx$$

As discussed above, the Coulomb force \vec{F} experienced by a particle of charge Q in an electric field \vec{E} is given by:

$$\vec{F} = Q\vec{E}$$

which is a conservative force proportional to the charge Q. If we consider moving a particle of charge Q moving along the x-axis from x = a to x = b, we can define a quantity V_{ba} called the electric potential difference which is simply the charge in potential energy divided by the charge of the particle:

$$V_{ab} \equiv \frac{U_{ab}}{Q} = -\int_{a}^{b} E_x \cdot dx$$

The unit of electrical potential difference is the Volt (V), where 1 V = 1 J/C, and a difference in electrical potential is often informally referred to as simply the voltage. This definition generalizes to three dimensions, but is tedious (you simply add additional identical integrals for y and z) without vector calculus, which we won't use in this course. In this case the points b and a are simply any two points in space.

$$\begin{array}{c|c} + & + & + \\ \hline - & \hline \end{array}$$
(a) (b) (c)

Figure 1.1: The circuit diagram for (a) voltage cell, (b) battery, (c) voltage source. The + and - symbols are usually omitted from (a) and (b). Often, symbol (a) is used to indicate a generic voltage source, even when (c) would be more appropriate.

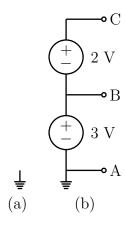


Figure 1.2: The (a) ground symbol, and (b) circuit diagram for two voltage sources connected in series and referenced to ground.

The battery is a frequently encountered electrical component that imposes a constant electrical potential difference between two points (the two terminals of the battery). Batteries are devices that exploit different chemical potentials of two metals. A cell consisting of zinc and copper will have a characteristic voltage of 1.1 V. To build batteries of different voltages, multiple cells can be combined. In lab, you will use a bench-top DC voltage supply, a more complicated piece of equipment that uses electrical power provided by the wall outlets to maintain a fixed voltage between its terminals. All real devices have limitations. For instance, batteries drop in voltage as they become discharged.

Because these devices supply a fixed voltage which is approximately constant with time, they produce circuits with currents in one direction only. As a result, these are called direct current (DC) sources. The electrical symbols for various DC voltage sources are shown in Fig. 1.1

By definition, a conservative force has a single value for the relative potential U_{ba} which is independent of the path taken from point a to point b. The same holds for V_{ba} . This implies that $V_{ab} = -V_{ba}$ which is why terminals of any voltage source have a polarity. If we specify a reference point, we can define the electrical potential as the electrical potential difference of the point of interest and the reference point. Most often, the reference point chosen is the earth ground, which has it's own symbol, shown in Fig. 1.2a. In the circuit diagram of Fig. 1.2b, examples of electrical potential differences are $V_{AB} = 3$ V, $V_{BC} = 2$ V, and $V_{BA} = -3$ V, while examples of electrical potentials are $V_A = 0$, $V_B = 3$ V $V_C = 5$ V.

1.3 Conductors and Resistors

The electric field in a perfect conductor is zero. This implies that any two points connected by a perfect conductor are at the same electric potential, which we refer to as a short circuit. As a practical matter, we treat very good conductors, such as copper, as if they were ideal conductors. We create a short circuit by connecting any two points with a segment of copper wire. Conductors allow charge to flow through them readily. The amount of charge that passes through any electrical component per unit time is called the current I:

$$I = \frac{dQ}{dt}$$

And is measured in Amperes (A) where 1 A = 1 C/s.

Materials which do not readily conduct charge are called electrical insulators, and are used to protect components, such as wires, from accidental contact causing unintentional short circuits. All materials eventually conduct electricity when the voltage is high-enough, and this imposes limits on the voltage that can be reached in practical circuits.

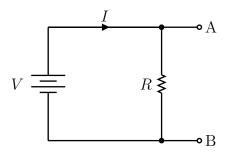


Figure 1.3: A resistor connected to a battery.

If we construct a composite material from a mixture of graphite (a conductor) and ceramic dust (an insulator) held together by a resin, we obtain a resistor: a device which has mobile charges that do not flow as readily as in a conductor. When we connect a resistor to a battery, as shown in Fig. 1.3, the battery imposes a non-zero electric potential across the resistor. This implies that a non-zero electric field is present in the resistor. This electric field exerts a force on the mobile electrons in the resistor, resulting in a net flow of electrons through the resistor. Resistors have a characteristic resistance R which relates the current I that flows through the resistor to the voltage

V across it:

$$V = IR$$

This relationship is referred to as Ohm's Law, and the unit of resistance is the Ohm (Ω) where $1 \Omega = 1 \text{ V/A}$.

1.4 Kirchhoff's Laws

The circuits we have encountered so far are quite simple, containing at most a single closed loop with only one current to consider. More complicated circuits can include multiple circuits. Ultimately the behavior of the circuit is governed by the laws of electrostatics, but Kirchhoff's Laws conveniently cast the important results from electrostatics into a form that can be applied to any circuit.

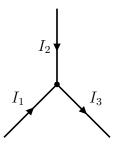


Figure 1.4: An example of Kirchoff's Current Law: $I_1 + I_2 = I_3$

The conservation of charge implies that the total current into a node must equal the total current out of a node. If currents are all defined such that positive current is into the node, this law can be written simply as:

$$\sum_{k} I_k = 0.$$

The electric field is conservative which implies that the net electric potential when traversing any closed loop is zero. If the voltage drop across each electric component is defined in the direction of the closed loop, this law can be written simply as:

$$\sum_{k} V_k = 0.$$

When determining the drop across a resistor from Ohm's law, one must remember the orientation of current and voltage (as in Fig. 1.3) when determining the sign. You should be able to reason that when traversing a resistor in the same direction as the current, you drop in voltage (so add a term -IR when using KVL). When traversing a resistor in the opposite direction as the current, you rise in voltage (so add a term IR when using KVL.)

For example, consider the circuit in Fig. 1.5. From KCL we obtain:

$$I_1 = I_2 + I_3$$

while from KVL we traverse the loop labeled "1" to obtain:

$$0 = V - I_1 R_1 - I_2 R_2$$

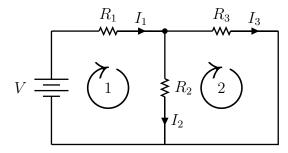


Figure 1.5: An example of Kirchoff's Voltage Law.

and we traverse the loop labeled "2" to obtain

$$0 = I_1 R_1 - I_2 R_2$$

Pay close attention to the signs of the voltage drops across the resistors and make sure you can reproduce them!

1.5 Resistors in Series And Parallel

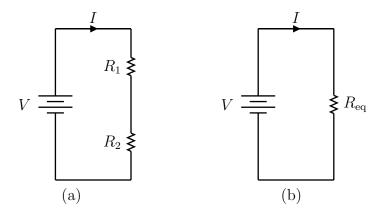


Figure 1.6: Resistor circuits showing (a) two resistors in series and (b) a single equivalent resistance $R_{\text{eq}} = R_1 + R_2$ which produces the same current I.

Consider the circuit in Fig. 1.6 in which a current I flows through to resistors R_1 and R_2 in series. We can simplify this circuit by determining a single equivalent resistance R_{eq} which when installed in the circuit in place of R_1 and R_2 will result in the same current I. Applying KVL to both circuits results in:

$$0 = V - IR_1 - IR_2$$
$$0 = V - IR_{eq}$$

which can be solved to obtain:

$$R_{\rm eq} = R_1 + R_2.$$

It is left as an exercise to show that for any number of resistors wired in series, the equivalent resistance is given by:

$$R_{\rm eq} = \sum_{i} R_i. \tag{1.1}$$

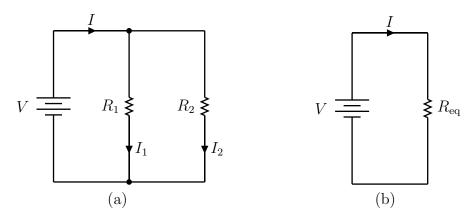


Figure 1.7: Resistor circuits showing (a) two resistors in parallel and (b) a single equivalent resistance R_{eq} which produces the same total current I.

Next consider the circuit in Fig. 1.6 in which a total current I is shared between two resistors R_1 and R_2 . Once again, we can simplify this circuit by determining a single equivalent resistance R_{eq} which when installed in the circuit in place of R_1 and R_2 will result in the same current I. Applying KCL and KVL we determine:

$$I = I_1 + I_2$$

 $0 = V - I_1 R_1$
 $0 = V - I_2 R_2$
 $0 = V - I R_{eq}$

which can be solved to determine:

$$R_{\rm eq} = \frac{R_1 R_2}{R_1 + R_2}$$

It is left as an exercise to show that for any number of resistors wired in series, the equivalent resistance is given by:

$$\frac{1}{R_{\text{eq}}} = \sum_{i} \frac{1}{R_i}.\tag{1.2}$$

The voltage divider circuit of Fig. 1.8a is a frequently encountered circuit. The two resistors have an equivalent resistance $R_1 + R_2$ and so the current is:

$$I = \frac{V}{R_1 + R_2}$$

and the output voltage is the voltage drop across one resistor R_2 ,

$$V_{\text{out}} = IR_2 = \frac{R_2}{R_1 + R_2} V$$

It is left as an exercise to show that the current divider circuit in Fig. 1.8b

$$\frac{I_1}{I_2} = \frac{R_2}{R_1} \tag{1.3}$$

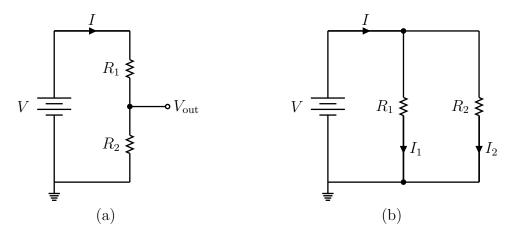


Figure 1.8: Resistors in series and in parallel used to implement a (a) voltage divider and a (b) current divider.

1.6 The Superposition Principle

In electrostatics, the superposition principle states that the electric field created at a point in space by a particular charge is independent of any other charges. This principle allows us to simplify problems in electrostatics by considering the contribution from each charge in turn, constructing a partial solution for each charge while neglecting other charges, and constructing a solution from the sum of each partial solution.

In circuit analysis, the laws of electrostatics govern our circuits as well. When considering a circuit with multiple voltage sources, we can consider one voltage source at a time, short circuiting other voltage sources. The complete solution is simply the sum of each partial solution.

1.7 Sources, Loads, *I-V* Curves, and Power

Analyzing complicated circuits inevitably involves breaking them down into smaller parts that are more easily understood. A particularly useful way to divide circuits is in terms of sources and loads. Consider first the simple circuit consisting of a battery connected to a resistor. As illustrated in Fig. 1.9, both the voltage source and the resistor can be considered as two separate two terminal networks. If we wish to connect these two networks together, we must agree on a definition for the direction of the current I. One of the networks will have positive current I leaving the positive terminal (A), and we call this network the "source". The other network will have positive current entering the positive terminal, and we call this network the "load".

When considered separately, we can imagine connecting various resistors or even additional batteries to the two terminal voltage source. However, as long as we don't do anything that breaks the voltage source, it will keep the voltage between A and B at the fixed value V_1 no matter what we do, providing any amount of current needed. We can also consider connecting batteries of different sizes to the two-terminal load network. In this case, the resistor will allow a current to flow consistent with Ohm's law, in this case: $V = IR_1$. We can consider both the source network and load network as having separate curves in the I-V plane, as shown in Fig. 1.10 . The source curve is the vertical line $V = V_1$ while the load curve is a line of slope $1/R_1$ with zero intercept.

The intersection of the source I-V curve with the load I-V curve is the operating point for the circuit that results form connecting the load to the source. A source providing a current I while

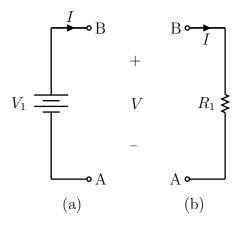


Figure 1.9: Two terminal network diagrams for (a) an ideal voltage as a source, and (b) as a resistor as load.

maintaining a potential difference V across it's two terminals is powering the circuit with power:

$$P \equiv \frac{dU}{dt} = \frac{VdQ}{dt} = V\frac{dQ}{dT} = VI$$

Meanwhile, the load is consuming power P = VI. It is instructive to consider what would happen if you called the battery the load and the resistor the source. In this case, the current simply changes sign, and you would find that at the operating point the so-called supply is providing negative power. In other contexts you might find that both the current I and the voltage V are negative, so that the power provided by the source is positive. In this case, you have likely correctly identified the roles of source and load while making a poor choice for the polarity.

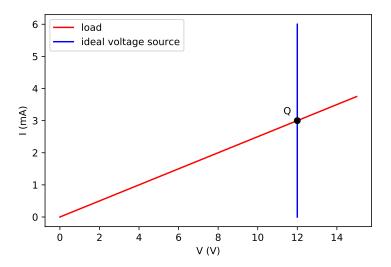


Figure 1.10: The source I - V curve for an ideal 12 V voltage source, and the load I - V curve for a 4 k Ω resistor. The operating point for the circuit when the load is connected to the source is the intersection of these two curves at point Q. The power provided by the source and consumed by the load is the product IV at the operating point.

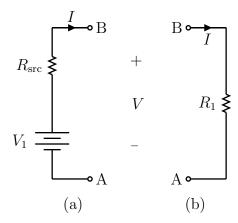


Figure 1.11: Two terminal network diagrams (a) for a voltage source include source resistance R_{src} as the source, and (b) a load resistor.

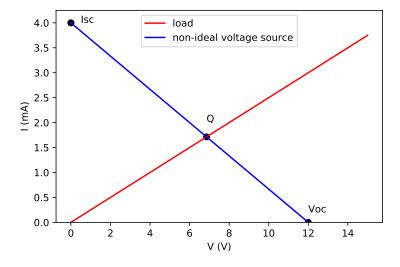


Figure 1.12: I-V curves for a non-ideal 12 V source which includes a source resistance of $R_{\rm src}=3~{\rm k}\Omega$ and a load resistance of 4 k Ω . The short-circuit current and open-circuit voltage, which fully constrain the line, are shown. The intersection of the two curves at point Q is the operating point of the circuit consisting of the source plus the load.

So far, we have only considered ideal voltage sources. All real voltage sources have additional parasitic features. For instance, real batteries provide a voltage but also have an internal resistance. In fact all voltage source you will encounter in ordinary circuits will have some source resistance, which is modeled as an additional resistor in series with an ideal source, as shown in Fig. 1.11a. What is the I-V curve of such an object? If you leave the two terminals isolated from each other (an open circuit) no current will flow, so the voltage drop across the resistor $R_{\rm src}$ is zero, and the voltage is simple $V = V_1$. If you short circuit the terminals A and B, a current $I_{sc} = V_1/R_{\rm src}$ will flow. Ohm's Law ensures a linear relationship between I and V, so that these two points fully define the I-V curve for this device, as shown in Fig. 1.12. It is left as an exercise to show that the slope of the source IV curve in this case is $-1/R_{\rm src}$, in entirely reasonable result, since the slope of a load resistor IV curve, with current in the opposite direction, is 1/R.

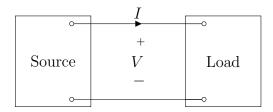


Figure 1.13: A generic source and load.

No matter how complicated the actual circuit is, if one part is connected to the next by only two connections, the source and load analysis can be applied. This is shown conceptually in Fig. 1.13. In fact, even if there are more than two connections, this analysis can be used if the additional connections have a negligible effect on the voltage and current that is under consideration. Very often, the second connection is simply the common ground. A very common arrangement for a complicated circuit is one that receives a signal and processes it in some way, e.g. amplifying it, before passing the output to the next stage, which further processes the signal, e.g. applying a filter, before passing it to the next stage. When analyzing such a circuit, each stage is a load for the previous stage, and source for the next stage.

1.8 Thevenin Equivalent Circuit

There's a tremendously powerful tool that we get, essentially for free, from our understanding of I - V curves for two-terminal networks.

If you set out to build the most complicated two-terminal network that you can imagine which includes resistors, voltages source, and wires, you have at your disposal: Ohm's Law, Kirchoff's Laws, and the Superposition principle. These are all linear equations, and so the source *I-V* curve of your circuit, despite all your efforts, will always remain a straight line, such as that of Fig. 1.14. But we can construct any straight line we wish using only a single voltage source in series with a single resistor. This means that any two-terminal network of resistors and voltage sources can be considered equivalently, with respect to those two-terminals, as a single voltage source in series with a single resistor. An example is shown in Fig. 1.15.

Conceptually, we can imagine making two measurements of any two-terminal network to determine it's Thevenin equivalent. Measuring the open-circuit voltage across terminals A and B reveals the Thevenin equivalent voltage $V_{\rm th}$. Measuring the current $I_{\rm sc}$ that flows when we short-circuit terminals A and B allows us to determine $R_{\rm th}$ from:

$$V_{\rm th} = R_{\rm th} I_{\rm sc}$$
.

In practice, it's almost always easiest to make effective use of the superposition principle. The Thevenin equivalent resistance is simply the equivalent resistance if all voltage sources are replaced with short-circuits. If there are multiple voltage sources, it's usually easiest to consider only one voltage source at a time, setting all other voltage source to short-circuits. The Thevenin equivalent voltage is then the sum of these partial solutions.

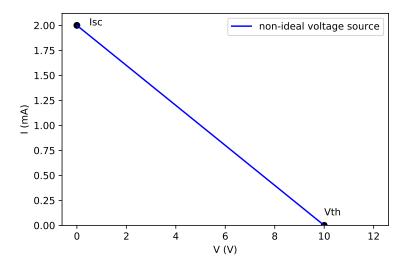


Figure 1.14: The source IV curve for an arbitrary two terminal network.

1.9 Exercises

- 1. Derive Equations 1.1 and 1.2.
- 2. Derive Equations 1.3.
- 3. Show that the power consumed by a resistor of resistance R conducting a current I is:

$$P = I^2 R$$

4. Show that the power consumed by a resistor of resistance R held at a voltage V is:

$$P = V^2/R$$

- 5. Using circuit analysis, determine the operating point (Q) of the circuit in Fig. 1.12 and compare with the value you read off from the plot.
- 6. Draw the IV curve for an ideal voltage source and a short-circuit. Do they intersect? What do you suppose would happen if you attempted to short-circuit an ideal voltage source? Now draw an open circuit (I=0). What is the operating point of a voltage source with it's terminals left open? Complete this sentence: "It's easy for an ideal voltage source to handle (an open/a short) circuit, but impossible for it to handle (an open/a short) circuit.

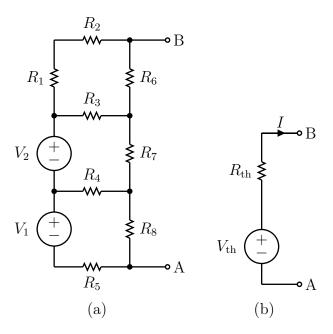
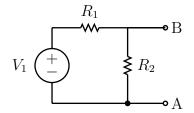


Figure 1.15: Two-terminal network diagrams for (a) a fairly complicated network, and (b) it's Thevenin equivalent.

- 7. We already discussed ideal voltage sources which will provide any necessary current to maintain a constant voltage across two terminals. Another linear circuit element source is an ideal current source, which provides any necessary voltage to maintain a constant current through its two terminals. Sketch the *IV* curve for a constant voltage source and a constant current source. Ideal voltage sources have zero intrinsic resistance. What intrinsic resistance does an ideal current source have? (Hint: look at the IV curve and recall the relationship between slope and resistance.) One way to make a current source is to place a very large resistor in series with a very large voltage.
- 8. Draw the *IVdiagrams* for an ideal current source and an open-circuit (I=0). Do they intersect? What do you suppose would happen if you attempted to leave the terminals of an ideal voltage source open? Now draw a short circuit. What is the operating point of a voltage source with it's terminals left open? Complete this sentence: "It's easy for an ideal voltage source to handle (an open/a short) circuit, but impossible for it to handle (an open/a short) circuit.
- 9. Find the Thevenin Equivalent voltage, resistance, and short-circuit current, for the two-terminal network in Fig. 1.15, assuming all the resistors are 10 k Ω and all the voltages source are 10 V.
- 10. Find the Thevenin Equivalent circuit for the following:



Chapter 2

Alternating Current Circuits and Impedance

2.1 Resistors, Capacitors, and Inductors

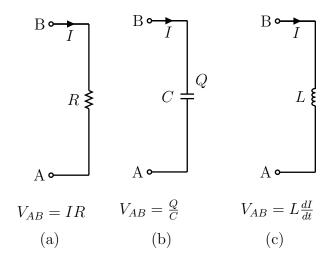


Figure 2.1: The voltage across a (a) resistor, (b) capacitor, and (c) inductor. The sign convention for all of these relationships implies that the component is being treated as a load, with current I entering the positive terminal. The charge Q is the charge which builds up on the upper plate, while the lower plate will have charge -Q.

We've already encountered our first two terminal electrical component, the resistor, which obeys Ohm's law. When considering the sign of Ohm's law, one must remember that the definition implies that the resistor is being considered as a load, as shown in Fig. 2.1a.

A capacitor is a two terminal electrical component which stores energy in an electric field. The electric field is the result of a built of charge, positive on one terminal of the capacitor and negative on the other, which produces the electric charge. When considered as a load, with charge Q on the upper terminal (and charge -Q on the lower terminal) the voltage across a capacitor is:

$$V = \frac{Q}{C}$$

An inductor is a two terminal electrical component which stores energy in a magnetic field when an electrical current passes through it. Because of the property of self-inductance, an inductor tends to resist changes in current. When considered as a load, the voltage across an inductor is:

$$V = L \frac{dI}{dt}$$

While the capacitor and the inductor have the capability to store energy, none of these three components are capable of adding power to a circuit, and are therefore called passive components.

2.2 Time dependence of a Capacitive Circuit

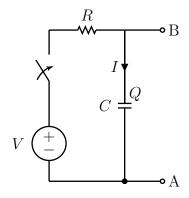


Figure 2.2: Circuits for considering the behavior of a capacitor.

Consider the RC circuit of Fig. 2.2 with the switch closed. From KVL:

$$0 = V - IR - \frac{Q}{C}$$

or matching source voltage to load voltage:

$$V = IR + \frac{Q}{C}$$

The charge as a function of time Q(t) therefore obeys the differential equation:

$$V = \frac{dQ}{dt}R + \frac{Q(t)}{C} \tag{2.1}$$

Our physical intuition leads us to try first for a steady-state solution of the form $Q(t) = Q_F$ for some constant Q_F , so that:

$$\frac{dQ}{dt} = 0$$

and so Equation 2.1 leads to:

$$Q_{\rm F} = CV$$
.

After enough time, the capacitor will always charge to CV, the voltage across it will be V exactly matching the voltage supplied, and no current will flow. This is the steady-state behavior for this circuit.

However, imagine that at time t = 0, the capacitor is initially uncharged with Q = 0. How will the circuit reach the steady-state? Since the differential equation is linear, any solution to the homogenous version (with V = 0):

$$0 = \frac{dQ}{dt}R + \frac{Q(t)}{C} \tag{2.2}$$

can be added to the steady-state solution to provide a solution to the non-homogenous version (with $V \neq 0$). Differential equations which relate the rate of change of a quantity to the quantity itself invariably lead to the exponential function, and we are led to try:

$$Q(t) = A\exp(Bt)$$

for constants A and B. Applying Equation 2.2 leads to:

$$0 = RBA \exp(Bt) + \frac{Q(t)}{C}$$
$$0 = \left(RB + \frac{1}{C}\right)Q(t)$$

which will be true provided that:

$$B = -\frac{1}{RC}$$

That is solutions of the form:

$$Q(t) = A \exp(-t/\tau)$$

with $\tau = RC$ satisfy Equation 2.2 and can be added to the steady state solution to obtain:

$$Q(t) = CV + A \exp(-t/\tau)$$

which is the general solution to Equation 2.1. (You can confirm this for yourself) For the specific case that Q(0) = 0, we conclude that A = -CV and so:

$$Q(t) = CV \left(1 - \exp(-t/\tau)\right)$$

which describes a capacitor charging with a time constant $\tau = RC$ asymptotically approaching the steady-state Q = CV. If instead, we specify that V = 0 and $Q(0) = Q_0$, we obtain:

$$Q(t) = Q_0 \, \exp(-t/\tau)$$

which describes a capacitor discharging with the same time constant, asymptotically approaching the steady-state Q = 0.

2.3 Time dependence of an Inductive Circuit

Consider the RL circuit of Fig. 2.3 with the switch closed. From KVL:

$$0 = V - IR - L\frac{dI}{dt}$$

or matching source voltage to load voltage:

$$V = IR + L\frac{dI}{dt}$$

The current as a function of time I(t) therefore obeys the differential equation:

$$V = RI(t) + L\frac{dI}{dt} \tag{2.3}$$

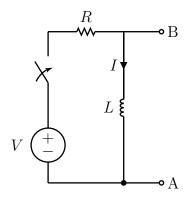


Figure 2.3: Circuit for considering the behavior of an inductor.

It is left as an exercise to show that the steady-state solution is the constant current:

$$I = V/R$$

and the general solution to Equation 2.3 is

$$I(t) = (V/R) + A \exp(-t/\tau)$$

with $\tau = L/R$.

For the specific case that I(0) = 0, we conclude that A = -V/R and so:

$$I(t) = (V/R) (1 - \exp(-t/\tau)).$$

2.4 Exercises

- 1. Derive V=q/C for parallel plate and show $\mathcal{C}=\mathcal{X}.$
- 2. Derive $V=L\frac{dI}{dt}$ for a long solenoid and show L = X.
- 3. Derive equivalent C for two capacitors in series and parallel.
- 4. Derive equivalent L for two inductors in series and parallel.
- 5. Derive the steady-state equation X and the general solution Y for the LR circuit of Fig. X.