Lab 3: Audio Amplifier

January 5, 2022

1 Pre-lab

No pre-lab calculations.

2 Introduction

In this lab, you will build an audio power amplifier capable of driving a 8 Ω speaker the way it was meant to be driven... loudly! You will need this device again next week, so build your circuit on a small protoboard that can be put aside for reuse next week.

We will be using the BD139 (npn) and BD140 (pnp) complementary power transistors for this lab (See Fig. 1 and Fig. 2. Each transistor is mounted on a heatsink. During normal operation, the transistors can become very hot. **Do not touch the transistors or you might burn yourself!** Instead, handle them by the wires and the heatsink without touching the transistor itself.
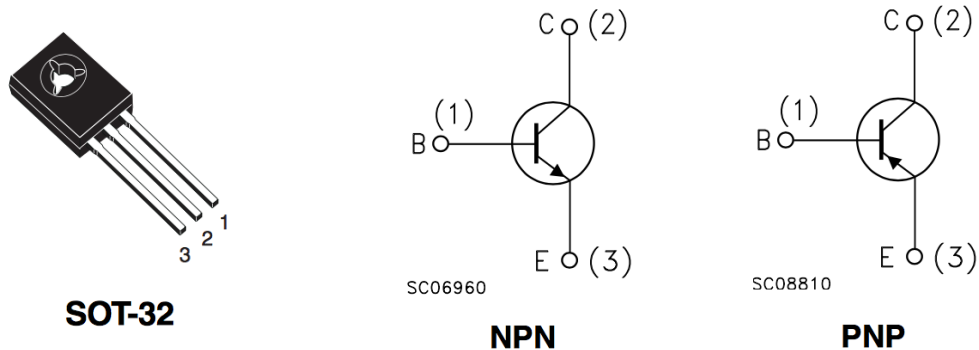

Figure 1: The SOT-32 package (left) and pinout (right) used by the BD139 (npn) and BD140 (pnp) used in this lab. For this lab, the transistor heat sink is mounted below the device as oriented in this photo, with part number on the side facing us.

For low-power circuits, one can (sometimes) get away with making adjustments while leaving the circuit powered. This is not a low-power circuit. **Do not make any adjustments while the circuit is powered.** Power down your DC supply, make your adjustments, and then power it back up.
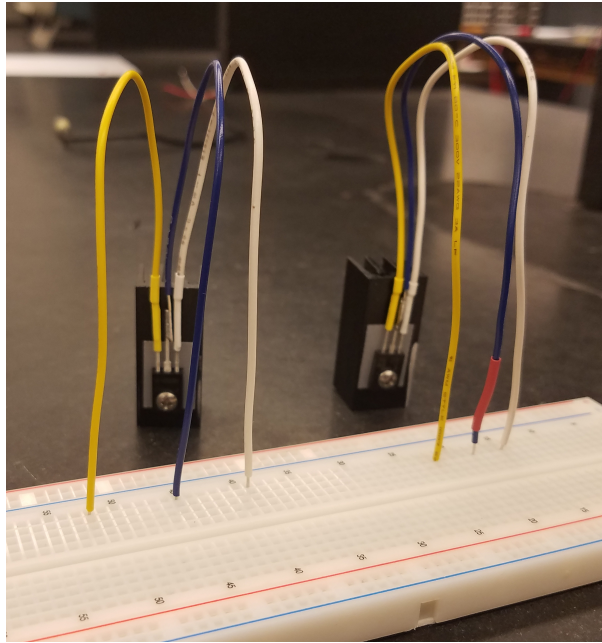
Figure 2: The BD140 (left) and BD139 (right) power transistors as prepared for this lab. Both transistors are mounted on an aluminum heat sink with an electrically insulating thermally conductive pad. The NPN version (BD139), located at the right of the photo, is identifiable by the additional section of red heat shrink tubing on the end of the blue (collector) wire, or by using a magnifying glass to read the part number. For both transistors, as shown in the photo, the pinout from left to right is base (yellow), collector (blue), emitter (white).

3 Power Amplification

Build the emitter follower in Fig. 3 using $R_L = 10$ kΩ, and a BD139 NPN power-transistor. Provide $V_{CC} = 8$ V with your bench-top DC power supply. Use your function generator to drive the input v_{in} with a 900 Hz sinusoidal with peak-to-peak voltage of 3 V. Observe the output with your oscilloscope.

In this configuration, the transistor is only in the active region when the input signal is one diode drop above ground. As a result, you only see a bit less than the top half of the sine wave.

We've already seen a solution to this problem in the transistor lab: we AC couple the input signal and DC bias the transistor into it's active region (See Fig. 4). This is called a Class A amplifier. The Class A amplifier has a major shortcoming that becomes important when dealing with power transistors: the DC bias current wastes a lot of power that is not needed for amplification. In this lab, we'll explore other options for providing amplification.
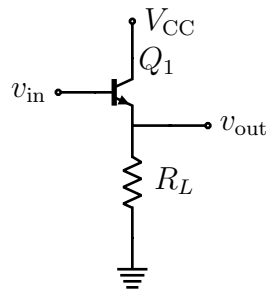

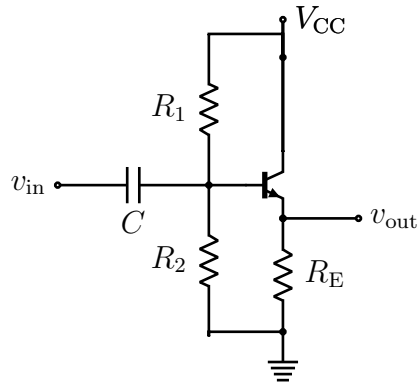Figure 3: An emitter-follower power amplification stage.

Figure 4: The DC biased emitter follower circuit you built for the transistor lab.

4 Push-pull Amplifier

The circuit in Fig. 5 is called a push-pull amplifier. The NPN transistor is in it's active region when the signal is one diode drop above ground, and the PNP transistor is in it's active region when the signal is one diode drop below ground. In between, however, neither transistor is active and distortion, referred to as *cross-over distortion* results. Build the circuit in Fig. 5 using a BD139 (npn)
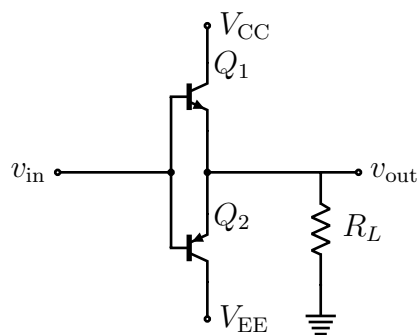
Figure 5: A push-pull power amplification stage.

transistor and a BD140 (pnp) transistor using $R_L = 10$ kΩ. Use your DC power supply to provide $V_{CC} = 8$ V and $V_{CC} = -8$ V. Use your function generator to drive the input v_{in} with a 900 Hz sinusoidal with peak-to-peak voltage of 3 V. Observe the output at v_{out} with your oscilloscope. You should see both halves of the sine wave, but with clear distortion from the cross-over region.

5 Push-pull Amplifier with Follower

Using an LM741 op-amp (pinout in Fig. 6) add a follower stage to your circuit as shown in Fig. 7. Use $R_D = 470$ Ω. This follower will have no effect on the output (yet!) so confirm that it is working by observing the sine wave with cross-over distortion at the output.

At this point, you are ready to hook up your speaker in place of the resistor R_L. You should hear a tone. You can adjust the voltage and frequency on your function generator to change the tone and volume.
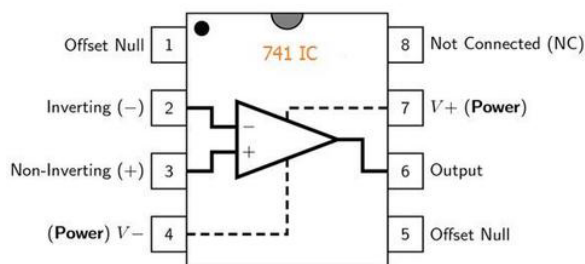

Figure 6: The LM741 8-DIP layout.

6 Feedback

An effective technique for dramatically reducing feedback distortion is to use feedback. With the circuit powered down, move the feedback from just after R_L to v_{out} instead, as shown in Fig. 8. Power your circuit and you should see (with your oscilloscope) and hear a dramatic reduction in the cross-over distortion.
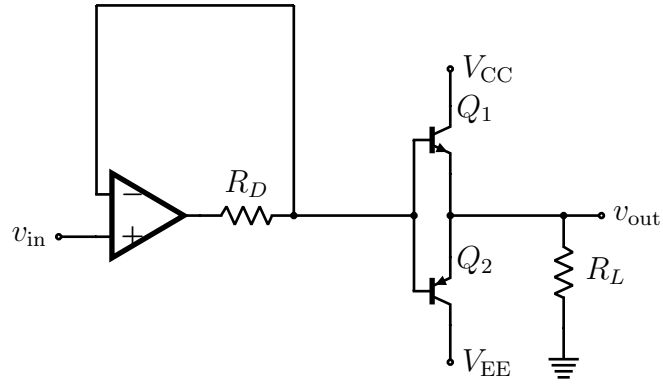
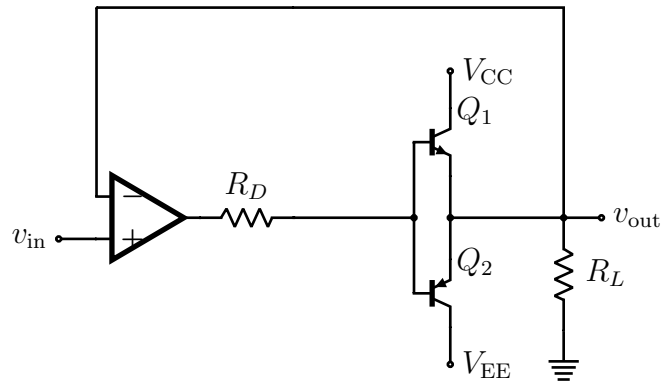Figure 7: Push-pull power amplifier with (ineffective) follower.

Figure 8: Push-pull power amplifier featuring cross-over distortion limiting feedback.

7   Feedback

Now build the circuit in Fig. 9 with $R_1 = 1$ kΩ, $R_2 = 18$ kΩ, and a variable 10k pot for R_V. As this circuit will provide voltage gain, it is now imperative that we remove high-frequency feedback, which is the role of the capacitor $C = 1$ nF. I found that $R_D = 470$ Ω gave the best overall performance.

It will help to build the circuit to the right of R_V first (omit the follower) and test that it works with the function generator to provide a voltage gain of 18. Then connect the variable resister and test that you can vary the gain. Then build and test the follower as a separate circuit, and finally connect the output of the follower to the rest of the circuit.

Once you build and test this circuit, and if you trust your work, you should be able to play music from your phone by attaching a phone jack between ground and v_{in}... but ask your TA for help! If you find you need more gain, you can increase R_2 to 50 kΩ.

Remembering to power down the circuit when reconfiguring, try playing music with the negative feedback taken after the push-pull stage (as in Fig. 9) and with the negative feedback taken before the push-pull stage (as in Fig. 10. You should hear a clear difference. That is the power of feedback!
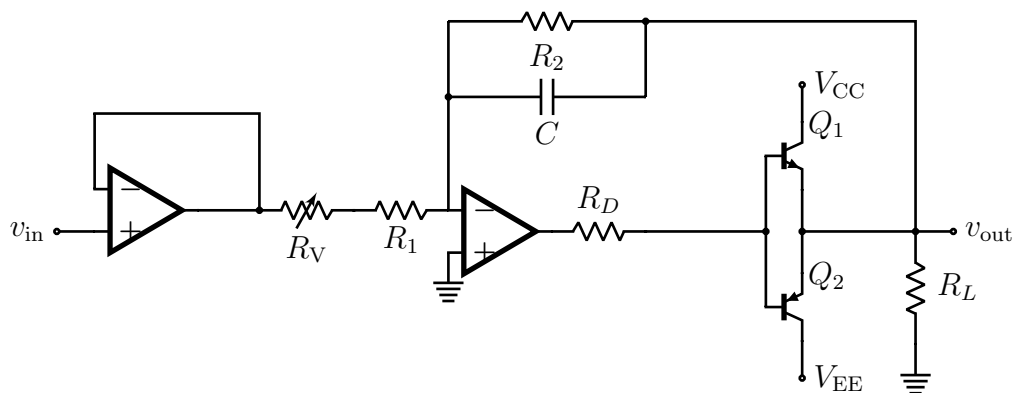

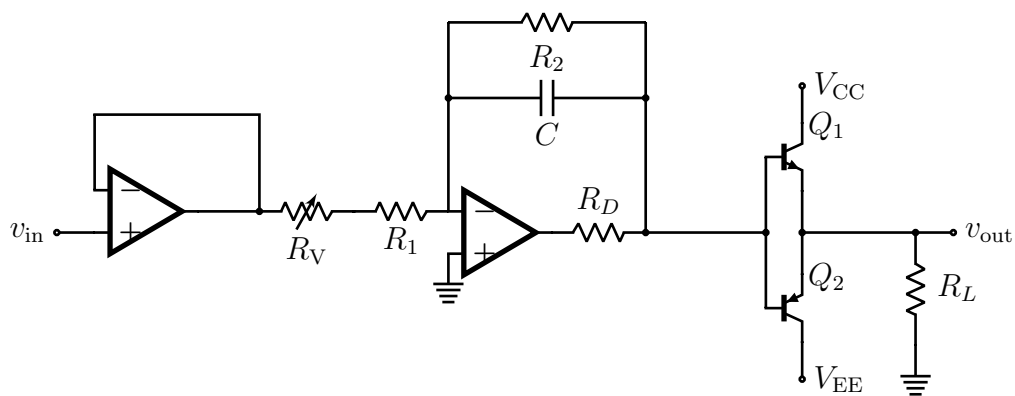Figure 9: Push-pull power amplifier with *cell-phone saver* buffer stage.

Figure 10: Hear the distortion!

8 Lab Report

Your report should include sketches of the waveforms and a record of your observations.