Design circuit with inputs Start, Stop, Clock and output Run (7.40)

- Start and Stop are of indeterminate duration and may span many clock cycles.
- When Start goes to 1, circuit must generate Run = 1.
- Run must remain 1 until Stop goes to 1, at which time it returns to 0.
- Changes in Run are synchronized with Clock.

$$D = (Start + Q) \cdot \overline{Stop}$$