

# VHDL for BCD to 7 segment converter using a Selected Signal Assignment (6.28)

Friday, August 25, 2017  
8:48 PM

with D select

$S \leftarrow \text{"0111111"} \quad \text{when "0000"}$   
 $\quad \text{"0000110"} \quad \text{when "0001"}$   
 $\quad \vdots$

