# Witcher: Systematic Crash Consistency Testing for Non-Volatile Memory Key-Value Stores

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#### Summary

- NVM enables writing crash consistent programs without paying storage overhead
- Writing crash consistent programs is error-prone
- Existing NVM bug detectors
  - Exhaustive Searching
  - User-provided Oracles
- Witcher
  - NVM-backed Key-Value Stores
  - Inference of Likely-Correctness Conditions
  - Validation with Output Equivalence Checking
  - Detected 205 (149 new) correctness/performance bugs

## Outline

1. Background and Motivation

2. Witcher

3. Evaluation

4. Conclusion

## Finally NVM is here to stay but ...

#### **NVM Characteristics:**

Persistence

Byte-addressability

Low access latency

High capacity

#### **Crash Consistency**

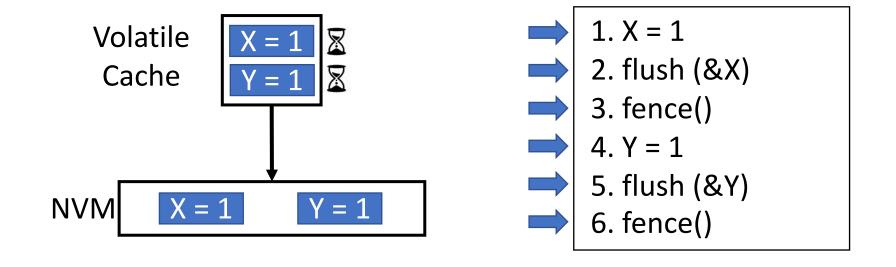
Applications can recover a consistent state from NVM in the event of a crash (e.g., power failure)

#### **Challenges in NVM Programming**

- "Volatile" cache states may be lost upon a crash
- Controlling the durability and the ordering for cachelines is the key

#### Controlling Durability and Ordering

- flush (x86: clwb): write back a cache line from cache to memory
- fence (x86: sfence): ordering guarantee between flushes



Durability and Ordering

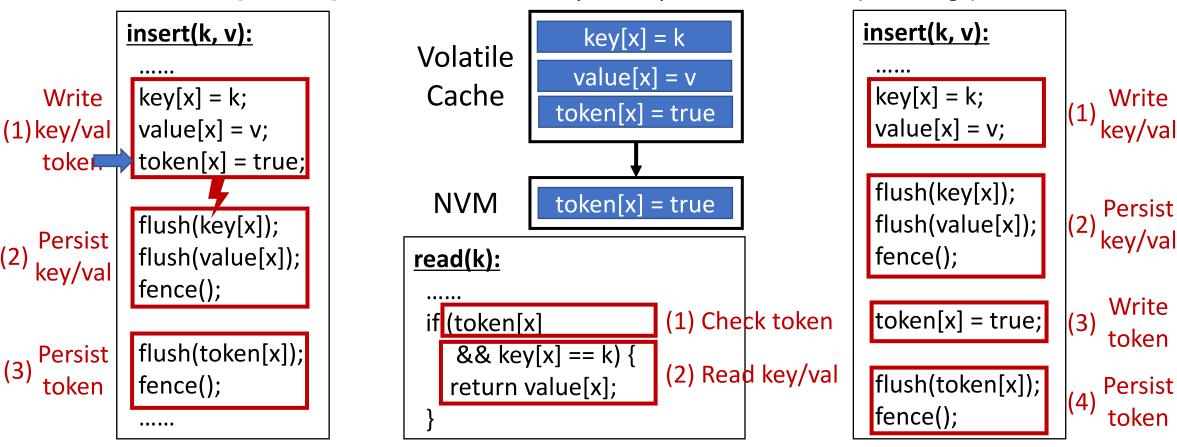
#### Persistence Bugs

- Persistence Ordering Bug
  - : Fail to enforce that "A" must be persisted before "B"
- Persistence Atomicity Bug
  - : Fail to enforce that "A" and "B" must be persisted together
- Persistence Performance Bug
  - : e.g., extra flush/fence

#### Persistence Ordering Bug

: Fail to enforce that "A" must be persisted before "B".

LevalHash [OSDI'18]: Each bucket has arrays of Keys, Values, Tokens (valid flags)

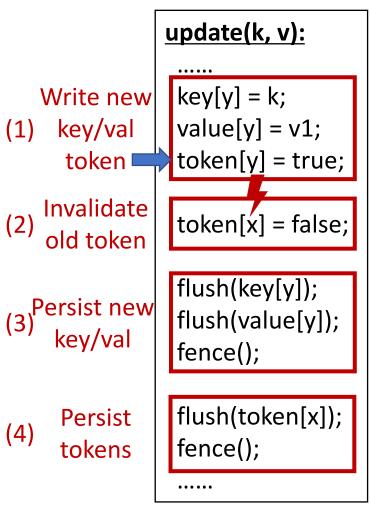


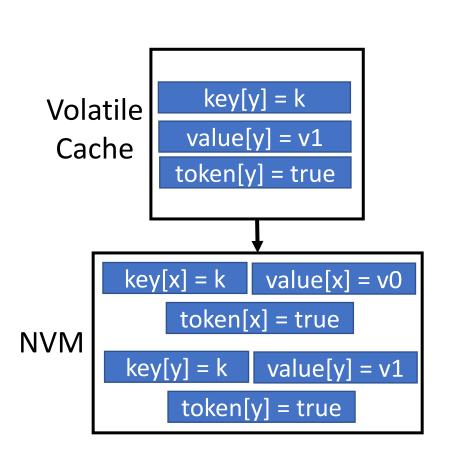
Application-specific knowledge is required to detect this bug!

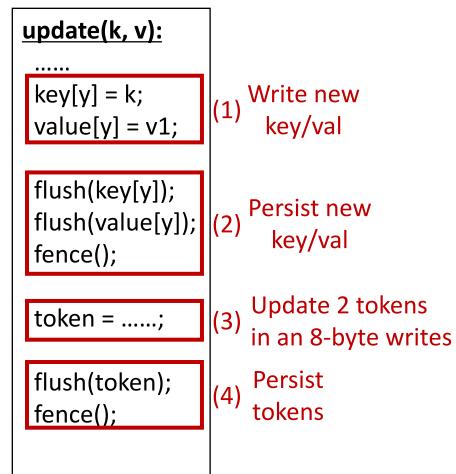
## Persistence Atomicity Bug

: Fail to enforce that "A" and "B" must be persisted together

LevalHash [OSDI'18]: Each bucket has arrays of Keys, Values, Tokens (valid flags)







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#### **Existing Works**

- Exhaustive searching:
  - Yat[ATC'14]
  - Enumerate all possible crash states
  - [Pros] no false negative
  - [Cons] not scalable
- User-provided oracles:
  - PMTest[ASPLOS'19], XFDetector[ASPLOS'20], Agamotto[OSDI'20]
  - Rely on users' guidance to validate a crash state
  - [Pros] make bug validation process simple
  - [Cons] manual efforts, error-prone, especially for application-specific oracles

Witcher uses neither exhaustive searching nor user-provided oracles.

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## Key Idea 1: Likely-Correctness Conditions

Challenge: How can we prune the test space without user-provided oracles?

Infer likely-correctness conditions from code

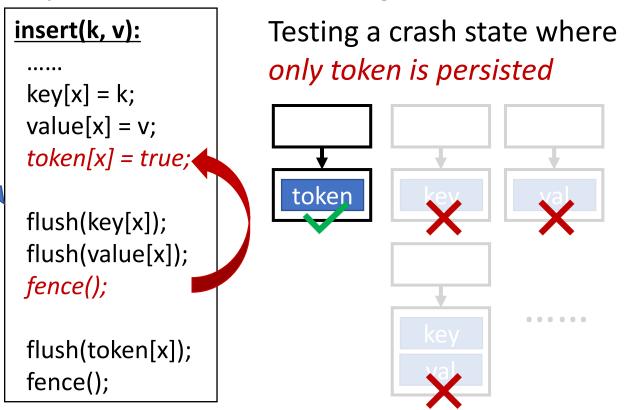
```
read(k):
.....
if (token[x]
    && key[x] == k) {
    return value[x];
}
```

Hint: Check token first then read key-val (Control dependence: token & key-val)

Likely-correctness condition:

Persist key-val before updating token

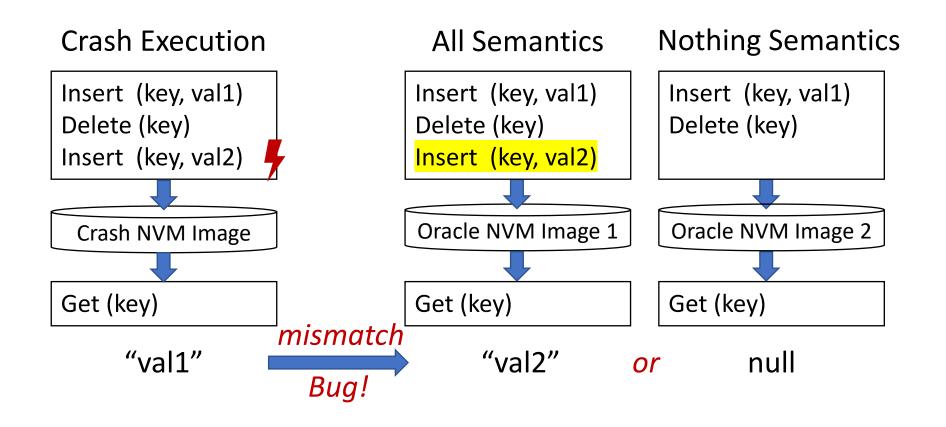
Only test crash states violating inferred conditions



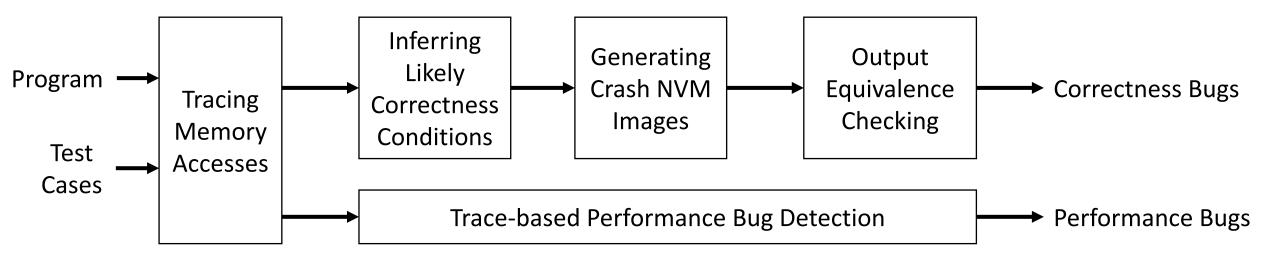
## Key Idea 2: Output Equivalence Checking

Challenge: How to automatically validate a crashed state?

Test durable linearizability (all or nothing semantics) by comparing outputs



#### Witcher: Systematic Crash Consistency Testing for NVM KV Stores <sup>13</sup>



#### Tracing Memory Accesses

- LLVM compiler pass
- Execute the instrumented binary with a test case to collect trace
- Load
- Store with updated value
- Branch
- Call/return
- Flush
- Fence

## Inferring Likely-Correctness Conditions

Correlate program dependence to NVM correctness conditions.

Y = X + 1; // both X and Y are on NVM

W(Y) is data-dependent on R(X)

Implicit correctness condition: X should be persistent before writing Y

Inferred likely-correctness condition: P(X) happens before W(Y)

Only test a crash state violating the condition: Y is persistent but X is not persistent

#### Inferring Likely-Correctness Conditions

- PO1: A data dependency implies a persistence ordering
- PO2: A control dependency implies a persistence ordering
- PO3: A guarded read implies a persistence ordering
- PA1: Guardian implies persistence atomicity

#	Hint		Likely-correctness Cond		<b>NVM Image</b>	
	Example	Rule	Example	Rule	$\mathbb{P}$	$\mathbb{U}$
P01	Y=X+3;	$\mathbb{W}(\mathbb{Y}) \xrightarrow{\mathrm{dd}} \mathbb{R}(\mathbb{X})$	X=; Y=;	$P(X) \xrightarrow{hb} W(Y)$	Y	Х
P02	<b>if</b> (X){Y=3;}	$\mathbb{W}(\mathbb{Y}) \xrightarrow{\mathrm{cd}} \mathbb{R}(\mathbb{X})$	X=; Y=;	$P(X) \xrightarrow{hb} W(Y)$	Y	Х
P03	<b>if</b> (X){Z=Y+3;}	$R(Y) \xrightarrow{cd} R(X)$	Y=; X=;	$P(Y) \xrightarrow{hb} W(X)$	X	Y
PA1	<b>if</b> (X){M=N+3;}	$R(N) \xrightarrow{cd} R(X)$	X=; Y=;	AP(X,Y)	X	Y
	<b>if</b> (Y){K=J+3;}	$R(J) \xrightarrow{cd} R(Y)$			Y	Х

- Program Analysis for Inference
  - Static analysis: register-level data and control dependency
  - Dynamic trace analysis: memory-level data dependency

#### Generating Crash Images

How to guarantee each crash NVM state is valid?

Cache and NVM simulation

- Starting from the empty cache and NVM states
- Simulates the effects of store, flush and fence along the trace

How to detect condition violations?

Check before simulating each fence instruction

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#### **Evaluation**

#### **Evaluation Questions:**

- Can Witcher detect new bugs?
- Is Witcher scalable?

#### **Tested Applications:**

- 20 NVM programs
  - highly optimized persistent key-value indexes
  - concurrent persistent indexes converted by RECIPE [SOSP'19]
  - Intel PMDK applications
  - Persistent server applications
- Low-level persistence primitives and High-level persistence transactions
- Single-thread, lock-based, lock-free
- 2000 randomly generated key-value operations

#### **Detected Correctness Bugs**

- 47 (36 new) correctness bugs from 18 apps
- 25 persistence ordering bugs and 22 persistence atomicity bugs
- All confirmed by the developers

#### Diverse impact

- Lost, unexpected, duplicated key-val pairs
- Unexpected operation failure
- Inconsistent structure

#### Fixing strategies

- Adding required persistence primitives
- Reordering persistence primitives
- Merge multiple writes into one word-size write
- Crash-inconsistency-tolerable
- Crash-inconsistency-recoverable
- Logging/transaction

All those bug fixes are complicated and require deep understanding of the applications.

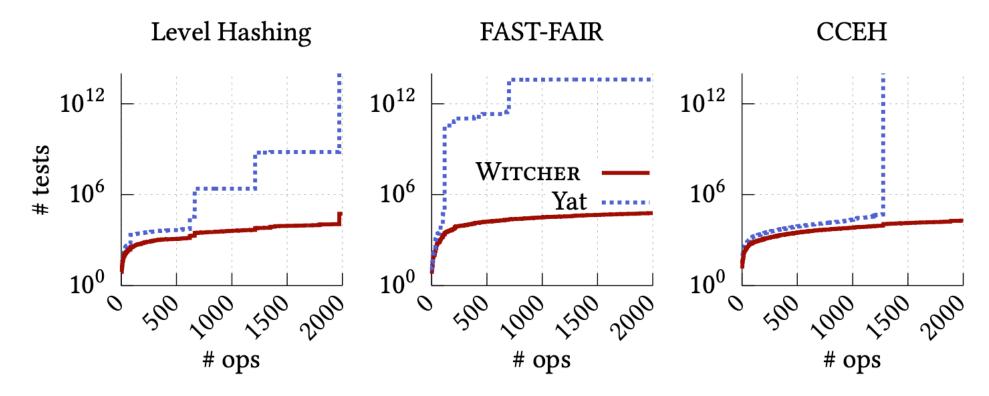
#### Detected bug in PMDK memory allocator

```
diff --git a/src/libpmemobj/heap.c b/src/libpmemobj/heap.c
index 4cbb52c42..a45e5742d 100644
--- a/src/libpmemobj/heap.c
@@ -953,11 +953,11 @@ heap_split_block(struct palloc_heap *heap, struct bucket *b,
        uint32 t new chunk id = m->chunk id + units;
        uint32 t new_size idx = m->size idx - units;
       I*m = memblock_huge_init(heap, m->chunk_id, m->zone_id, units); ---> Change the old header
                                                                        → Write a new header
        struct memory_block n = memblock_huge_init(heap, -----
                                                  new chunk id,
                                                  m->zone id,
                                                  new_size_idx);
```

- Detecting this bug requires application-specific knowledge
- Witcher is able to detect this bug by using
  - Likely-correctness condition inference
  - Output equivalence checking

## Scalability

Comparison with Exhaustive Searching approach (Yat [ATC'14])



Comparison with Random Searching

- 100 Million (1 week)
- Detect one or two of the bugs

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#### Conclusion

- Developing a correct NVM-backed crash consistent program is hard
- Witcher
  - Infers Likely-Correctness Conditions to prune test space
  - Performs Output Equivalence Checking to test inconsistencies automatically
- Detected 205 (149 new) correctness/performance bugs in NVM-backed keyvalue stores and PMDK library.

#### Witcher can effectively detect NVM bugs

- without a user-provided checker
- without a test space explosion problem.