

## CORE

### MEMORYCONTROLLER

ICACHE

DCACHE

TLB

REG

FREG

CSR

TIMERS...

## SYSTEM INTERFACE

HANDLETRAP(\*CORE)

HANDLEBOOT(\*CORE)

MEMORY() \*MEMORY

RESERVATIONSETS() \*RESERVATIONSETS

INTERRUPTMATRIX() \*[N][N]UINT32