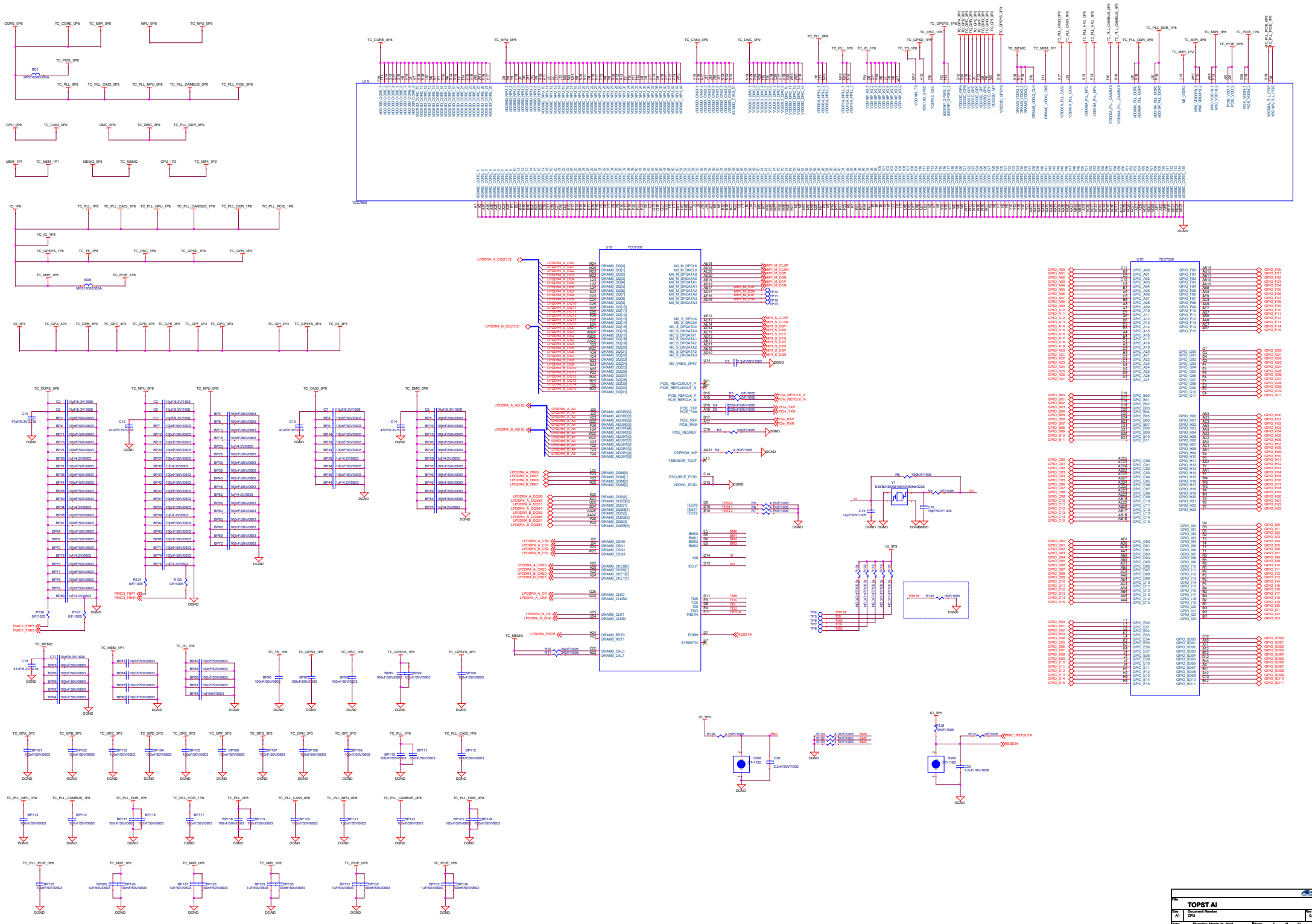
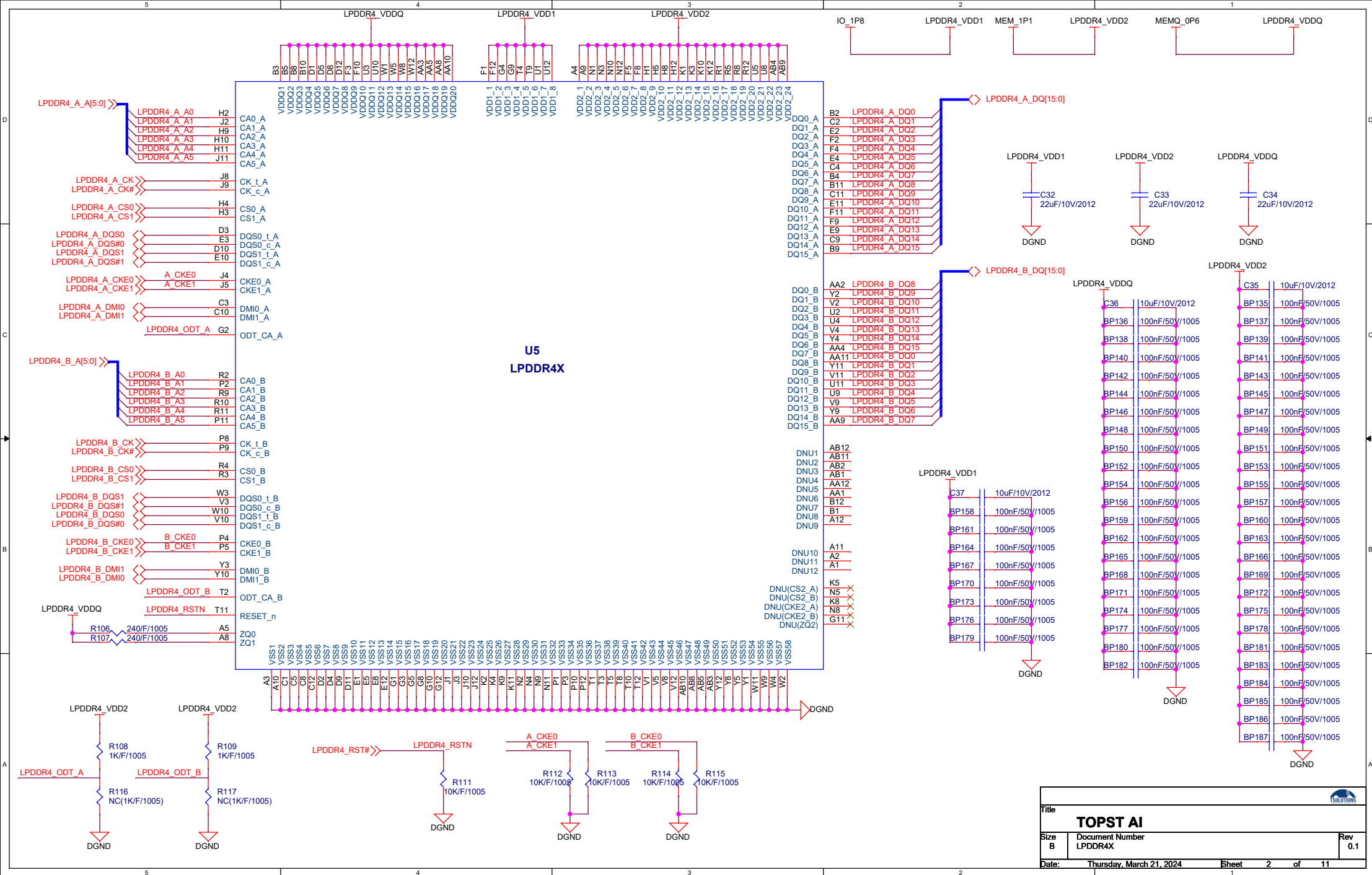


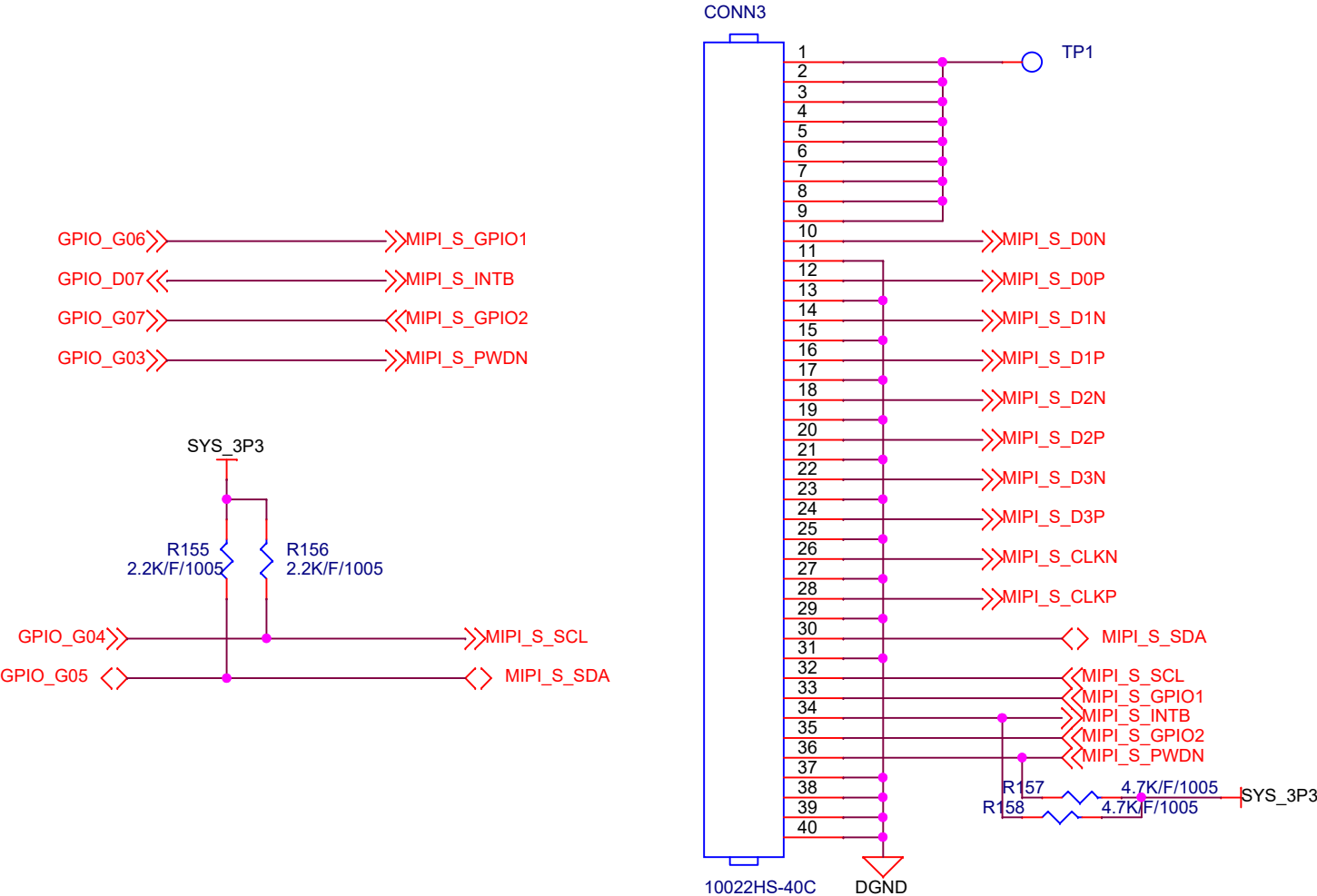
TOPST AI VER1

Rev.	Date	Description		Schematic Page	Remark
		Schematic	PCB		
V0.1	2023.07.07	- Initial Release	- Initial Release		



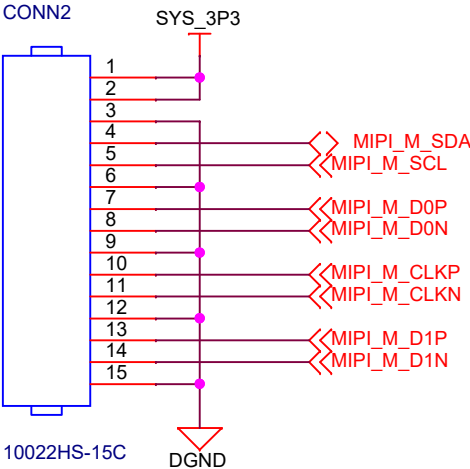
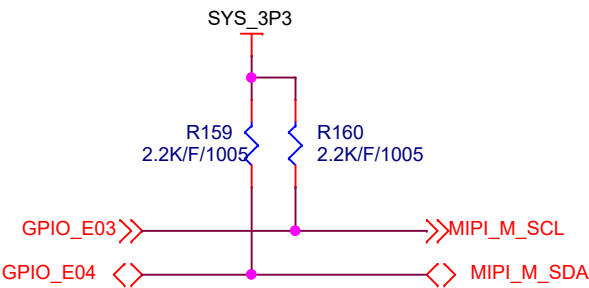


MIPI-CSI Connector

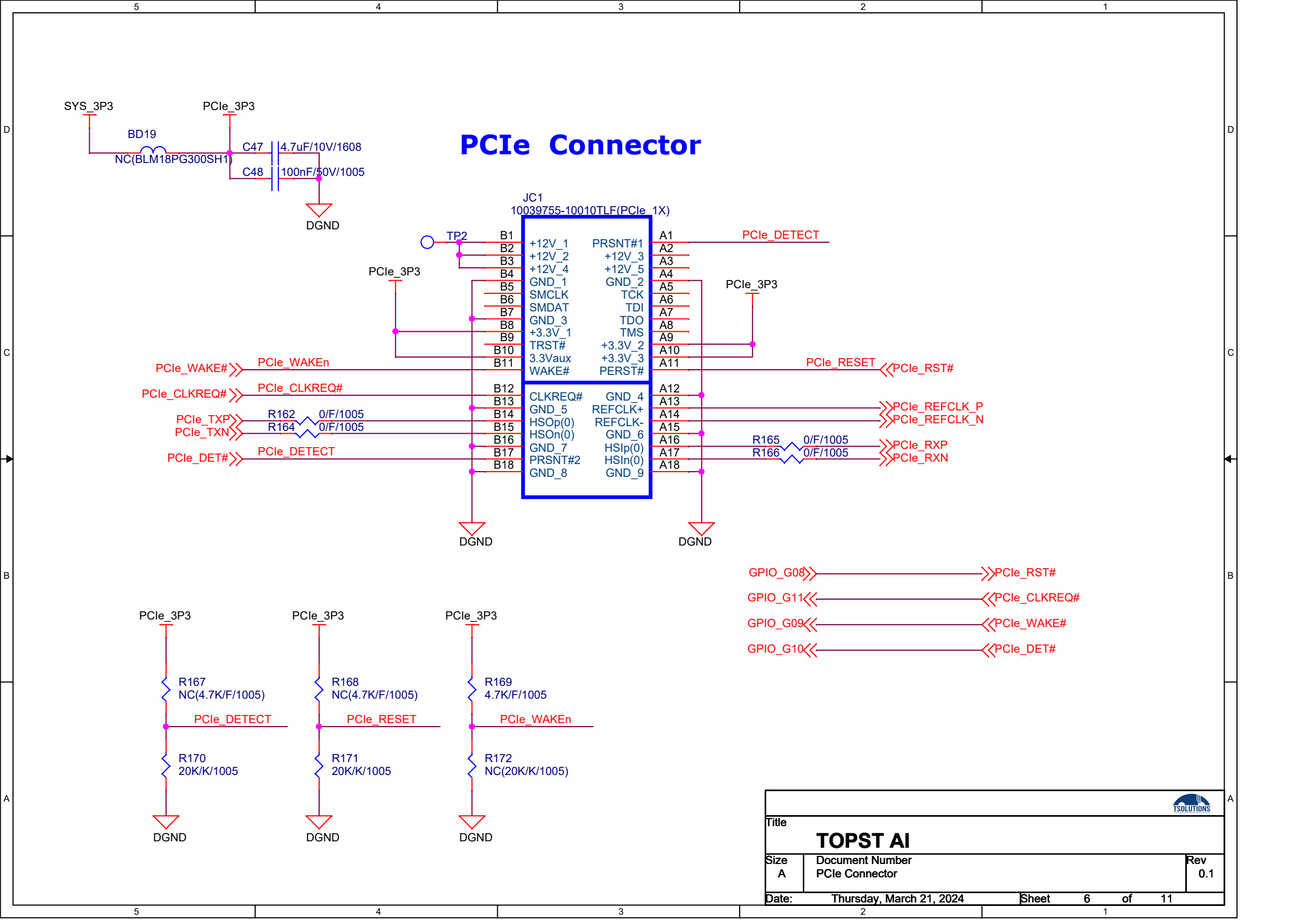


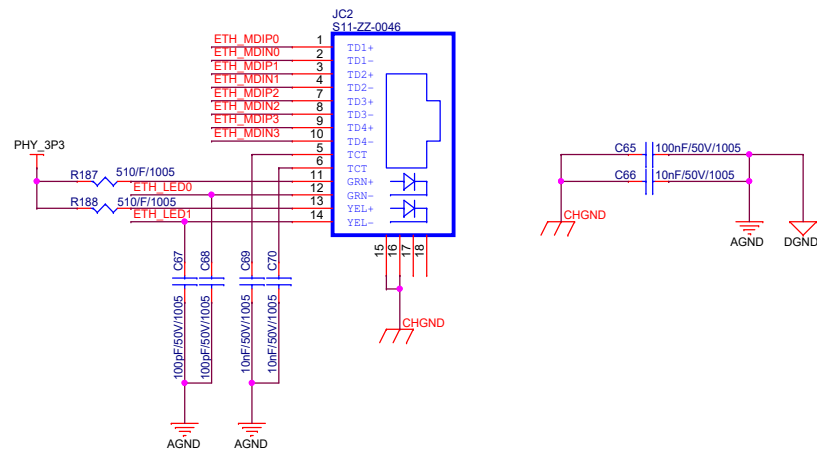
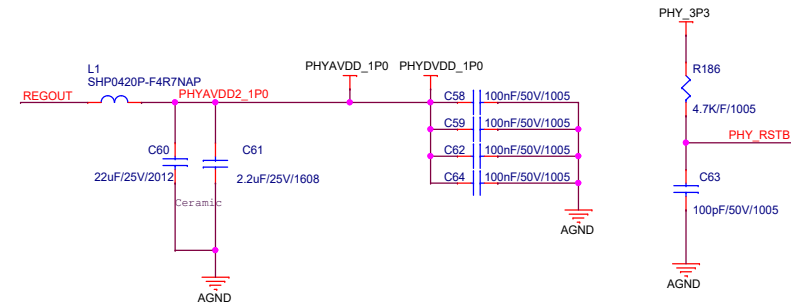
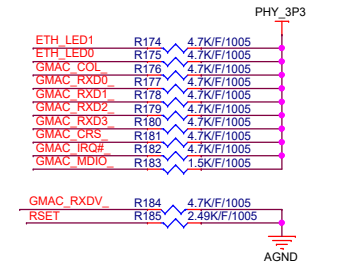
TSOLUTIONS		
Title		
TOPST AI		
Size	Document Number	Rev
A	MIPI CSI	0.1
Date:	Thursday, March 21, 2024	Sheet 4 of 11

MIPI-DSI Connector



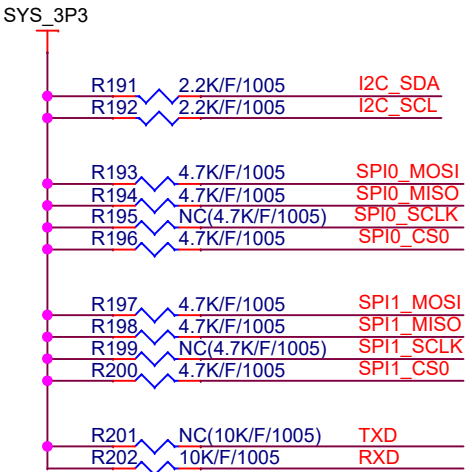
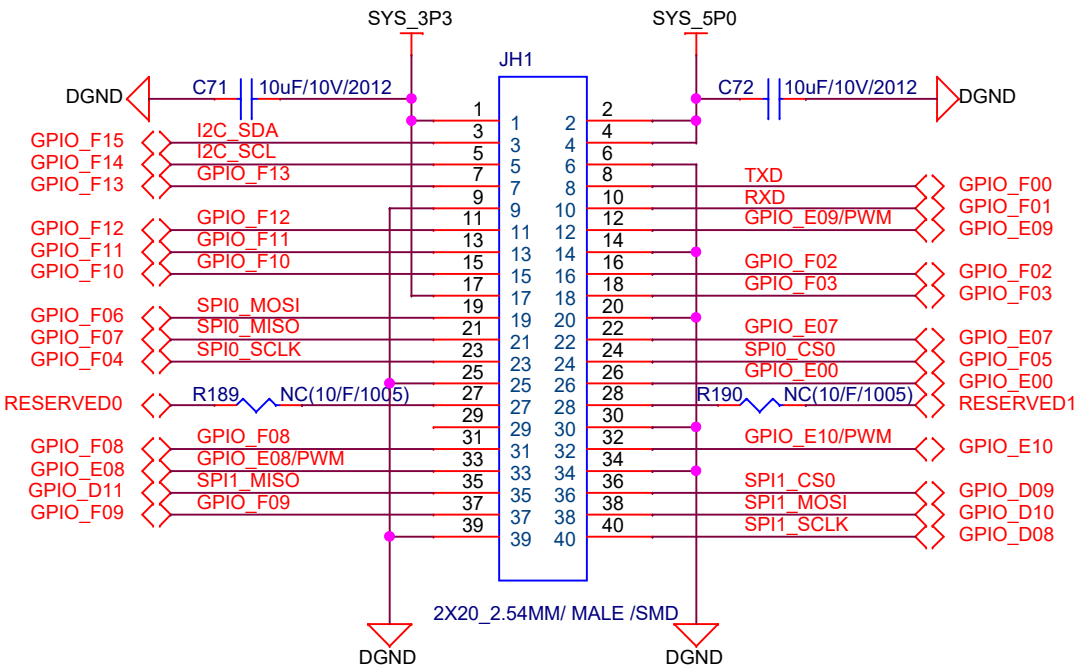
Title		
TOPST AI		
Size	Document Number	Rev
A	MIPI-DSI	0.1
Date:	Thursday, March 21, 2024	Sheet 5 of 11



[illegible]

GPIO_A00	GMAC_TXCLK
GPIO_A01	GMAC_MDC
GPIO_A02	GMAC_MDIO
GPIO_A03	GMAC_RXD0
GPIO_A04	GMAC_RXD1
GPIO_A05	GMAC_TXD0
GPIO_A06	GMAC_TXD1
GPIO_A07	GMAC_TXEN
GPIO_A08	GMAC_RXDV
GPIO_A09	GMAC_RXD2
GPIO_A10	GMAC_RXD3
GPIO_A11	GMAC_TXD2
GPIO_A12	GMAC_TXD3
GPIO_A13	GMAC_RXCLK
GPIO_A14	GMAC_RX0#
GPIO_A15	PHY_RSTB

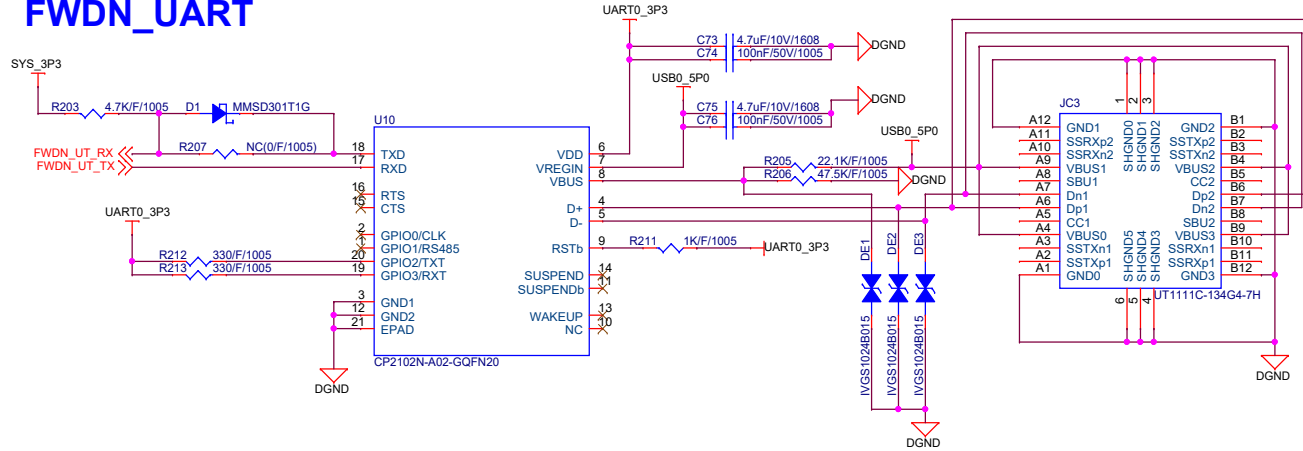
GPIO 40PIN HEADER



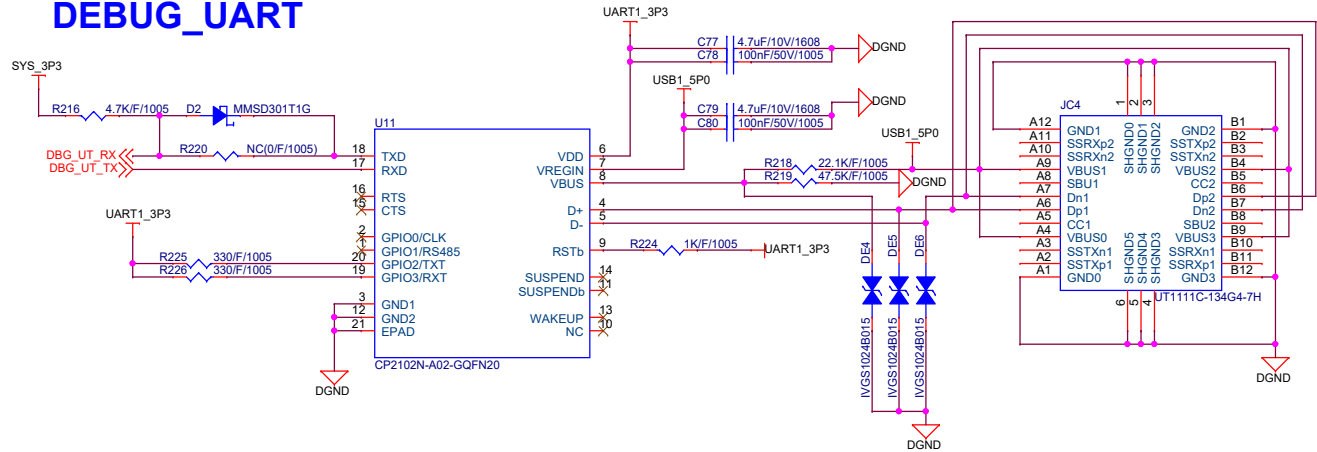
GPIO_E05 <> RESERVED0
GPIO_E06 <> RESERVED1

Title		
TOPST AI		
Size	Document Number	Rev
A	GPIO 40PIN HEADER	0.1
Date:	Thursday, March 21, 2024	Sheet 8 of 11

FWDN_UART



DEBUG_UART



GPIO_C12 >>> FWDN_UT_TX
 GPIO_C13 >>> FWDN_UT_RX
 GPIO_G00 >>> DBG_UT_TX
 GPIO_G01 >>> DBG_UT_RX

