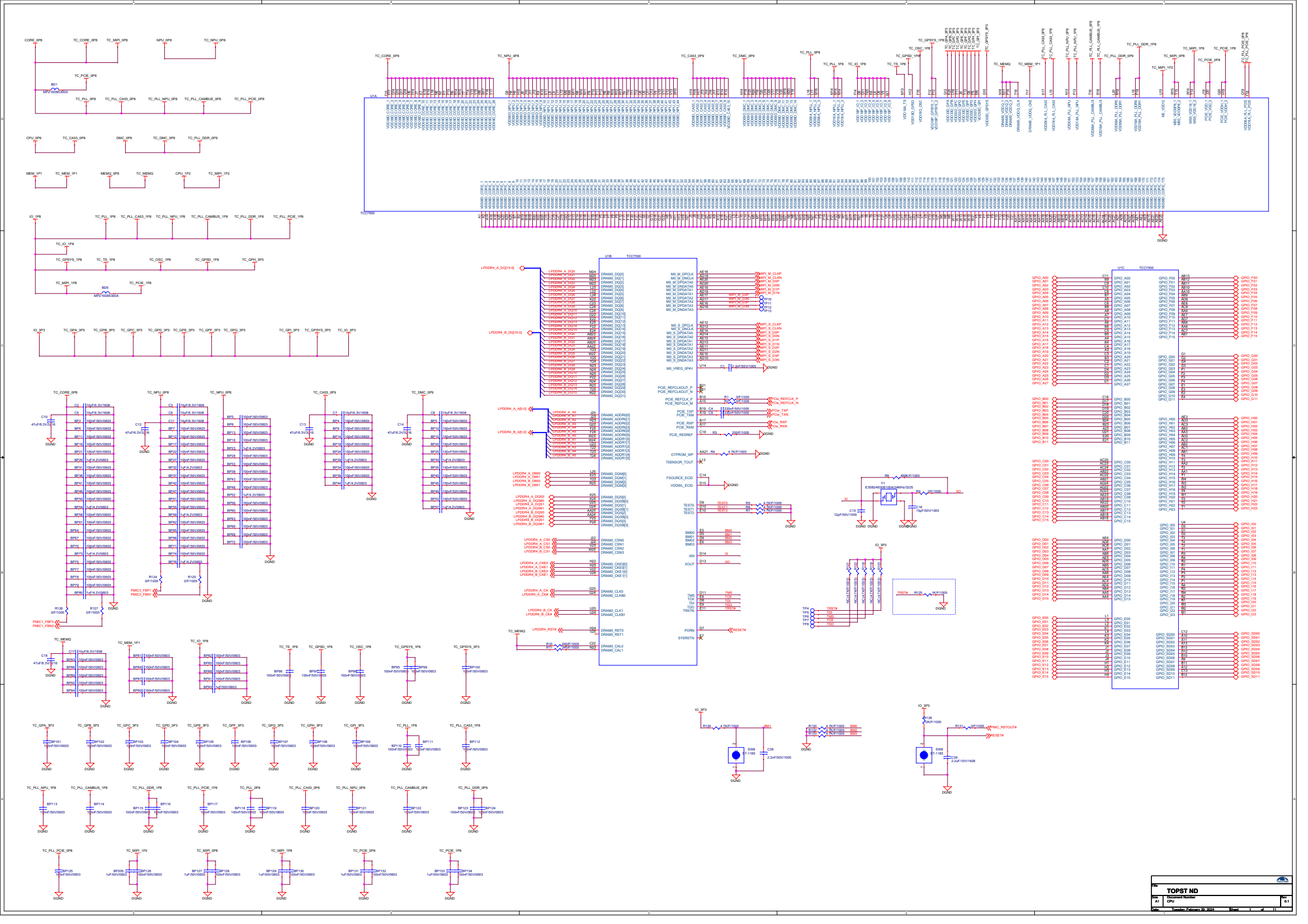
TOPST ND VER1

Rev.	Date	Description		Schematic Page	Remark
		Schematic	PCB		
V0.1	2023.07.07	- Initial Release	- Initial Release		

TSOLUTIONS.,INC



File:	TOPST ND
Size: A1	Document Number: CPU
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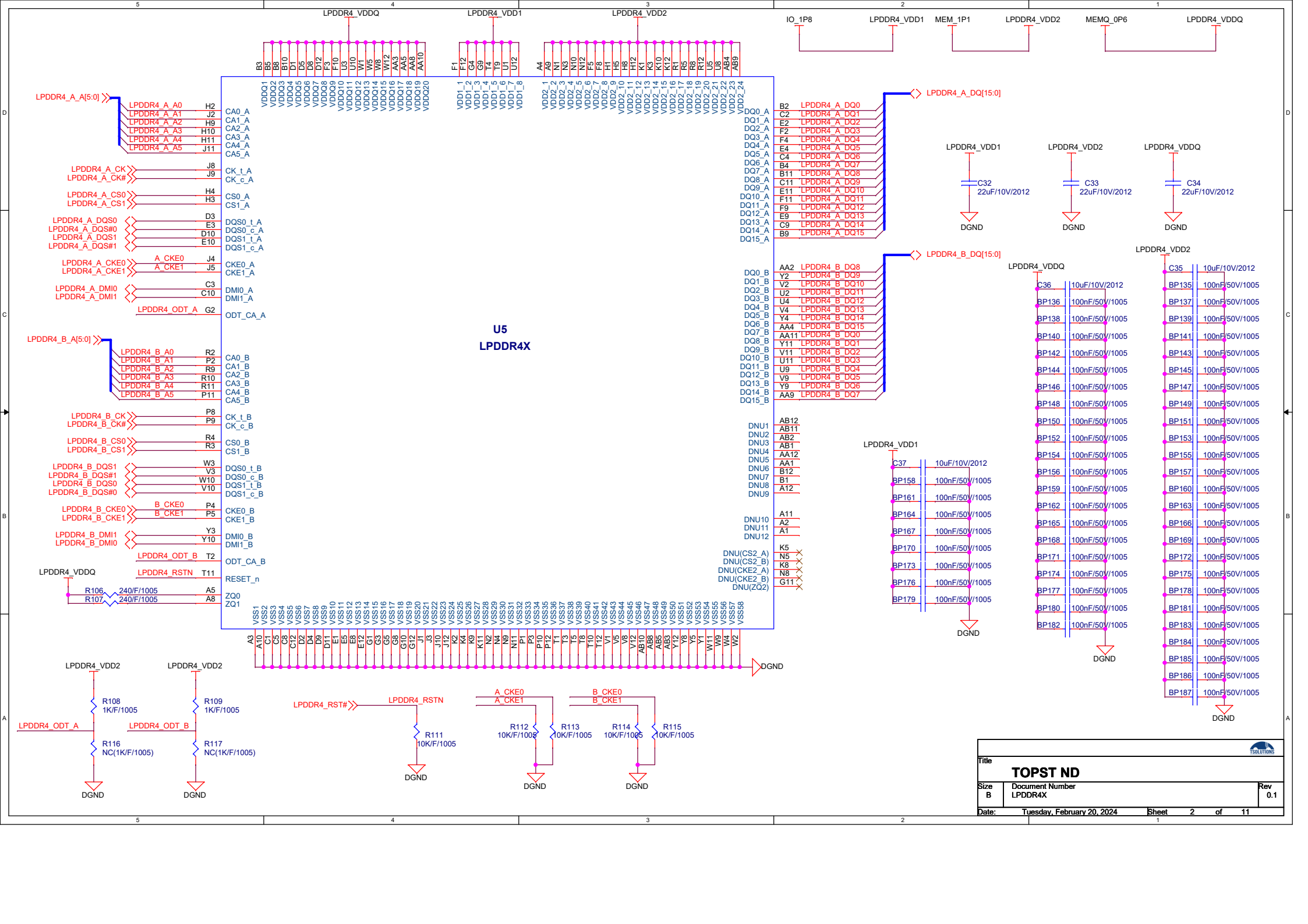

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Size B	Document Number LPDDR4X	Rev 0.1
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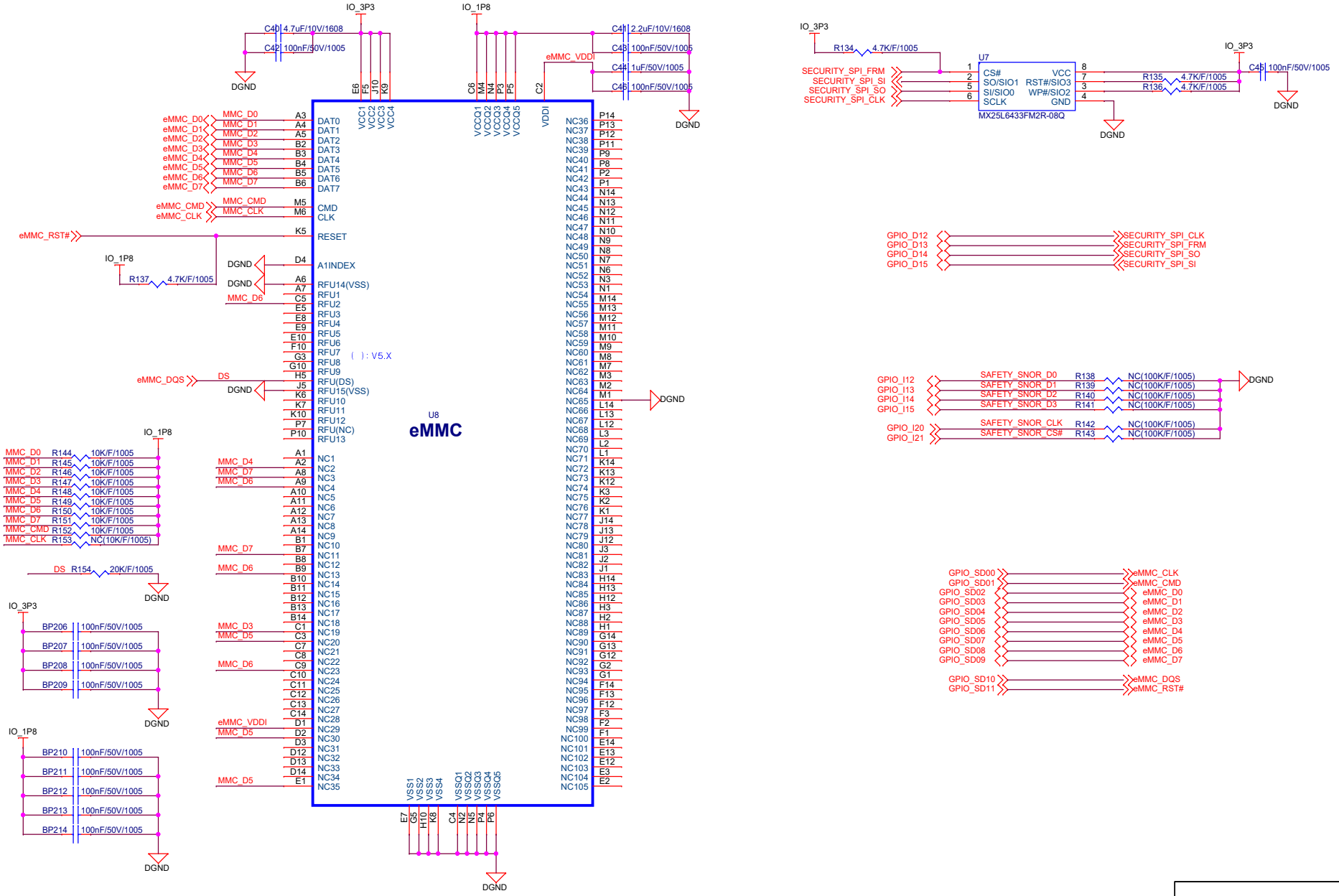

Title	TOPST ND
Size	A3
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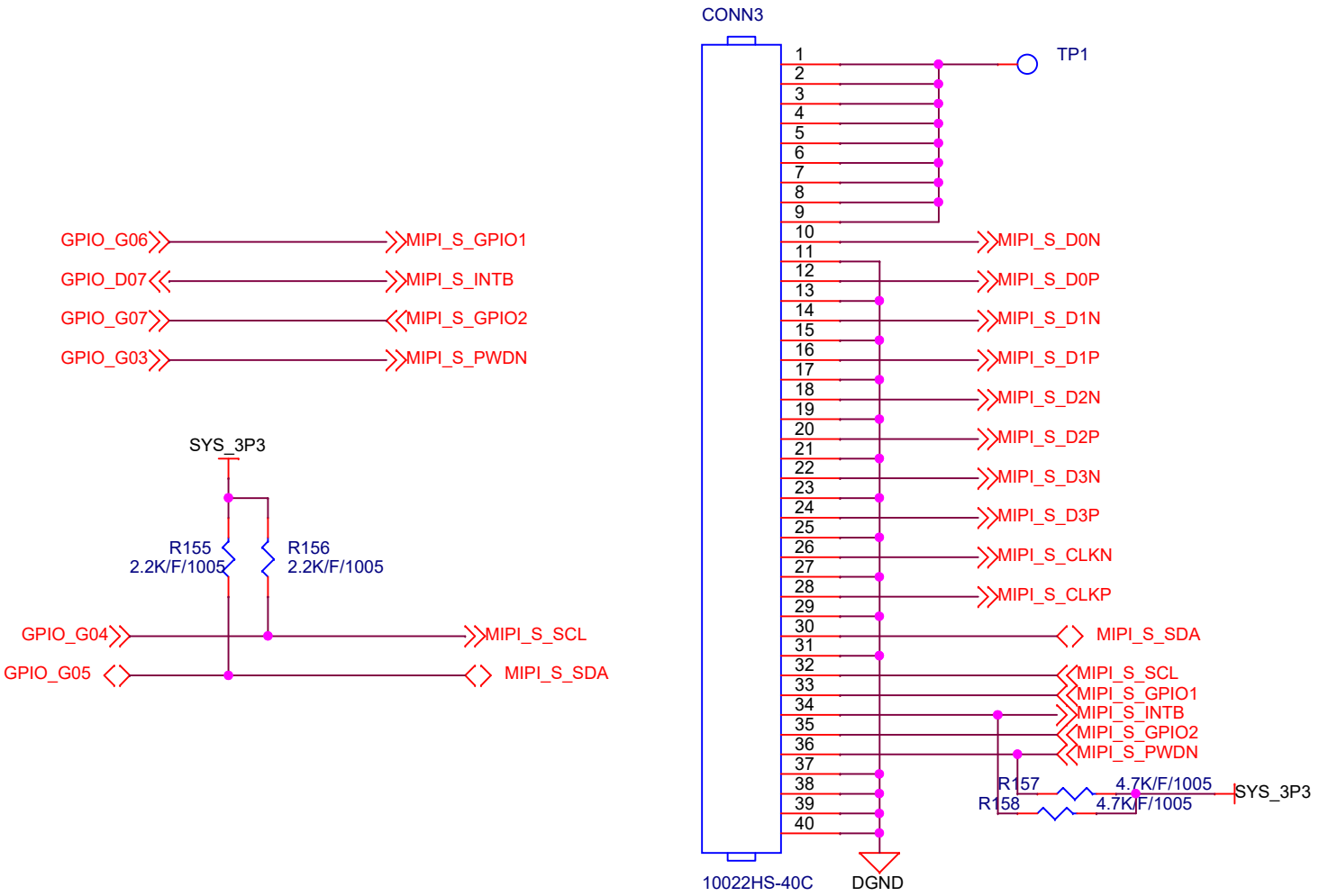
MIPI-CSI Connector



Title	TOPST ND	
Size: A	Document Number: MIPI CSI	Rev: 0.1
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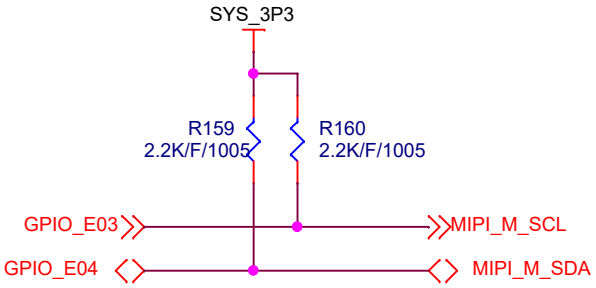
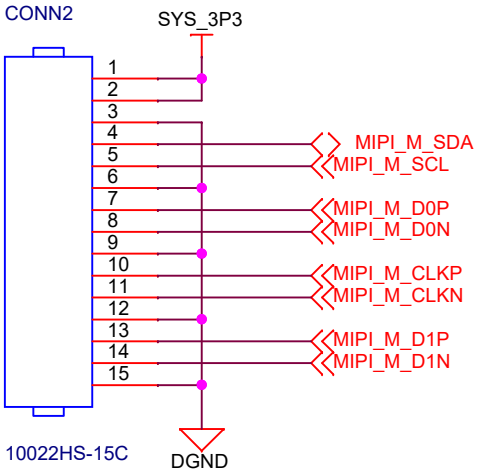
MIPI-DSI Connector





Title: TOPST ND		
Size: A	Document Number: MIPI-DSI	Rev: 0.1
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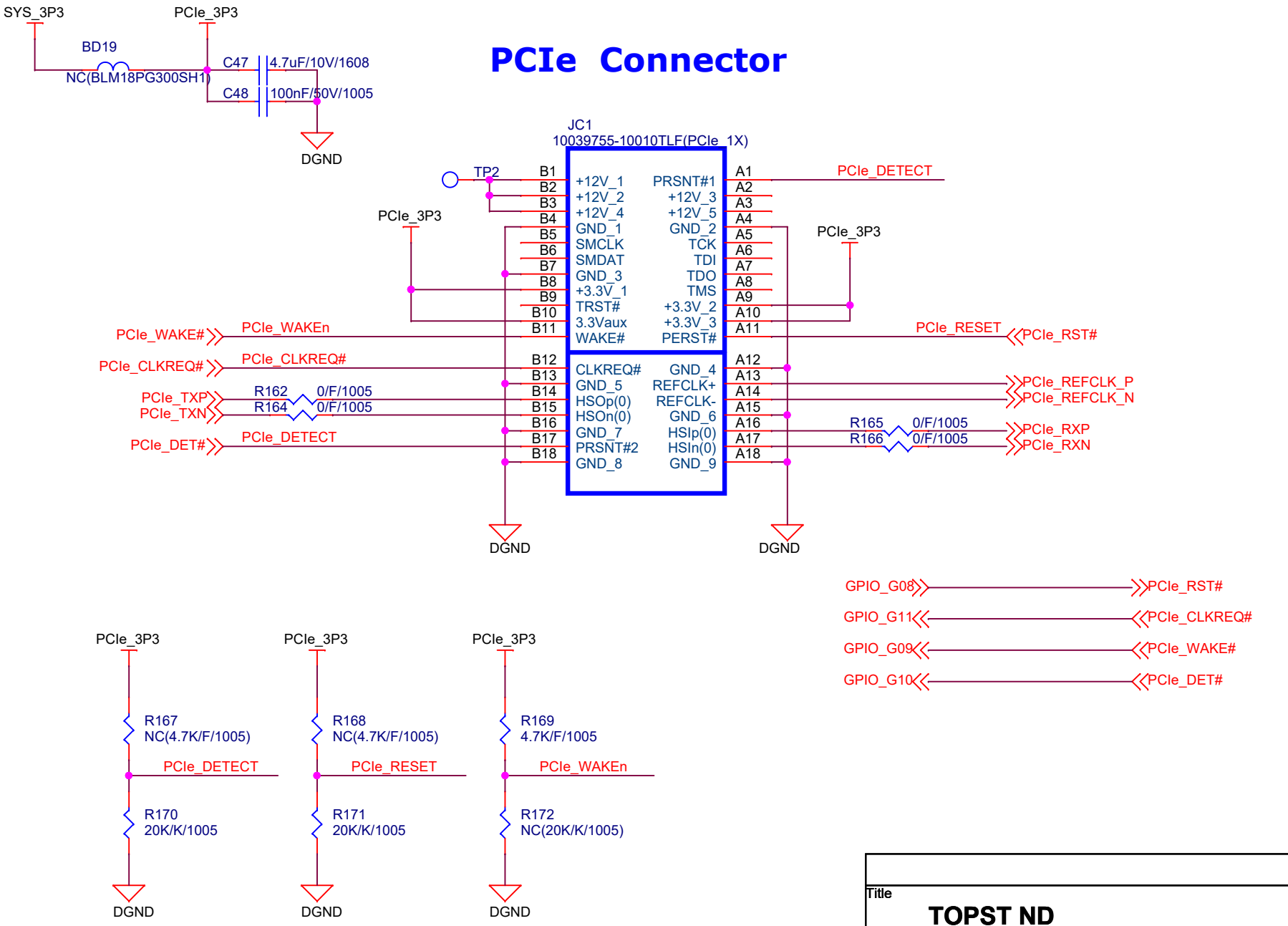
PCIe Connector



Title	TOPST ND	
Size: A	Document Number: PCIe Connector	Rev: 0.1
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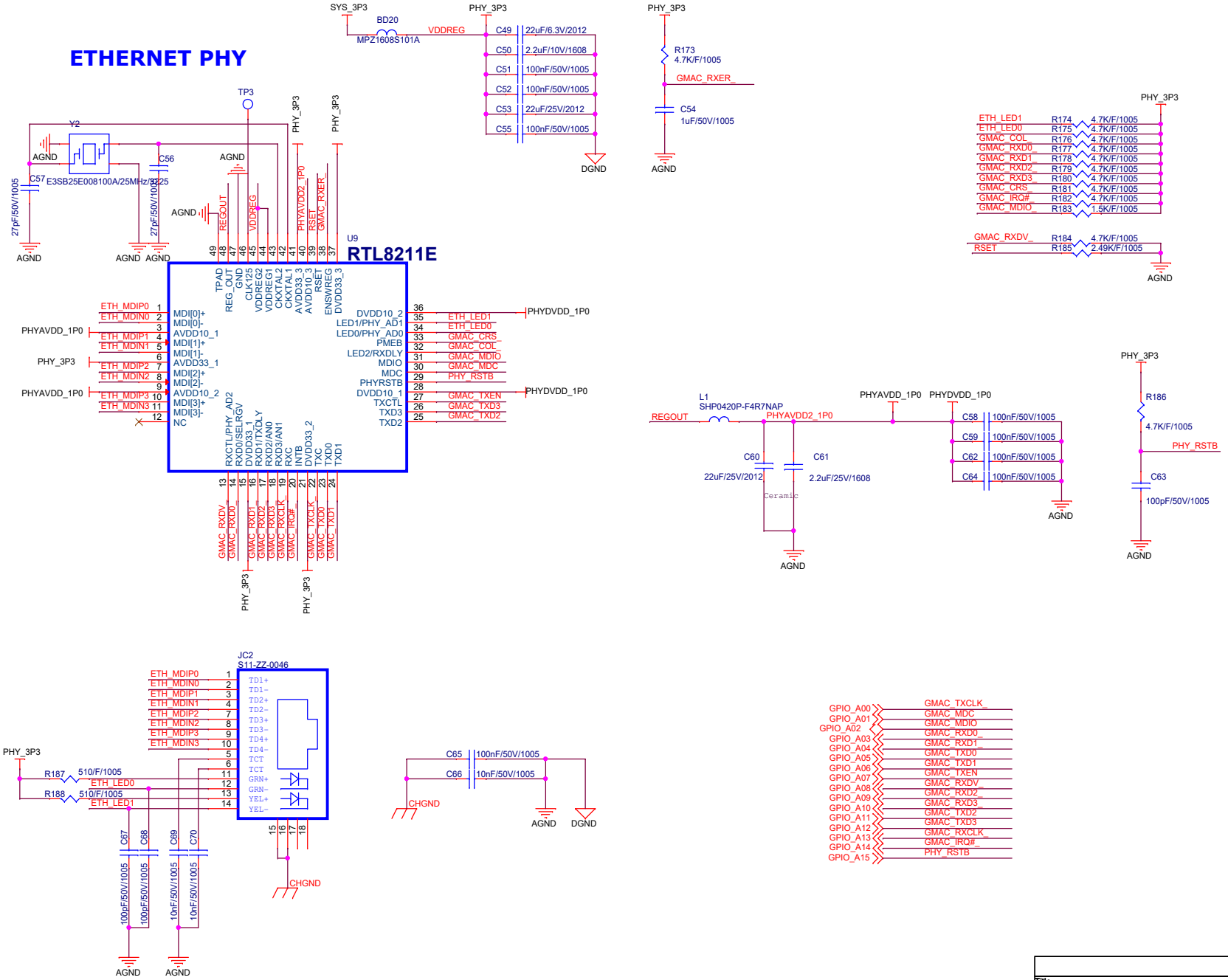
ETHERNET PHY



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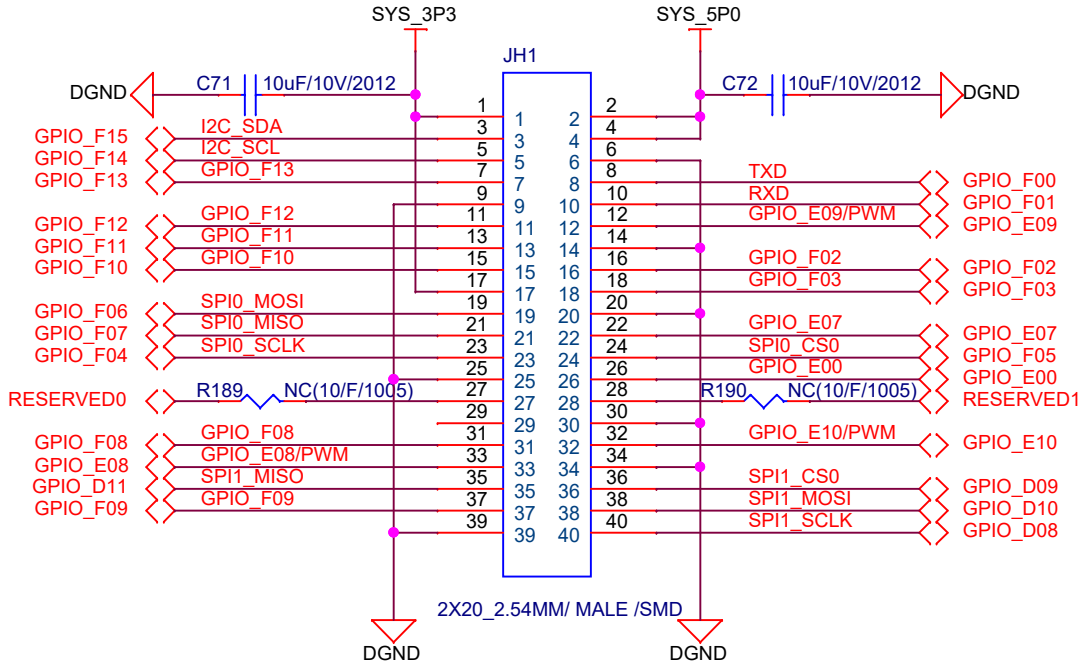
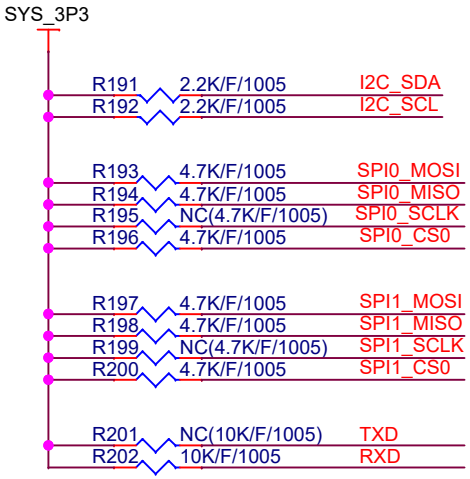
GPIO 40PIN HEADER





Title	TOPST ND	
Size: A	Document Number: GPIO 40PIN HEADER	Rev: 0.1
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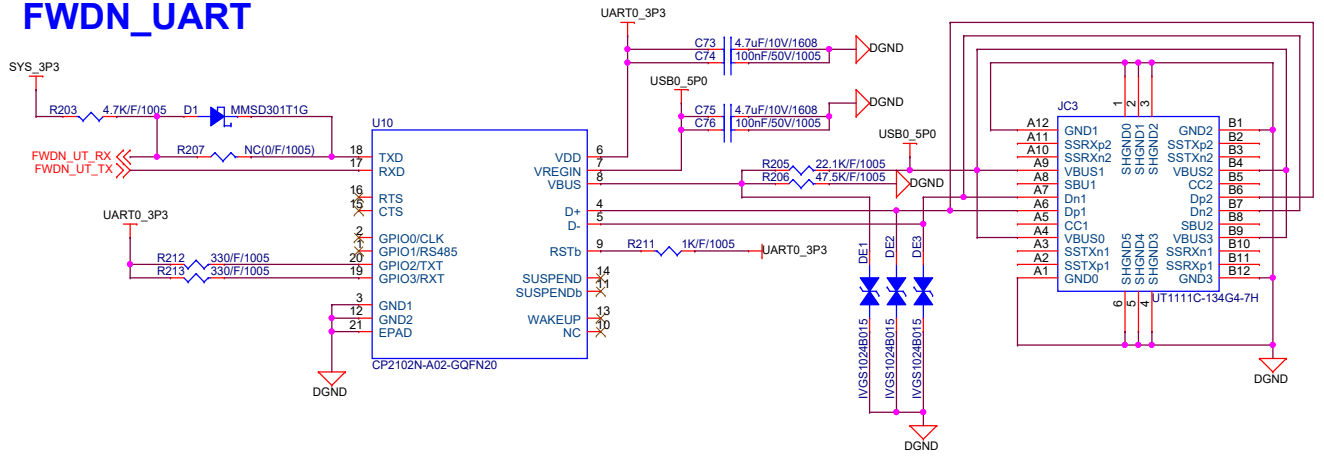
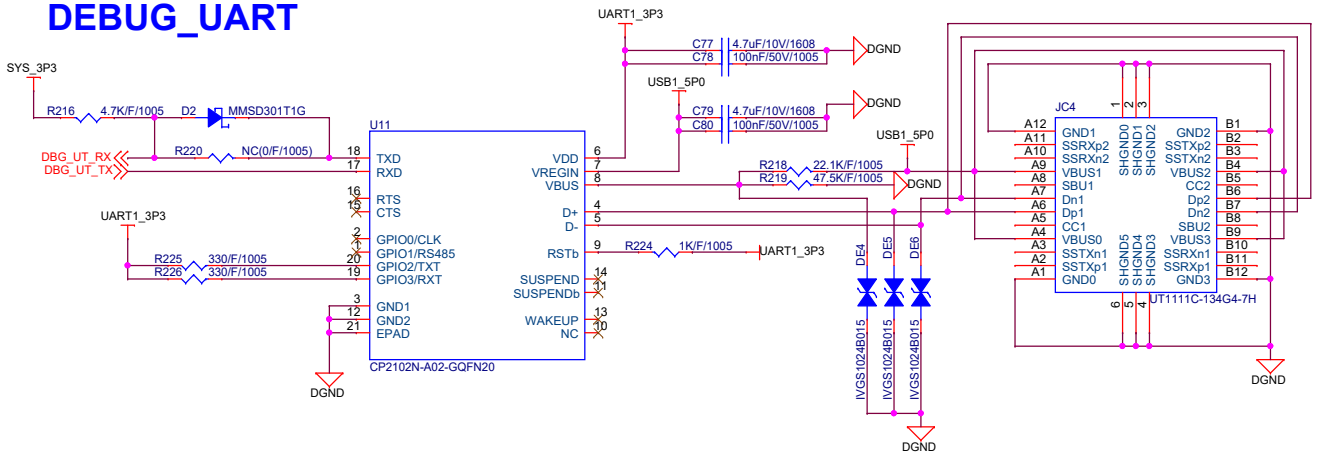
FWDN_UART



DEBUG_UART



GPIO_C12 — FWDN_UT_TX
GPIO_C13 — FWDN_UT_RX

GPIO_G00 — DBG_UT_TX
GPIO_G01 — DBG_UT_RX

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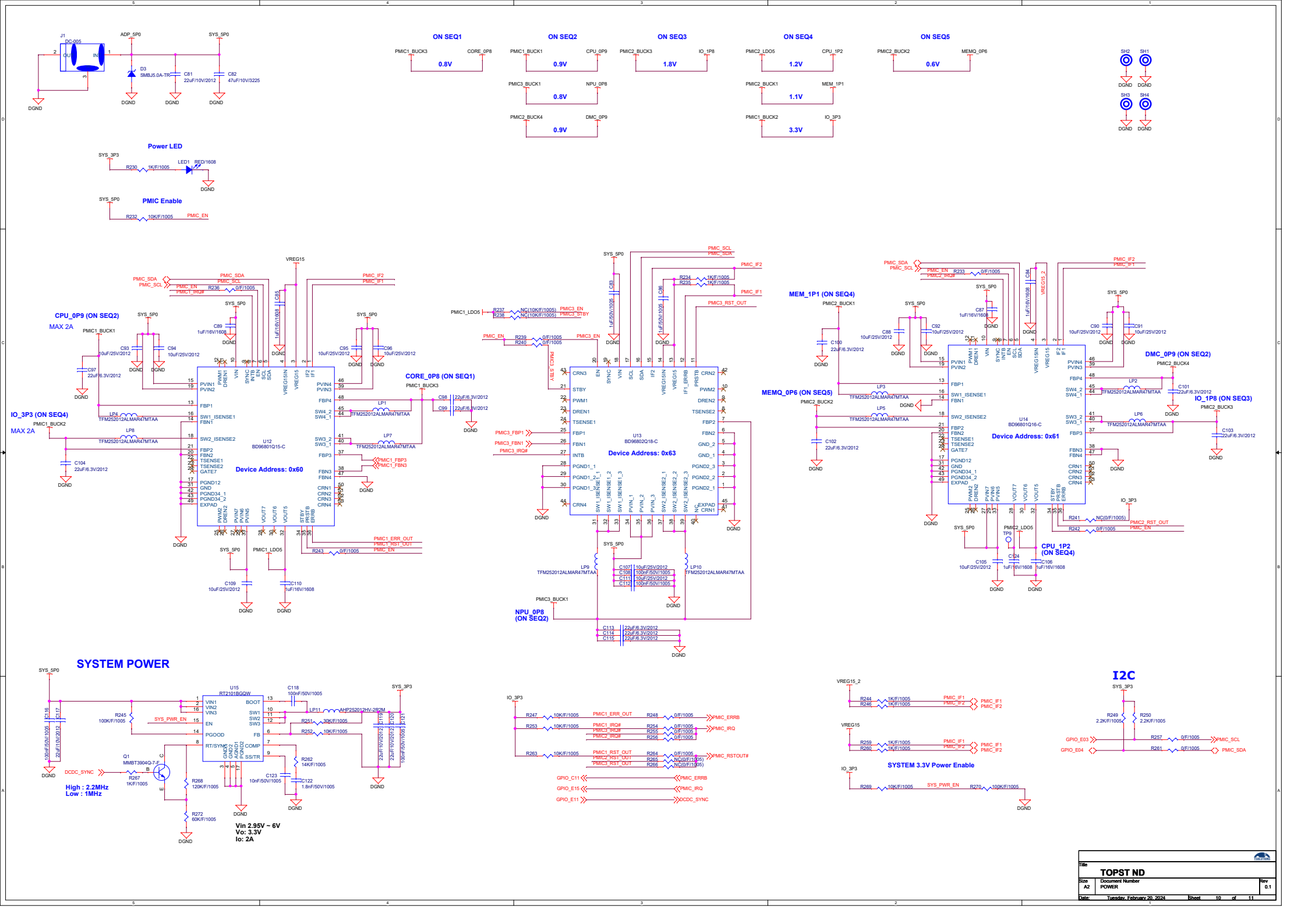
ON SEQ1
PMIC1_BUCK3
CORE_0P8
0.8V
ON SEQ2
PMIC1_BUCK1
CPU_0P9
0.9V
PMIC3_BUCK1
NPU_0P8
0.8V
PMIC2_BUCK4
DMC_0P9
0.9V
ON SEQ3
PMIC2_BUCK3
IO_1P8
1.8V
ON SEQ4
PMIC2_LDO5
CPU_1P2
1.2V
PMIC2_BUCK1
MEM_1P1
1.1V
PMIC1_BUCK2
IO_3P3
3.3V
ON SEQ5
PMIC2_BUCK2
MEMQ_0P6
0.6V
Power LED
PMIC Enable
CPU_0P9 (ON SEQ2)
MAX 2A
IO_3P3 (ON SEQ4)
MAX 2A
CORE_0P8 (ON SEQ1)
Device Address: 0x60
Device Address: 0x63
MEM_1P1 (ON SEQ4)
MEMQ_0P6 (ON SEQ5)
Device Address: 0x61
DMC_0P9 (ON SEQ2)
IO_1P8 (ON SEQ3)
CPU_1P2 (ON SEQ4)
NPU_0P8 (ON SEQ2)
SYSTEM POWER
High : 2.2MHz
Low : 1MHz
Vin 2.95V ~ 6V
Vo: 3.3V
Io: 2A
I2C
SYSTEM 3.3V Power Enable
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POWER
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