| Submitted by: Ali Asjad (SP22-865-055) LOGICAL ADDRESS VC PHYSICAL ADDRESS: Crenerated by CPU during program execution. This address is part of abstract address space Used by program. Physical ADDRESS: | Date: OS ASSIGN | / |
|--|--|------|
| PHYSICAL ADDRESS: LOGICAL ADDRESS: Crenerated by CPU during program execution. This address is part of abstract address space Used by program. Physical ADDRESS: In the computer memory hardware currectate or instructions reside The Memory Management Unit translate logical address to Physical Address Page 1 11. | Submitted by: Ali Asjad (Sezz-RCS-055) | /// |
| PHYSICAL ADDRESS: LOGICAL ADDRESS: Crenerated by CPU during program execution. This address is part of abstract address space Used by program. Physical ADDRESS: In the computer memory hardware alrested to rinefructions reside The Memory Management Unit translate logical address to Physical Address Page 1 11. | | // |
| PHYSICAL ADDRESS: LOGICAL ADDRESS: Crenerated by CPU during program execution. This address is part of abstract address space Used by program. Physical ADDRESS: In the computer memory hardware alrested to rinefructions reside The Memory Management Unit translate logical address to Physical Address Page 1 11. | | - |
| LOGICAL ADDRESS: Crenerated by CPY during program execution. This address is part of abstract address space Used by program. Physical ADDRESS: The actual location in the computer memory hardware charectata or instructions reside The Memory Management Unit translate logical address to Physical Address Page The | LOGICAL ADDRESS | |
| during program execution. This address is part of abstract address space used by program. Physical ADDRESS: The actual location in the computer memory hardware alware data or instructions reside the Memory Management Unit translate logical address to Physical Address Peace I II. | PHYSICAL ADDRESS | - |
| during program execution. This address is part of abstract address space used by program. Physical ADDRESS: The actual location in the computer memory hardware alware data or instructions reside the Memory Management Unit translate logical address to Physical Address Peace I II. | NOGICAL ADDRESS: | |
| Physical ADDRESS: The actual location in the computer memory hardware charedata or instructions reside The Memory Management Unit translate logical address to Physical Address Page The Co | | |
| Physical ADDRESS: The actual location in the computer memory hardware charedata or instructions reside The Memory Management Unit translate logical address to Physical Address Page The Co | is part of abstract addiess space | |
| in the computer memory hardware where data or instructions reside. The Memory Management Unit translate logical address to Physical Address. Percent III. | Used by program. | |
| in the computer memory hardware where data or instructions reside. The Memory Management Unit translature logical address to Physical Address Page This B | Physical ADDRESS: | |
| Page T. M. B | INC actual location | + |
| Page T. M. B | where data or inchriction a raido | + |
| Page T. M. B | The Memory Management Unit transle | atri |
| Page Table: Monagement scheme, it maps logical addresses to Physical Addresses | logical address to Physical Address | 4 |
| management scheme, it maps logical addresses to Physical Addresses | Paga T. M. | # |
| management scheme, it maps logical addresses to Physical Addresses | Light table. | + |
| addresses to Physical Addresses | management scheme the mans last cont | 4 |
| | additises to Physical Addresses | 1 |

| MIWIPS |
|--|
| byges. byges. |
| pages. |
| Segnent Tables Used in Segmentation |
| Segment Scheme it maps |
| monory management scheme, it maps monory management scheme, it maps segments (variable-sized blocks) to physical segments (variable-sized blocks) to physical addresses. Fach entry in segment table holds the base addresses and length of segment. |
| table holds the base address |
| and length Of orginent. |
| |
| First-Fit Placement: Allocates the first |
| 11 04 11 01 50 10(0) |
| Available Manary bloar that is painted enough to accompadate the requested cize. It is fast but can lead |
| to fragmentation. |
| Rest Fit Placement: Ecorches for smallest |
| available memory block that i's large enough. This can reduce |
| large enough. This can had might wasted space but might be slower & can had to fragmentation |
| overtime. |

| | | | (J) | | The same | The state of the s | Date |
|--------------|-----|---|--|--|----------|--|------|
| and the same | | CONTIGUO | us storage | ALLOCAT ION | | | |
| | | single co to proc and he lead to | ontinuous k lecs. This h is low ov s external | slock of nethod ethead I fragme | e mem | 2000 00 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 | 5 |
| | | NON-CON. | riguous stop | PAGE ALLO | CATION: | - | |
| | | make and redu | to be de memore effect fragme man | itiend Lutation 1 | ace or | f memory | |
| | 13 | | GKB | | 6KB | | 1- |
| | IKB | 13KB | 17118 | | 17KB | -121/0 | + |
| | KB | | LSKB | | LTUB | 13KB | + |
| | CB | | 1416 | 13KB | 1488 | | T |
| 19 | KB | | 19KB | | 1988 | | 1 |
| | | | | | | | 众 |
| | | -inst-ex | | Best-Fit | | Worst-F | |

| | 000000000 | DC |
|---|--|--|
| | - | |
| QH3 | | |
| | and the second s | |
| Toble | | |
| Segment Table | the Spare Par | Lilia |
| 1200-1799 | espair fair | THION |
| AITH THE LOCO | ///// | The same of the sa |
| A AIIIX COLO | | -50 |
| WE HILLIAM | 11111 | 150 |
| S4: Not Allocated 14B | 12. 22. | 550 |
| Sulphan | 1/11/11 | 231 |
| 605B | | 1200 |
| | 111111 | 1805 |
| | | - 3500 |
| | | 3180 |
| GRO | | 3160 |
| | | |
| 2 | | |
| | | |
| External Fragmentation Co | alculation | N |
| | | |
| ·608 (1200-1805) ·(00 (50-150) | | |
| (00 (50-150) | | |
| 14 butes (200-234) | | |
| 680 (2500-3180) | 11 10 | |
| (00 (50-150) 14 butes (200-234) 680 (2500-3180) Sum of free space: 605+10 = 139 | 10+14+08G | 2 |
| = 139 | 9 bytec | |
| | J | |
| | | |

| Total Size of Non Allocated Segment |
|--|
| SI=14KB SI=100KB |
| Su= 961CB |
| Size of non-allocated segment Sizes=14KB+100KB+580KB+96KB |
| Tute (na) Fragmentation / 90/CB |
| So any 80 is allocated and its fits perfectly into partition without leaving any internal fragmentation. |
| (4) |
| Logical Address corresponds to Physical Address |
| 0.080 |
| 2.66 no |
| 3.82 |
| |
| |