No	Test_case_name	Scenario_description	Test procedure	Register Configuration
1	GPIO_pins_reset_cond	Toggling all the GPIO pins to high and reset it and check	1. apply reset and observe reset functionality working or not	
2	Reg_config_check	Checking the register whether they are able to configure or not	1. assert reset after few cock cycles deassert the reset 2. Apply configure values to all registers 3. check the configured results slave side(Ex: paddir = 0 then pin will be output in master and input in slave side)	PADDIR, PADIN, PADOUT, INTEN,INTTYPE0/INTTYPE1, INTSTATUS
3	Interrupt_pin_config	Checking the interrupt pins (peripheral devices connected to GPIO) behaviour with register configuration	1. assert reset after few cock cycles deassert the reset 2. Apply configure values to the interrupt register and check for default interface transaction 3. check the configured results slave side	INTEN,INTTYPE0/INTTYPE1, INTSTATUS
4	Config_change_with_interrupt	Changing the register configuration during the interrupt.	1. assert reset after few clock cycles deassert reset	

2. check selected interface transactions by giving selected interface value for interrupt register 3. In middle of tranasaction change all the register values as configured values and make sure configuration happens or not in the middle of transaction	PADDIR,PADIN, PADOUT, INTEN,INTTYPE0/INTTYPE1, INTSTATUS