

| Sl No | Test_case_name |
|-------|----------------------------|
| 1 | GPIO_pins_reset_cond |
| 2 | Reg_config_check |
| 3 | Interrupt_pin_config |
| 4 | Config_change_with_interru |

Scenario_description

Toggling all the GPIO pins to high and reset it and check

Checking the register whether they are able to configure or not

Checking the interrupt pins (peripheral devices connected to GPIO) behaviour with register configuration

Changing the register configuration during the interrupt. False scenario