Bit Position			7	6	5	4	3	2	1	0	Related Instruction
SRC Address			R[n] (Output at X2)				R[n+1] (Output at X3)				
Access Target	ROM (4001)	I/O Port	Chip Selection				don't care				RDR WRR
	RAM (4002)	Out Port	Chip Selection in Bank	don't care							WMP
		Main Memory Character	Chip Selection in Bank	Register Selection in Chip		Character Selection in Register					RDM, WRM ADM, SBM
		Status Character	Chip Selection in Bank	Register Selection in Chip		don't care					RD0, RD1, RD2, RD3 WR0, WR1, WR2, WR3