

COE/F13/JUL.08

Guru Gobind Singh Indraprastha University, Delhi

7200155

CONSOLIDATED STATEMENT OF MARKS BACHELOR OF TECHNOLOGY (COMPUTER SCIENCE AND ENGINEERING)

NAME ENROLLMENT: MUNISH KUMAR SHARMA 00516401209

FATHER'S NAME

TEJPAL SHARMA

YEAR OF ADMISSION:

TOTAL CREDIT OF PROGRAMME: 210
MINIMUM CREDITS REQUIRED: 200

YEAR OF COMPLETION: PROGRAMME DURATION: MAY, 2013 00 SEMESTERS



| 300 | PAPER | C3 | INT | EXT | TOTAL | CODE | PATER | G | INT | EXT | ŀ |
|--------|---|----------|-----|------|----------|----------|--|--|--------------|-------------|----|
| | | _ | | | FIRST SI | MESTER | | | _ | | _ |
| | COMMUNICATION SKILLS - 1 | , | 24 | 30 | 88 | BA103 | CHEMISTRY I | 1, | 34 | 10 | Γ |
| T106 | MTRODUCTION TO COMPUTERS | , | 38 | 47 | 16 | IT107 | ELECTRICAL SCIENCE | 14 | 29 | 40 | Γ |
| A189 | MATHEMATICS I | 1 | 36 | 31 | 87 | BA111 | PHYSICS I | 11 | 23 | 46 | T |
| 5110 | EMPACT OF SCIENCE & TECHNOLOGY ON SOCIETY - I | 1 | - | 62 | 62 | DA181 | CHEMISTRY I LAB | 11 | 33 | 47 | Ť |
| AIR | PHYSICS I LAB | • | 35 | 43 | 70 | 1T166 | COMPUTER LAB | 11 | 26 | 51 | Ť |
| 1187 | ENGINEERING GRAPHICS ! | 1 | 30 | 36 | . 44 | IT169 | ELECTRICAL SCIENCE LAB | 1 | 24 | 49 | Ť |
| | | | | | SECOND | EMESTER | | | | | _ |
| 5102 | COMMUNICATION SKILLS - II | Ŀ | 20 | 48 | 73 | IT104 | ENGINEERING MECHANICS | 14 | 34 | 62 | T |
| A101 | MATHEMATICS - I | 1 4 | 39 | 57 | 14 | BA110 | PHYSICS - B | 1 | 24 | 42 | Ť |
| A114 | STATES THEORY OF PROBABILITY AND LINEAR PROGRAMMING | , | 35 | 81 | 14 | BA118 | CHEMISTRY - 11 | 1 | Ħ | 48 | Ì |
| 3126 | IMPACT OF SCIENCE & TECHNOLOGY ON SOCIETY - II | + | - | 78 | 78 | 17128 | DATA STRUCTURE | 1,1 | × | ត | Т |
| T162 | DATA STRUCTURE LAB | 1 | 11 | 47 | 80 | IT184 | ENGINEERING GRAPHICS - II LAD | +++ | 77 | 12 | ÷ |
| AIS | PHYRICS II LAS | 1 | 36 | 40 | 45 | BA162 | CHEMISTRY - H LAB | +++ | 18 | 67 | ÷ |
| | | | | | THIRD S | EMESTER | | | | <u></u> | _ |
| TZ# | COMPUTATIONAL METHODS | 14 | 37 | 42 | 71 | IT203 | CIRCUITS AND SYSTEMS | 141 | 31 | 44 | T |
| TZQS | BLECTRONIC DEVICES AND CIRCUITS | 1 | 30 | 58 | 80 | 17207 | OBJECT ORIENTED PROGRAMMING USING C++ | \rightarrow | 32 | 4 | 肀 |
| L3N | COMPUTER GRAPHICS | 1 | 33 | 57 | 100 | T211 | DATABASE MANAGEMENT SYSTEMS | \rightarrow | 24 | 4.0 | ÷ |
| T251 | BLECTRONIC DEVICES AND CIRCUITS LAB | 11 | 32 | 48 | 71 | IT253 | COMPUTATION LAB | +++ | 34 | 54 | ÷ |
| T256 | DBJECT DRIENTED PROGRAMMING LAB | 1 | 35 | 80 | 85 | 17257 | COMPUTER GRAPHICS LAB. | 11 | 23 | 47 | ÷ |
| TZS9 | DEVISIAE | 1 | 35 | 42 | 77 | 1,,,,,, | | | | | |
| | | | | | | SEMESTER | | | | | _ |
| Tzez | JAVA PROGRAMMING | 1 | 30 | 52 | 12 | 1T204 | MULTIMEDIA APPLICATIONS | 7.7 | 30 | 4.5 | T |
| T294 | SWITCHING THEORY AND LOGIC DESIGN | 1 | 31 | 45 | 76 | M3201 | ORGANIZATION BEHAVIOUR | - | 26 | 4 | ÷ |
| T210 | FOUNDATIONS OF COMPUTER SCIENCE | 14 | 38 | 47 | 13 | IT212 | SOF WARE ENGINEERING | 11 | 24 | 41 | ÷ |
| T252 | JAYA PROGRAMMING LAB. | 1 | 27 | 45 | 72 | 17254 | VULTIME 'IA LI B | \rightarrow | | 45 | ÷ |
| 1725 6 | EWITCHENS THEORY AND LOGIC DESIGN LAB | 十 | | , 49 | 1 12 | 17258 | SOFTWA - SUGINEE, NO Los | | 37 | 60 | ÷ |
| | | | | | FIFTH S | EMESTER | | | - | - | 4 |
| 10671 | THE DRY OF COMPUTATION | 14 | 2.7 | 32 | 54 | IT303 | ANALOG AND DIGITAL COMMUNICATION | 1.1 | 10 | 4 | 1 |
| 17205 | COMPUTER ARCHITECTURE | 4 | 32 | 45 | 77 | 17367 | DIGITAL SIGNAL PROCESSING | 141 | 33 | - | ÷ |
| IT361 | OB. ECT OR ENTED BOFTWARE ENGINEERING | 1. | 28 | 47 | 75 | 17311 | DIGITAL DESIGN USING VHDL | 11 | 79 | 32 | 卡 |
| (T251 | ANALOG & CIGITAL COMMUNICATION LAB. | 1 | 32 | 42 | 74 | ITSES | DIGITAL SIGNAL PROCESSING LAB. | | ** | 1 47 | 누 |
| 17266 | DIGITAL DESIGN LAB. | 11 | 36 | 51 | 110 | 17357 | SUMMER TRAINING REPORT, SEMINAR AND THE | - | ÷= | | ᆉ |
| | | | | | | | VOCE | - | - | 1 | ٠ |
| | | _ | - | | SIXTHS | EMESTER | | - AND SECTION OF THE PERSON OF | | - | _ |
| IT302 | MICROPPOCESSORS | 4 | 36 | 42 | 17 | 1T304 | COMPUTER NETWORKS | -11 | i | J | _T |
| IT301 | ALGORITHM ANALYSIS AND DESIGN | 4 | 29 | 44 | 17 | IT308 | COMPILER DESIGN | | - | Ţ. | Ĭ. |
| 11315 | OPERATING BYSTEMS | 1 | 30 | 31 | 41 | IT382 | MICROPROCESSOR LAB. COMPILER DESIGN LAB | | | | |
| IT354 | ALGORITHM AMALYSIS & DESIGN LAB. | 2 | 32 | 48 | 77 | 17358 | COMPILER DESIGN LAB | | | 1 | |
| JT401 | ADVANCED COMMITTED HE PROPERTY | _ | _ | _ | | SEMESTER | | | المراجعة الم | ، دمجد | _ |
| 17405 | ADVANCED COMPUTER METWORKS DISTRIBUTED SYSTEMS | 1. | 28 | 63 | 11 | 17403 | BOFTWARE TESTING | 13 | N. | -61 | I |
| ., | CONTRIBUTED SYSTEMS | 1. | 32 | 41 | 73 | 17413 | PRONT END DESIGN TOOLS AND WEB | 1-1. | -4 | 1 4 | I |
| éT451 | ACN LAB. | 1 | 20 | 40 | 60 | 1*465 | LABORATORY WORK FOR ELECTIVES LAB. | - - | 34 | 7 44 | Т |
| 17457 | NEWOR PROJECT LAB. | 1 | 31 | 39 | 70 | 17489 | SUMMER TRAINING REPORT, SEMINAR AND | 11 | = | | = |
| AT441 | SOFTWARE TESTING LAB. | 1 | 29 | 40 | T | | VVA-VOCE | | | | _ |
| | | <u> </u> | | | | EM2STER | | | | | |
| HS402 | TECHNICAL WINTING | 1 2 | - | " | 1 84 | 17498 | ADVANCED DATABASE MANAGEMENT BYSTEMS | T. T | - | T | _ |
| IT414 | WINDOWS 'ET FRAMENORK AND CO / | | 22 | -44 | 78 | 1 17482 | MAJOR PROJECT(REPORT) LAB. | - ; | 35 | 12 | ⇌ |
| 17111 | PROGRAMMING | 二 | | _ | | | | | | | _ |
| IT454 | VIVA-VOCE (ON MAJOR PROJECT) LAB. | +- | 34 | 62 | 16 | 17488 | SEIMINAR AND PROGRESS REPORT LAB. | 11 | Ξ | AS | J |
| 1T458 | LABORATORY WORK FOR ELECTIVES LAB. | 1 1 | 14 | 63 | 77 | | | | _ | | |

Date of Print:

*: With Grace marks Ab: Absent INT: Internal EXT: External

CS: Credit Secured

Place: Delhi, India

Ondhiea Officer In-charge

Controller of Examinations