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Section: 61-J

Answer to the question no-1

a) (i)	Allocation	Max need	Connected	Available
Process	ABCD	ABCD	ABCD	ABCD
·P,	0230	1450	1220	7 5 8 10
P2	3101	5202	2101	77110
Ps	10 10	1212	0202	1081211
Py	0001	0101	0100	11 8 12 12
P5	2110	3231	1121	1181212
Pa	1100	2211	1111	139 13 12
	7552			14101312

Given that,

$$A = 124$$
 | $C = 13$
 $B = 10$ | $D = 12$

safe sequence > P2 > P2 > P3 - P4 > P5 > P6

· Not sofe, request exceeds need (Po)

```
For Pi: Cheek if request & need (Pi)
(0,1,0,0) & (2,1,01) safe
```

Arranting two trequest:

1) available update: (7,4,8,10)

2) exploited allocation: (3,2,0,1)

3) Needed update: (2,0,0,1)

so, Po can not be granted but By can be granted.

b) Herre, Available resources:

Ra (Laptop) +3; R2 (Rowteres) -> 2; R3 (Hourd Drives) >2

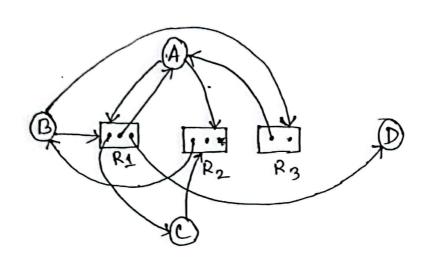
Now,

Team A: Allocation: 1 Laptop, 1 hard drive Request: 1 Laptop, 1 Routen

Team B: Allocation: 1 Rowlett

Request: 1 Laptop, 1 hard drive

Team c: Allocation: 1 Laptop Request: None : Resource Allocation Proph:



(C) - The resource Allocation graph represents sofe state:

 $P_2 \rightarrow P_3 \rightarrow P_1 \rightarrow P_4 \rightarrow P_5 \rightarrow P_6$

-As there is no cycle iso each resource has enough instance to satisfy the requests. Here P2 can finish because it has two requests, where the P3 need Ry, which can be finish as well. Aftern that P, can finish and then Py also. Ps can finish because R2 is available. Then Po also can finish at the last and all the processes are executed.

Answer to the question no-2

-Dynamic portitioning Scheme:

- Memory is allocated dynamically which is based on process requests and often that creating postitions at required Sizeq.

Advantages: 1. Flexible for process of different sizes.

2. Efficient memory utilization.

Disadvantages!

- 1. Overhead in managing memory allocation.
- 2. External frequentation,
- b) The resource behind the loggy issue in penformance described below:
- 1. Memory Leaks; The process fail to nelease memony.
- 2. Insufficient memory: i) Excessive disk shows the system.
 - 11) The system spends to much time in Swapping

3. Memorry Fragmentation: Non-configuous blocks prevent longer allocation.

-solutions for penformance improvement:

1. Penform memory compaction to reduce 2. Detect and fix memory teaks.

3. uses effective memorry allocation Algorithm

Answer to the question mo-B

Physical memory: 3. frame Aiven that,

Ref: 1,2,3,4,1,25,1,2,3,4,5,6,2,1,3,7,0,9 LRU:

	-	_	,	,	-	_	_	-	-												
Q.	1	2	3	4	1	2	5	1	2	3	4	5	6	2	1	3	7	7	0	0	
f1	1	1	1	4	4	7	5	5	3	3	3	3	6	6	8	3	3	3	3	9	-
f_2		2	2	2	1	1	1	1	1	1	4	4	4	2	2	2	7	7	7	7	-
£3	_	_	3	3	3	2	2	2	2_	2	2	5	5	5	1	1	1	1	0	0	
	F	F	FI	F	F	F	F	H	Н	F	F	F.	E.	F	F	F	F	-11:	.F.	E	

Hit reutio = 3 x 100 % = 15%.

Fall Ratio = 17 × 100 = 85%.

optimal:

Ŀ	2	3	14	1	2	5	1	2	3	4	5	1	2	Ţī	3	17	17	0	19
	1	1	1	1	1	1	1	1	3	4	4	4	4	4	3	3	3	3	19
	2	2	2_	2		170		2		- 1		E 5					1	0	0
		3	4	4	4	5	5	5	5	5	5	4	6	6	6	7	7	7	7

Hit radio = 70 × 100 = 35%.

Foult natio = 130 ×100 = 65%

so, optimal is more efficient.

- 6)-Stratigies to improve LRU efficiency:
 - 1. Second chance Algorithm.
 - 2. clock Algorithm
 - 3. Adaptive Algorithm.
- -Stratigies to improve optimal Algorithm
 efficiency:

 farrecost future "pag-

its motivates to forecast future page uses despite the fact that they were improveticable. Practical constraints and Trade off:

a. Hardware support

6. Complexity vs pertformance

c. prediction challenges

In conclusion page replacement refliciency can be increased by utilizing optimals principles and improving LRU.