

Errata File (September 2013)  
Computer Organization and Architecture, Ninth Edition  
William Stallings  
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-----SYMBOLS USED-----

| ti = ith line from top; bi = ith line from bottom; Fi = Figure i |  
| X -> Y = replace X with Y; Ti = Table i; Pi = Problem i |

----- September LIST -----

PAGE      CORRECTION

168 F5.5: Abbreviation for Memory address register on left should be MAR

207 b5: SDD -> SSD

209 t5: SDD -> SSD

----- February LIST -----

127 b5: 2w -> 2<sup>w</sup>  
b3: 2r -> 2<sup>r</sup>

132 t5: 2w -> 2<sup>w</sup>

174 t2-3: with a Hamming code of 0111 -> with a Hamming code of 0001

311 T9.1 3rd row, 3rd column: 10<sup>0</sup>  
T9.2 1st row, 5th column: 1

373 Equation 11.3 should be  $F = \overline{A}B + B\overline{C}$

395 t16 - t23, 4 times: A, B, and C -> C, B, and A  
t17: least significant -> most significant

396 t2: interchange A and C

397 t3: A, B, and C -> C, B, and A

466 t8: 10.3 -> 12.3

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A current version of this file, named Errata-COA9e-mmyy.pdf,  
is available at <https://www.box.com/shared/r7evm4jr0d>  
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