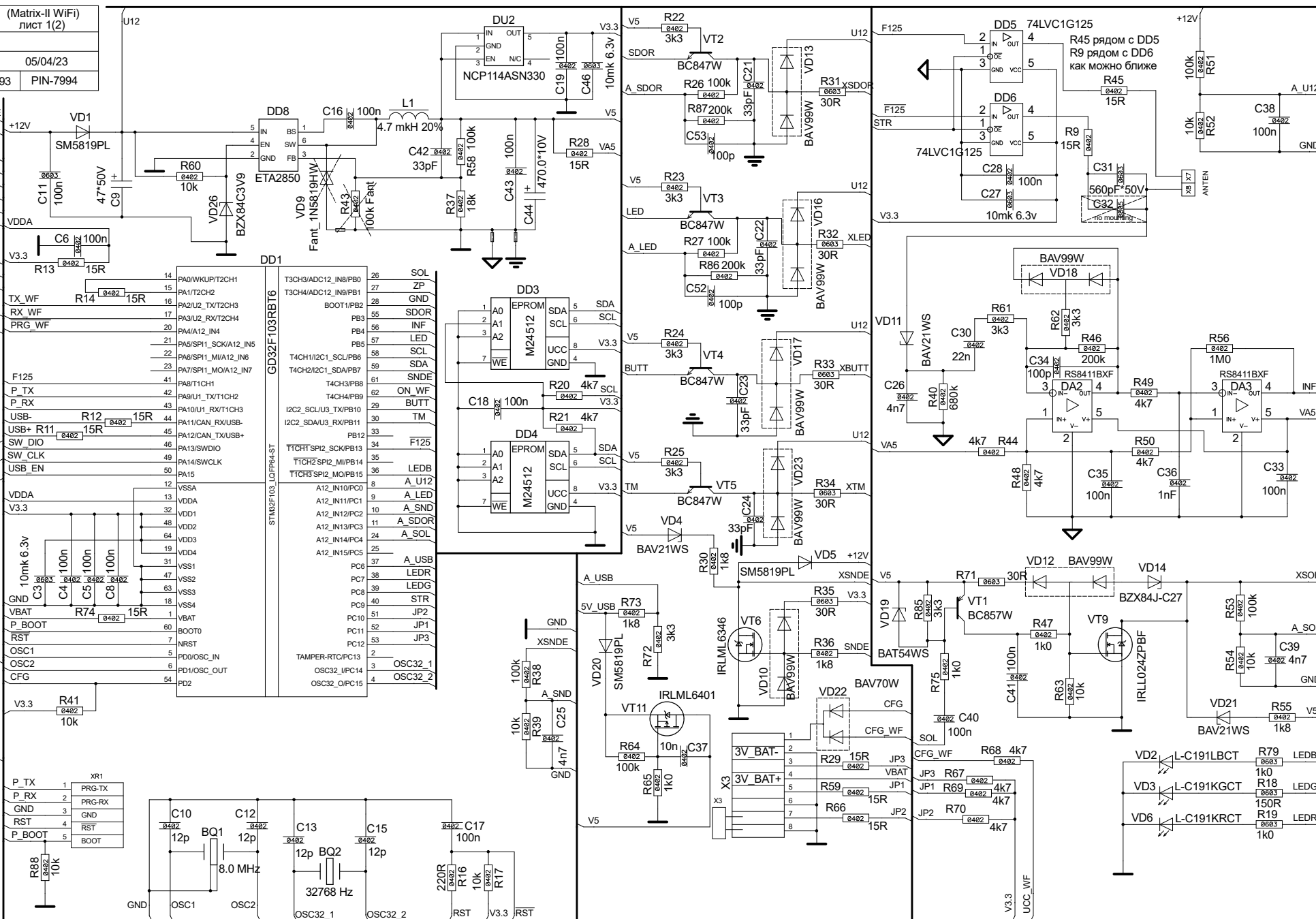


The circuit diagram shows a voltage doubler configuration. A BC847W NPN transistor is connected with its base to ground and its emitter to the output ZP. The collector of the transistor is connected to the cathode of a diode KPX-G1205. The anode of KPX-G1205 is connected to the anode of another diode BAV21WS. The cathode of BAV21WS is connected to the positive supply V5. A resistor R57, labeled with a value of 8482 and a tolerance of 1k8, is connected between the collector of the transistor and the output ZP.



Наименование	PIN-104 (Matrix-II WiFi) лист 2(2)	
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