Eric Murphy

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| City, State ZIP | LinkedIn | Email |

EDUCATION

Purdue Univerity, West Lafayette, IN
Bachelor of Science in Computer Engineering — May 2016
3.94/4.00

Relevant Coursework

ASIC Design Lab; Data Structures; Intro to Computer Security, Operating Systems
Microprocessor System Design and Interfacing; Compilers; Computer Architecture

EXPERIENCE

DellEMC, *Software Engineer II* — July 2016 – Present
Santa Clara, CA

Qualcomm, *Software Engineering Intern* — May 2015 – August 2015
Designed a testing framework to verify VoLTE and VoWiFi carrier requirements — San Diego, CA
Verified Verizon's carrier-specific requirements through my framework
Progressed to the semi-final round of IdeaQuest, an intern project competition

Hewlett-Packard, *Networking Intern* — May 2014 – May 2015
Developed an SDN application that monitors network flows — Palo Alto, CA / West Lafayette, IN
Participated in the Purdue Pathmaker Program as a student intern

PROJECTS

Triple DES, *Team Member* — Spring 2015
Constructed a Triple DES implementation using SystemVerilog
Created an AHB-Lite Interface for use in an ASIC design

LED^3, *Team Member* — Fall 2014
Developed a cube comprised of 512 LEDs
Designed a printed circuit board (PCB) to route electrical components

Altera DE1-SoC FPGA, *Individual* — Summer 2014
Expanded my Verilog and Quartus II software knowledge
Completed Digital Logic labs through Altera's University Program

ACTIVITIES

BoilerMake, *Executive Board Member* — Spring 2015 – Fall 2015
Planned Purdue's hackathon (BoilerMake) for Fall 2015
Improved upon the existing BoilerMake SBC design with a team

ECE Student Society, *Mentor* — Fall 2013 – Spring 2015
Provided advice related to coursework and employment to three mentees
Networked with ECE students and professors at social and professional events

SKILLS

C (Proficient), Python (Familiar), Java (Familiar), SystemVerilog (Familiar), C++ (Familiar)
GDB, Git, LaTeX, MATLAB, ModelSim, Vim