GROUP 08 REVIEW:
EEE3088F Week 6

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# PEER REVIEW OF PCB DISCOVERY HAT

## 1. Adherence TO Mr. Pead's basic requirements

| Unacceptable<br>1 | Ok attempt<br>2 | Reasonable<br>3 | Good<br>4 | Excellent<br>5 |
|---|---|---|---|---|
| | | | √ | |
| Comment: No 18650 connector. No test points in the power module or sensing module and no 0-ohm resistors to allow for bypassing. Everything else is present. | | | | |

## 2. Adherence TO Mr. Pead's debugger requirements

| Unacceptable<br>1 | Ok attempt<br>2 | Reasonable<br>3 | Good<br>4 | Excellent<br>5 |
|---|---|---|---|---|
| | | | | √ |
| Comment: | | | | |

## 3. Schematic

| Unacceptable<br>1 | Ok attempt<br>2 | Reasonable<br>3 | Good<br>4 | Excellent<br>5 |
|---|---|---|---|---|
| | | | | √ |
| Comment: The schematics are neat and easy to follow with good annotations. | | | | |

## 4. PCB Layout

| Unacceptable<br>1 | Ok attempt<br>2 | Reasonable<br>3 | Good<br>4 | Excellent<br>5 |
|---|---|---|---|---|
| | | | √ | |
| Comment: Traces seem to be too thin eg R8-IC1 otherwise the layout of the board is good. | | | | |

## 5. Silk Screen

| Unacceptable<br>1 | Ok attempt<br>2 | Reasonable<br>3 | Good<br>4 | Excellent<br>5 |
|---|---|---|---|---|
| | √ | | | |
| Comment: Components are labeled but there is no version number, design name, or module names; difficult to see what is what. | | | | |

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## 6. Low voltage protection circuit

| Unacceptable<br>1 | Ok attempt<br>2 | Reasonable<br>3 | Good<br>4 | Excellent<br>5 |
|---|---|---|---|---|
| | | | | √ |
| Comment: | | | | |

## 7. Physical board design

| Unacceptable<br>1 | Ok attempt<br>2 | Reasonable<br>3 | Good<br>4 | Excellent<br>5 |
|---|---|---|---|---|
| | | | | √ |
| Comment: | | | | |

## 8. Test points and recovery approaches

| Unacceptable<br>1 | Ok attempt<br>2 | Reasonable<br>3 | Good<br>4 | Excellent<br>5 |
|---|---|---|---|---|
| | | √ | | |
| Comment: Not enough test points. More test points could be added to the digital sensors SCL , and SDA line to test for data transfer; more test points in the power system for bypassing in case module does not work and to check if voltages are correct . | | | | |