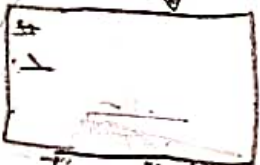
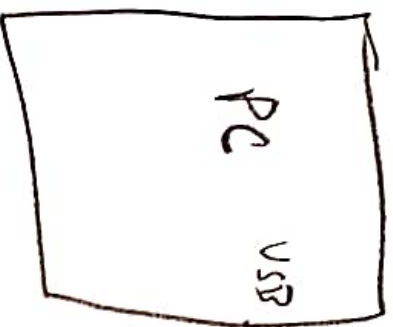


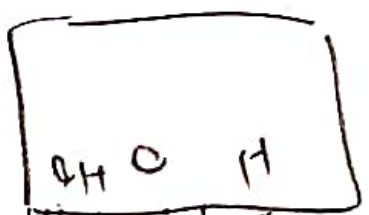
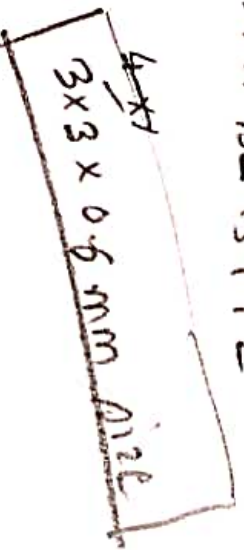
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Electrical Blk diag.



- 1) STM8L101F3
- 2) 93

3) STM BL 15-1 F-2



C; or ASM. - Programming

4x4 x 1 mm CPU size

8-12 I/O.

2 timers

E²PRom - 128 byte

RAM - 2K

Flash Prog - 16K. 12K Assembly

S/W

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A

- ZigBee: Type (skipped)
- listen decide Tx - Rx. (default)

- Try implement heartbeat: [dev. ^{I; 0} like no by 1.]

B

- Managing timers - 2ms

C

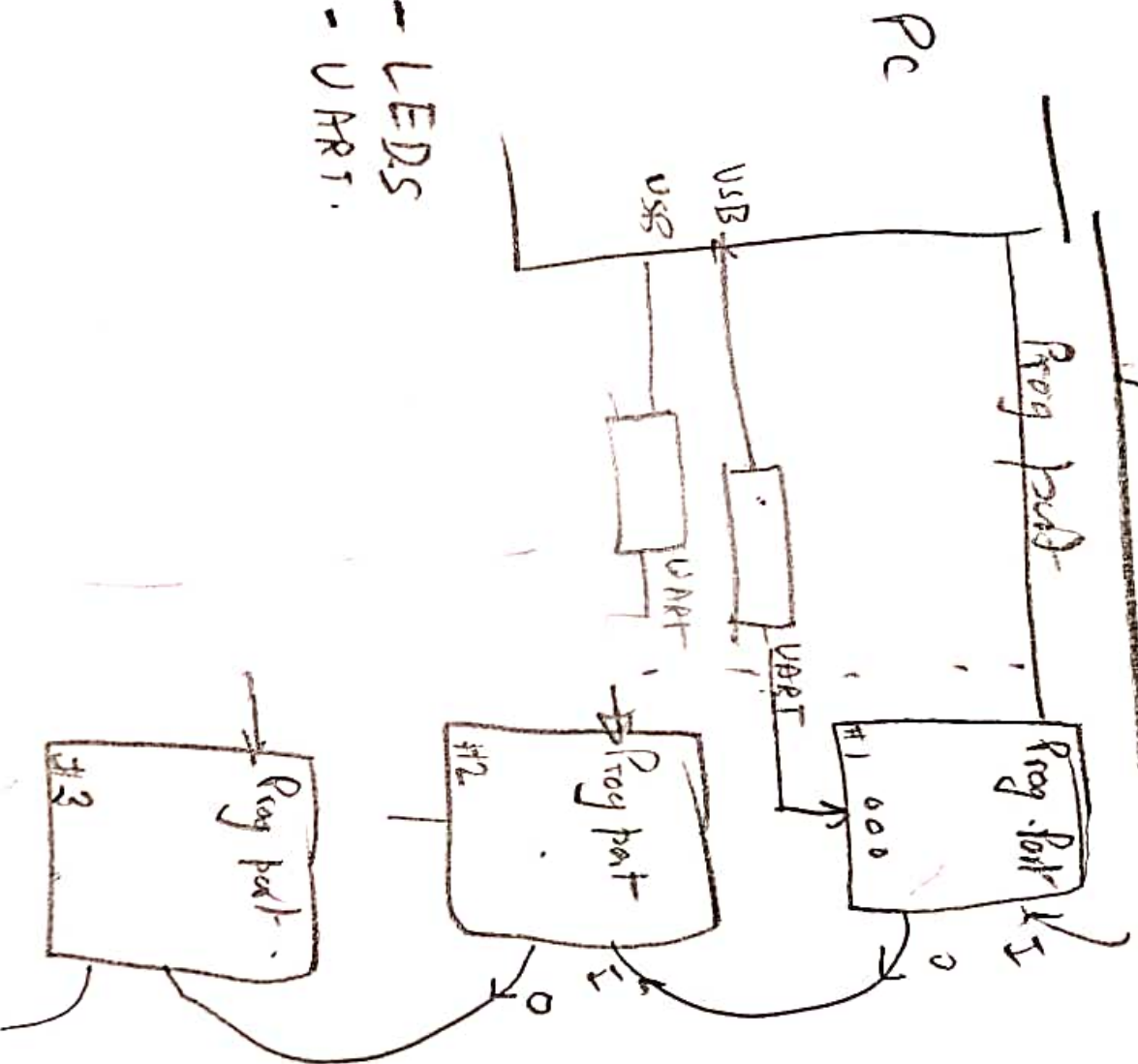
- E² Prom - UTD Dev-ID. - (to Master)

Programming

19/1/24.

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Debug - LEDs
- UART.



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Actions

1. kit.
2. S/wc required.
3. H/w required (P.C.)
4. USB \rightarrow sr. + P.S. . . . set of 2.
5. wiring ; $\frac{1}{2}$; etc.
6. Bread board.
7. Multimeter
8. USB splitter 184