

ASSIGNMENT

 \mathbf{ON}

"VLSI"

Course code: EEE- 4128

Submitted To,

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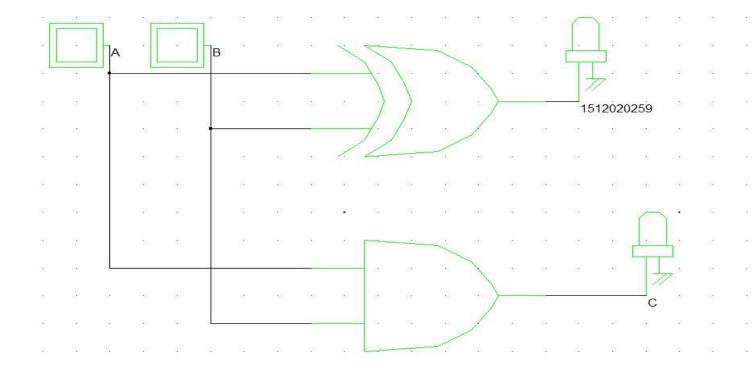


Fig: Half Adder

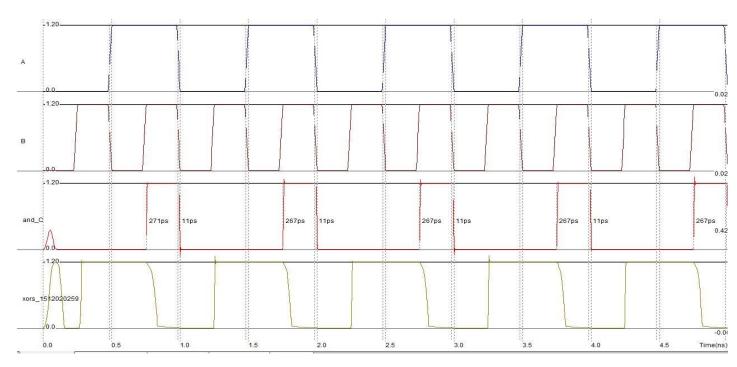


Fig: Half Adder Output

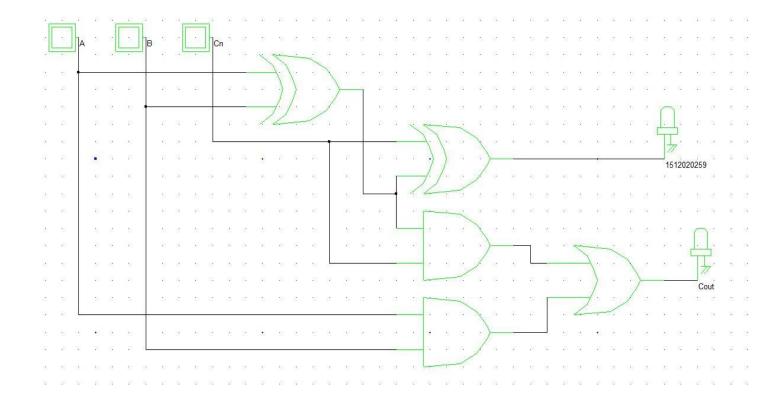


Fig: Full Adder

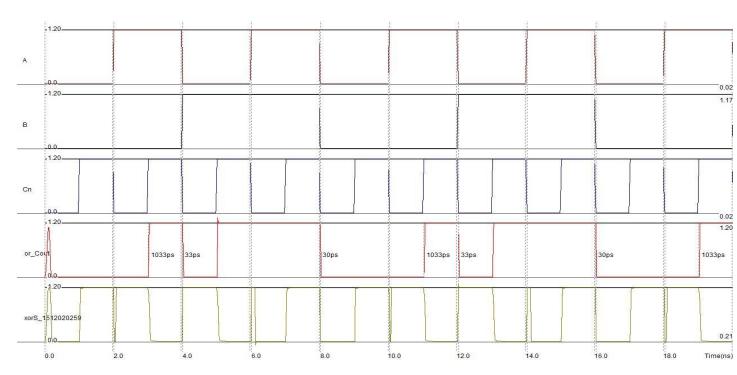
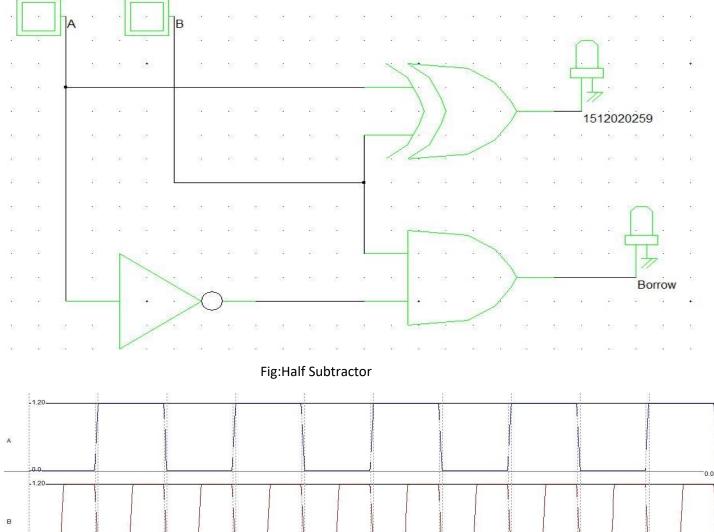
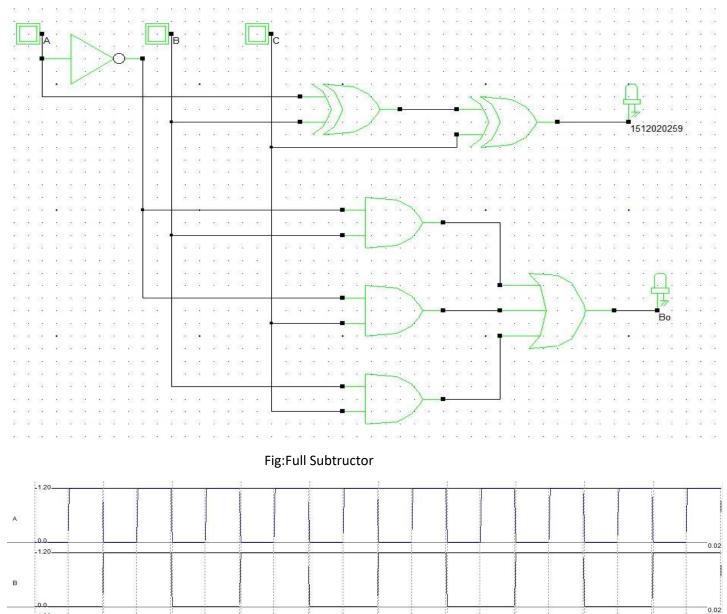


Fig:Full Adder Output



120 0.02
120 120 0.02
120 0.02
120 0.02
120 0.02
120 0.02
120 0.00
0.01 120 0.00
0.00 0.5 1.0 1.5 2.0 2.5 3.0 3.5 4.0 4.5 Time(ns)

Fig: Half Subtructor Output



1.17 С or_Bo 0.00 115ps 117ps 107ps 20ps 116ps 115ps 85ps 106ps 19ps 18ps 84ps 107ps 0.0 2.0 4.0 12.0 14.0 16.0 Time(ns)

Fig:Full Subtractor Output