



LEADING UNIVERSITY

ASSIGNMENT

ON

“VLSI”

Course code: EEE- 4128

Submitted To,

Puja Rani Saha

Guest Teacher,

Department of CSE,

Leading University, Sylhet

Submitted By,

Name: - Musa ibn Ashraf

ID No: - 1512020259

Sec: 10C(F)

Department of CSE

Leading University , Sylhet

Submission Date: 11. 04. 2018

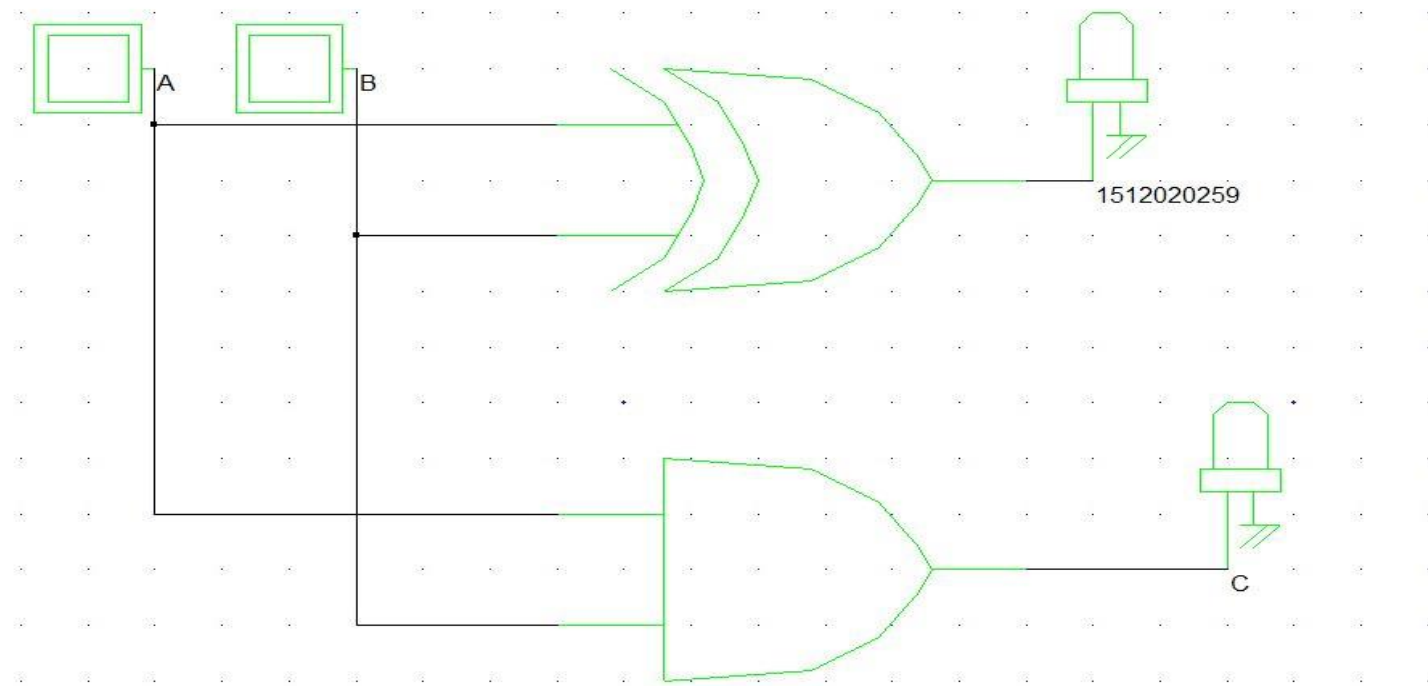


Fig: Half Adder

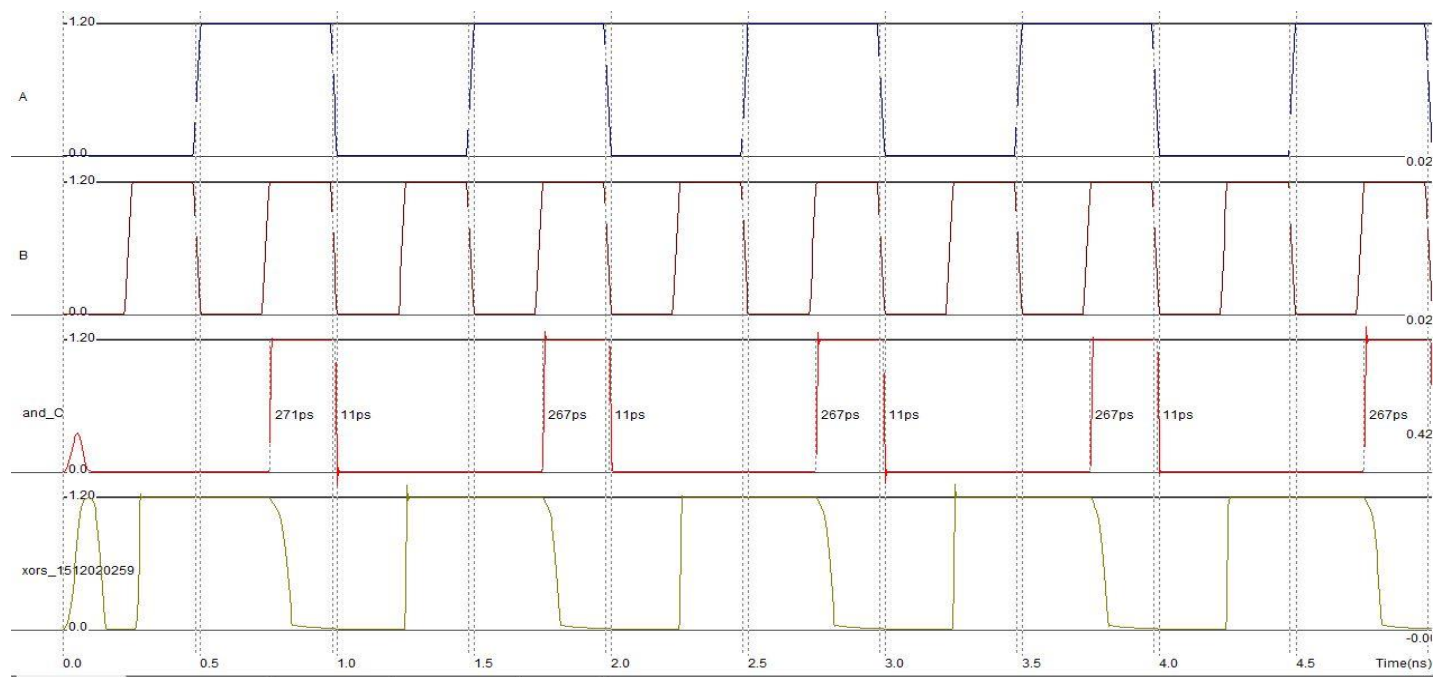


Fig: Half Adder Output

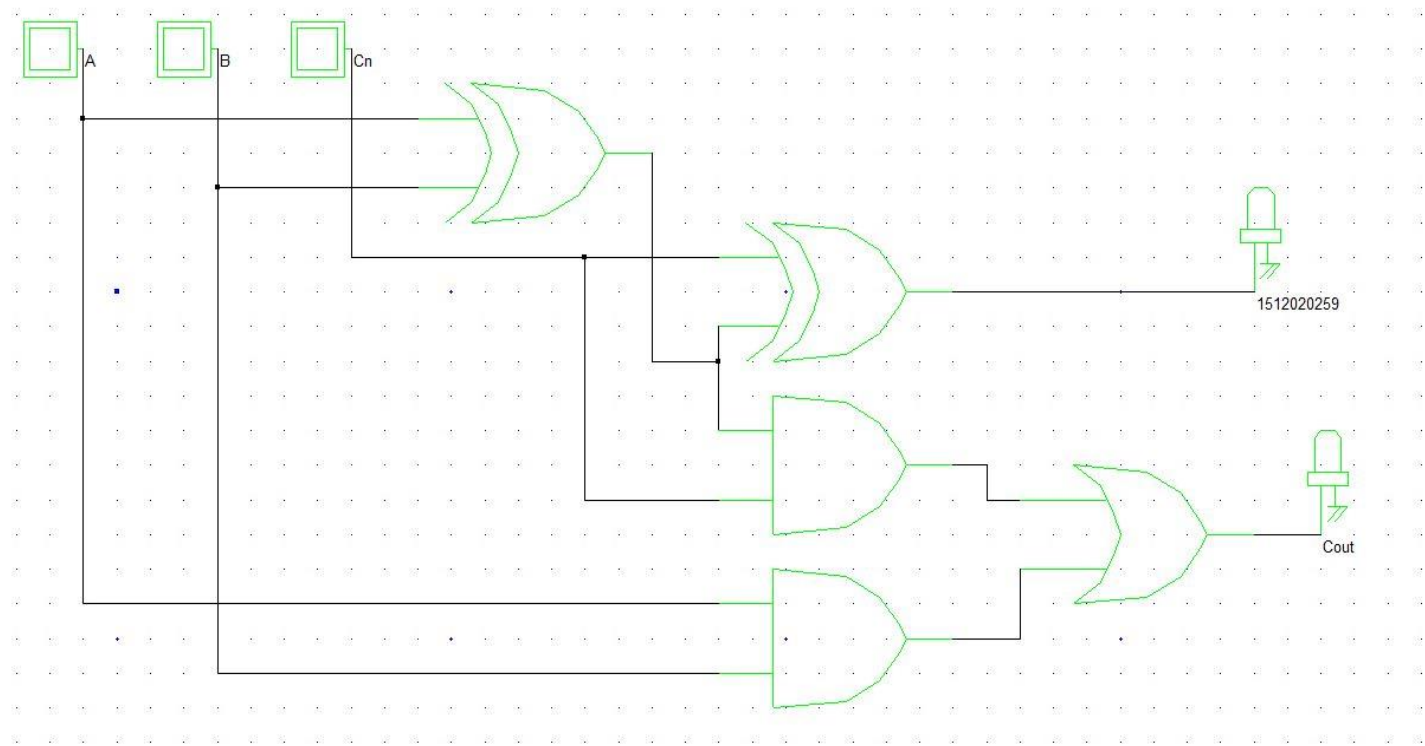


Fig: Full Adder

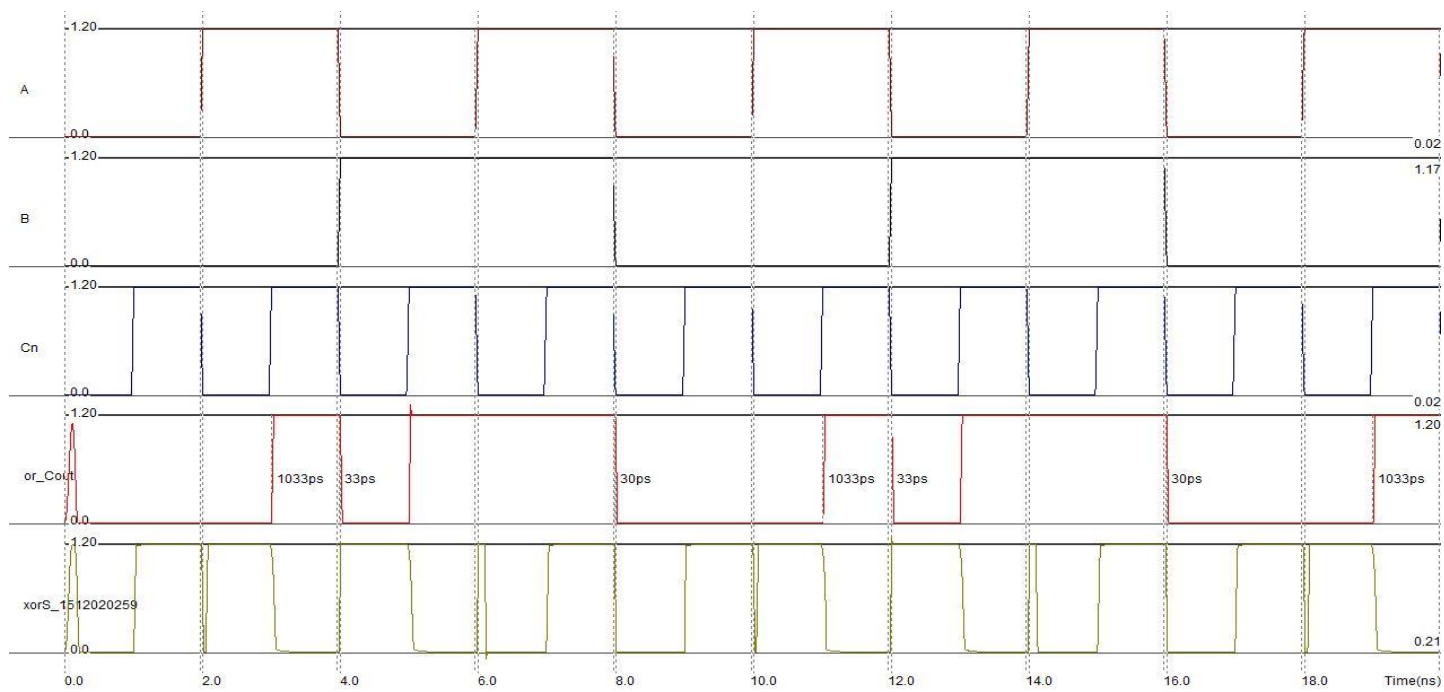


Fig:Full Adder Output

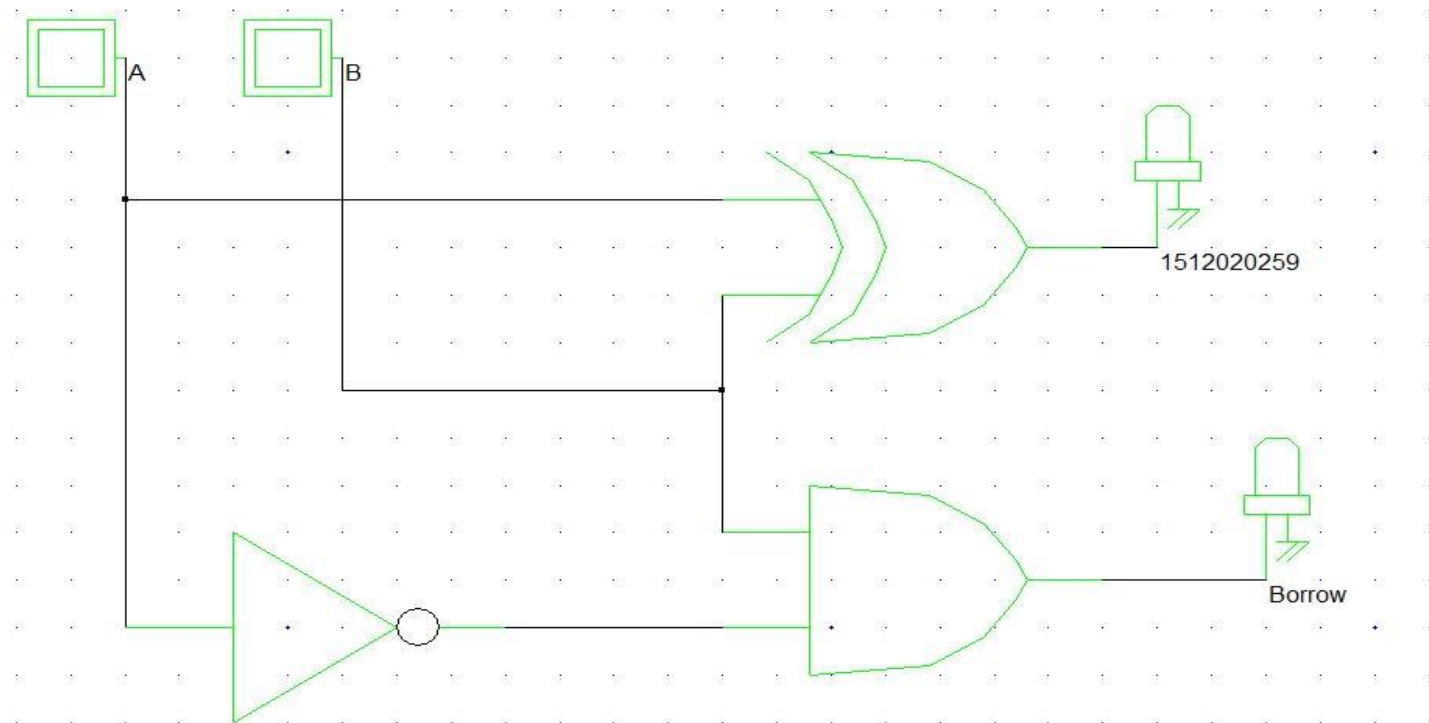


Fig:Half Subtractor

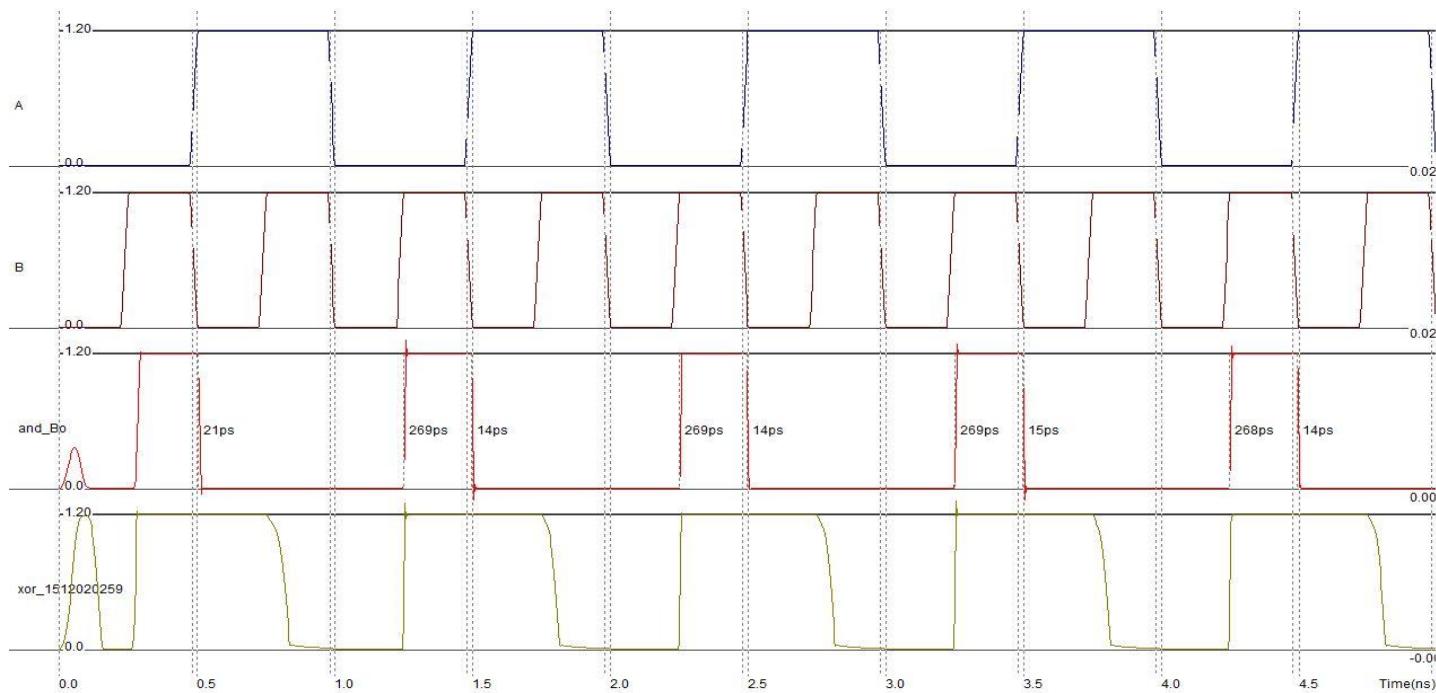


Fig: Half Subtractor Output

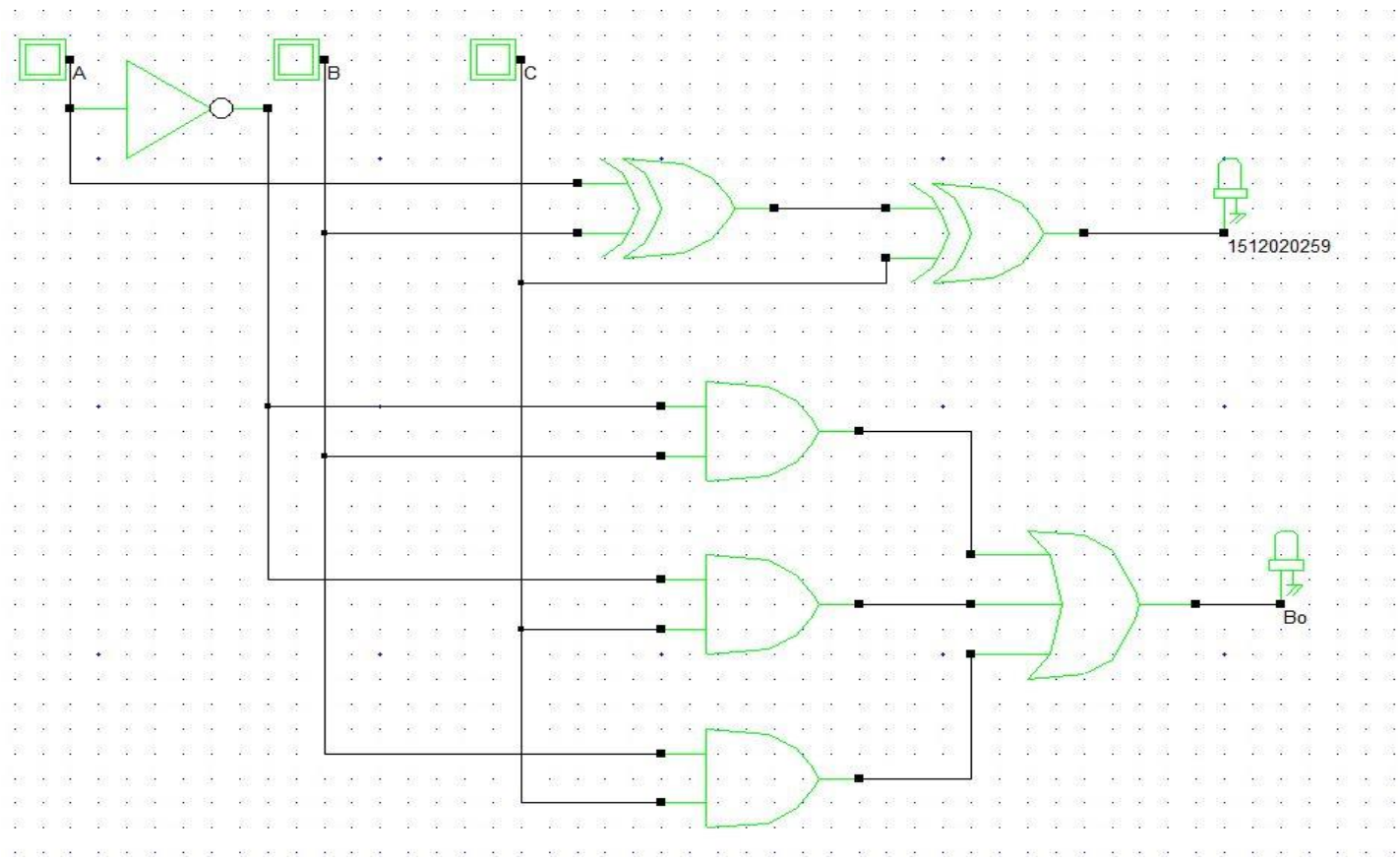


Fig:Full Subtractor

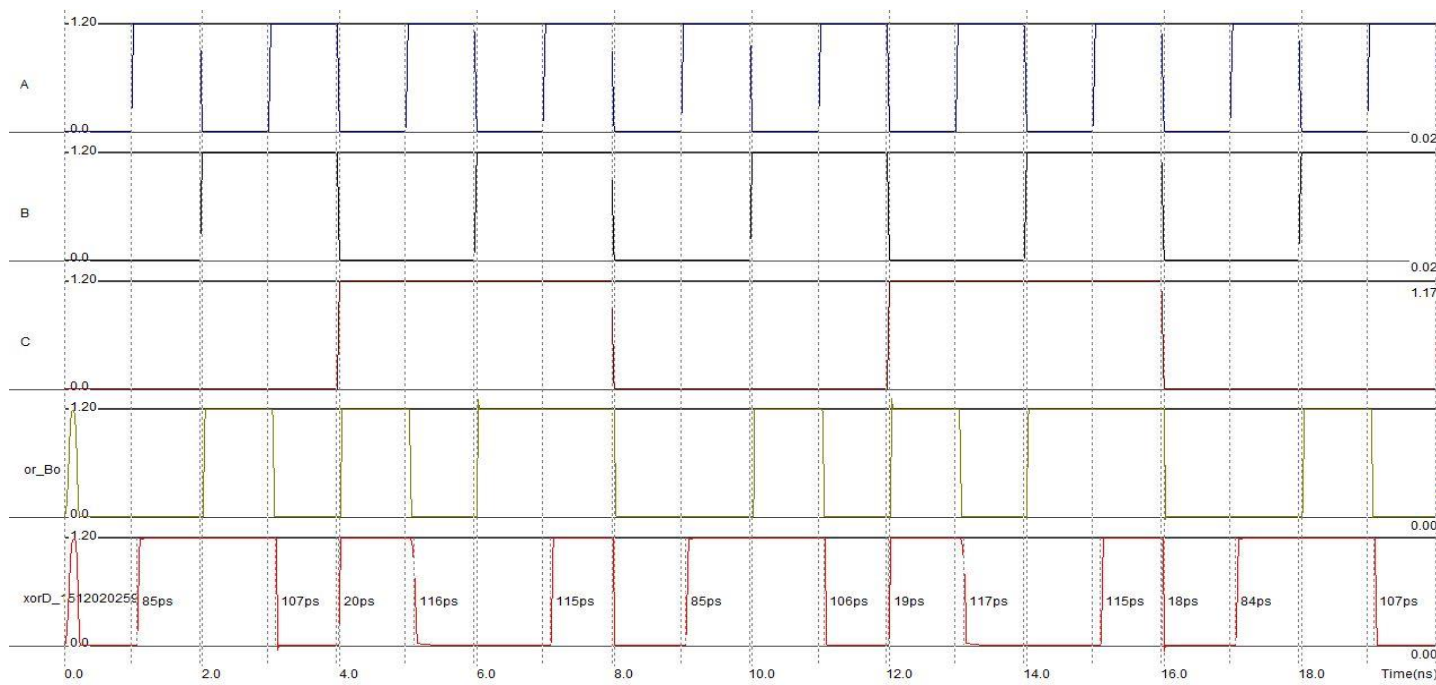


Fig:Full Subtractor Output