

1

2

3

4

A

A

B

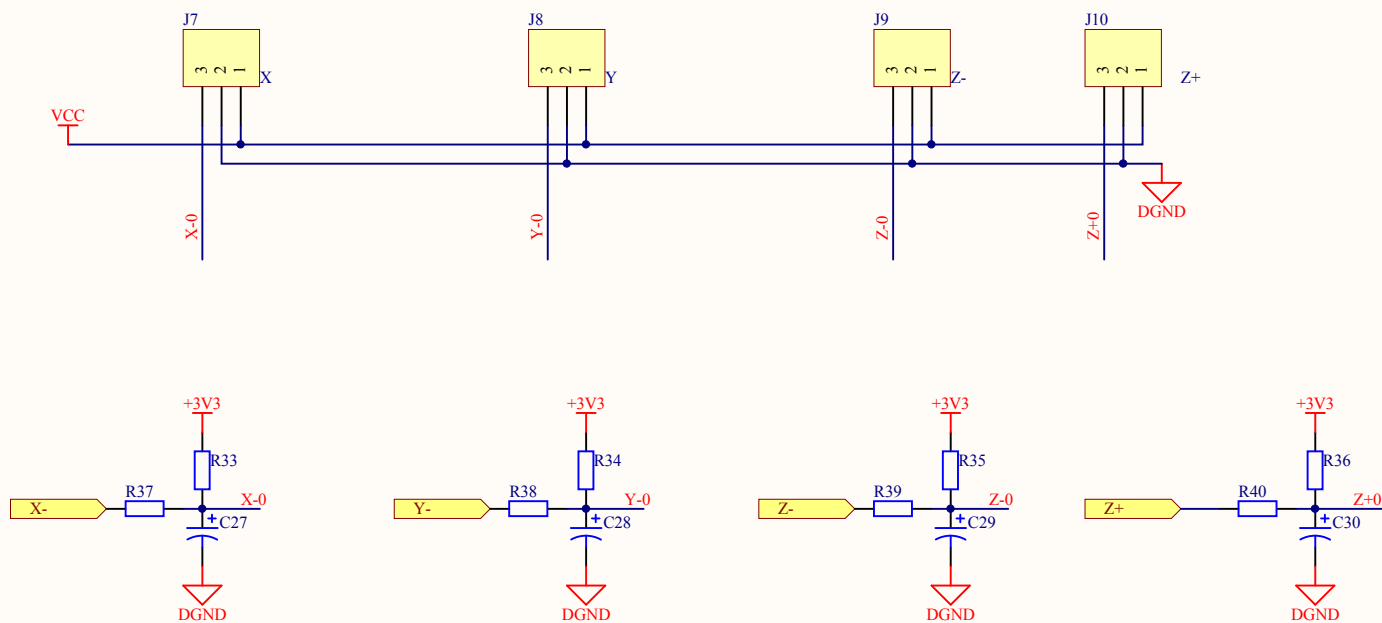
B

C

C

D

D



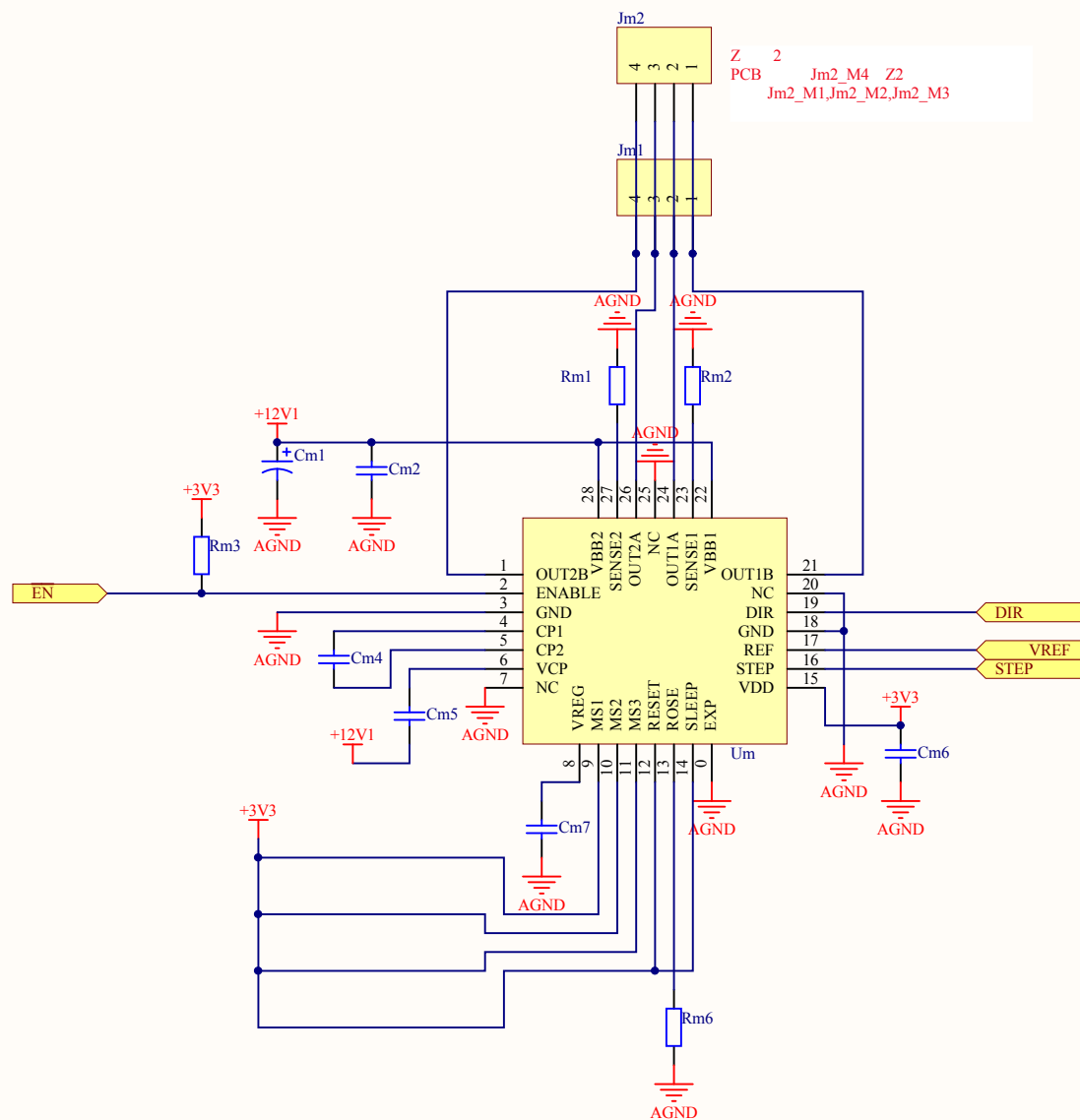
Title		
Size	Number	Revision
A4		
Date:	2020/8/27	Sheet of
File:	H:\SRC ET4000+ V1.0 003\ENDstops.Sch	Drawn By:

1

2

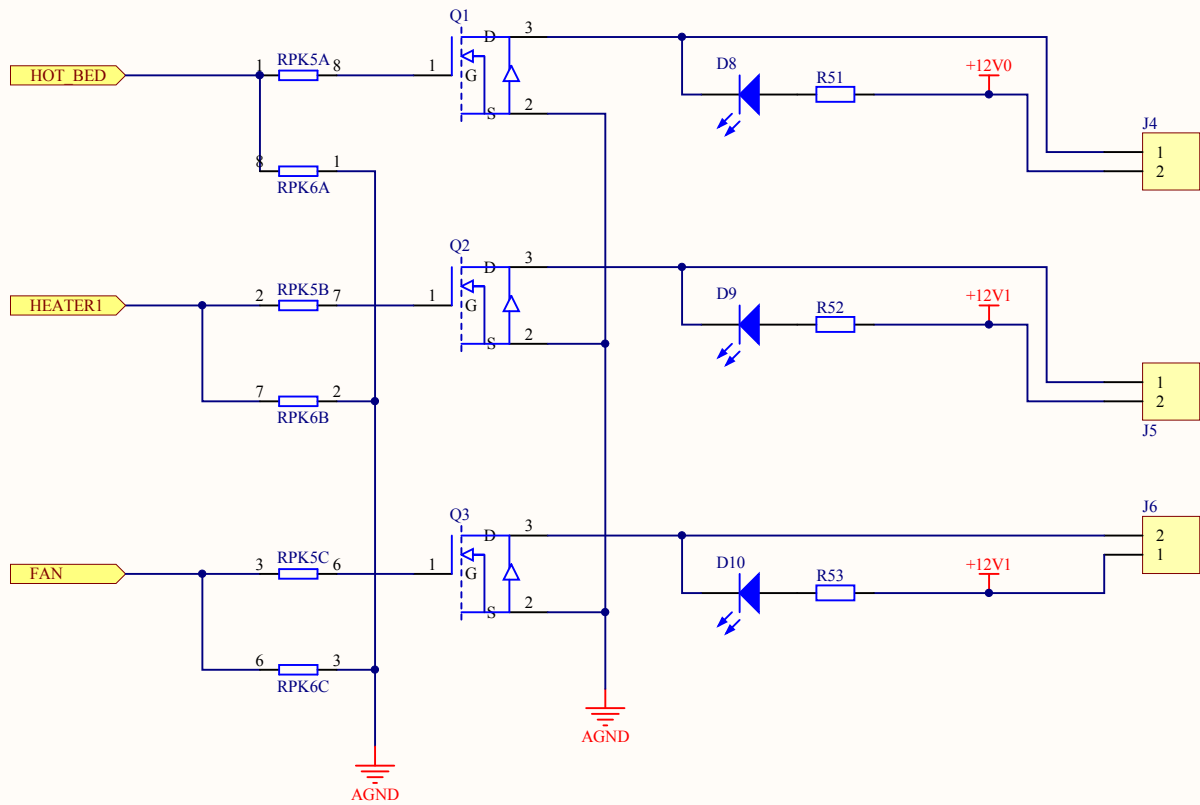
3

4

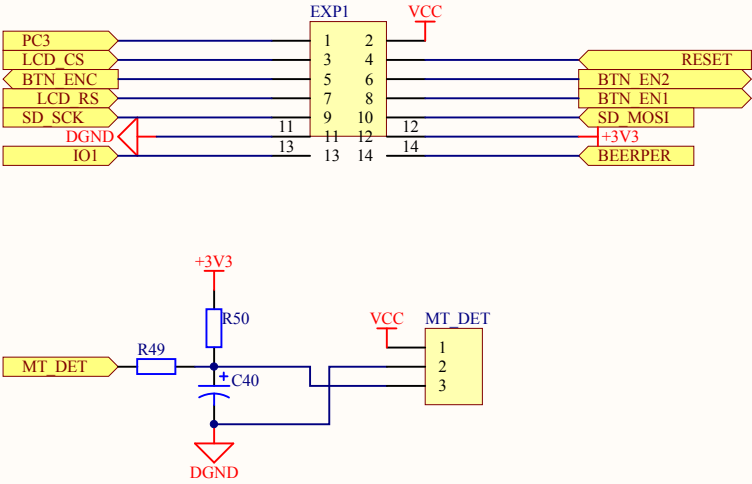


$V_{ref} = V_{CC} \cdot R_{P2} / (R_{P2} + R_{m4})$   
 $I_{max} = V_{ref} / (8 \cdot R_s)$   
 $R_{m4} = 21.25K \quad 0.5A$   
 $R_{m4} = 20K \quad I = 0.52A$

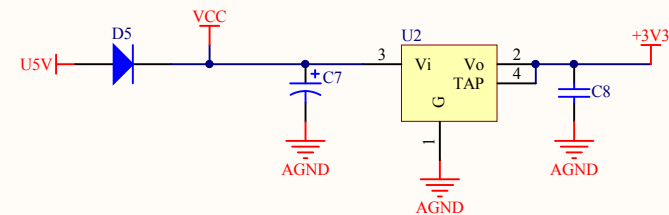
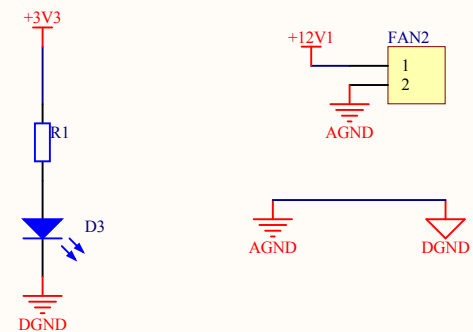
Title		
Size	Number	Revision
A4		
Date:	2020/8/27	Sheet of
File:	H:\SRC ET4000+ V1.0 003\HR4988.SchDoc	Drawn By:



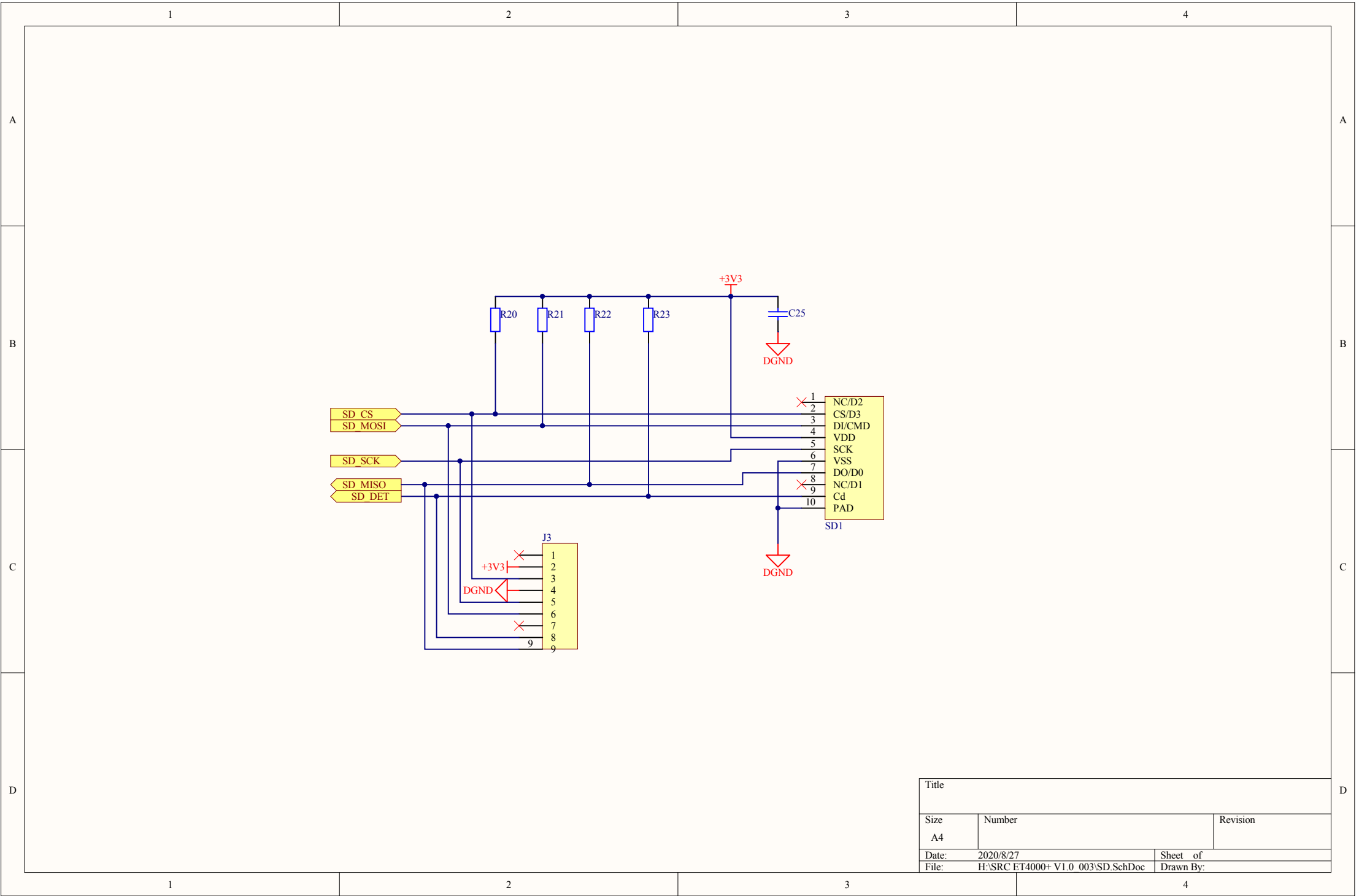
Title		
Size	Number	Revision
A4		
Date:	2020/8/27	Sheet of
File:	H:\SRC ET4000+ V1.0 003\MOSFET Sch	Drawn By:

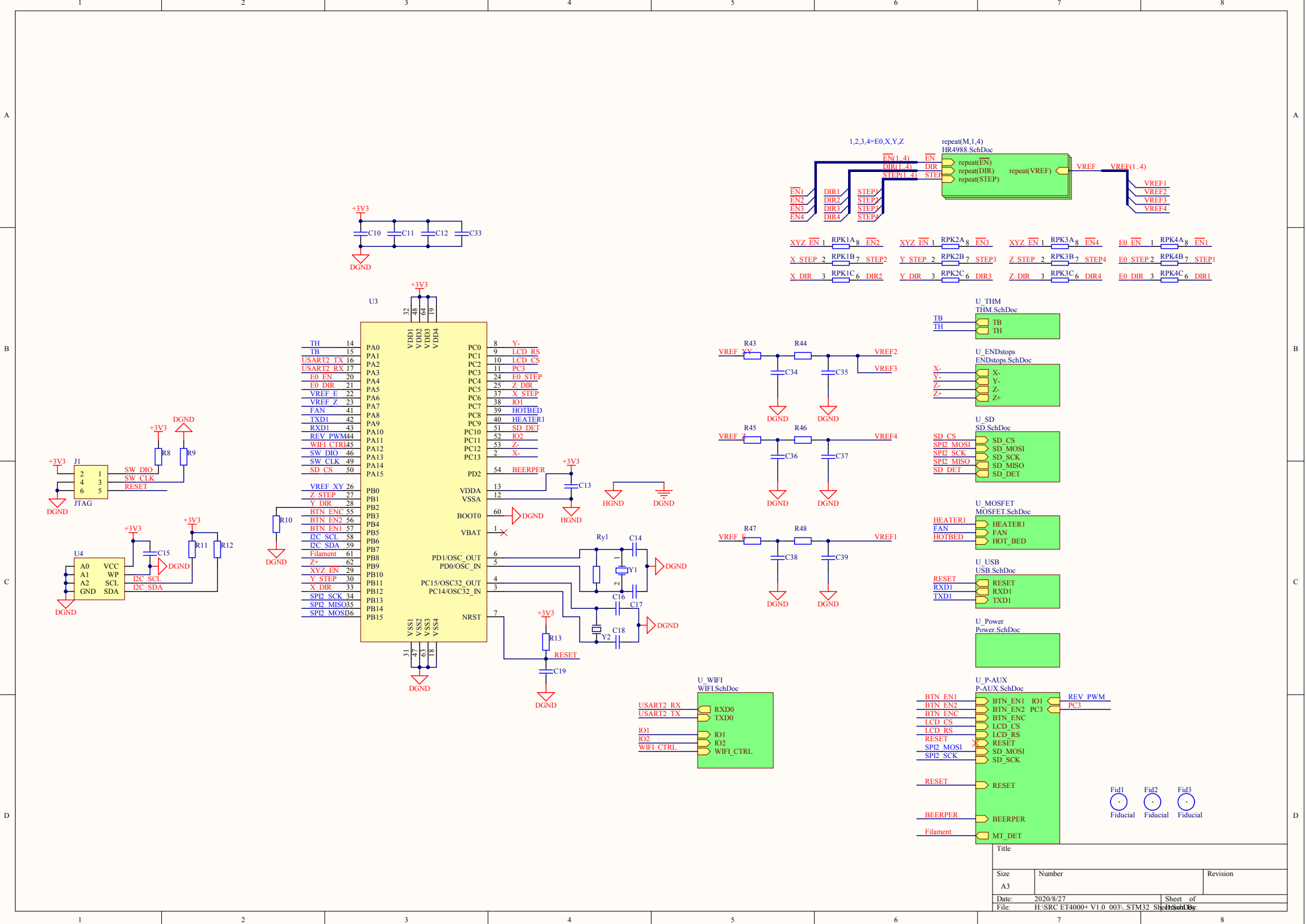


Title		
Size	Number	Revision
A4		
Date:	2020/8/27	Sheet of
File:	H:\SRC ET4000+ V1.0 003\P-AUX.SchDpDrawn By:	

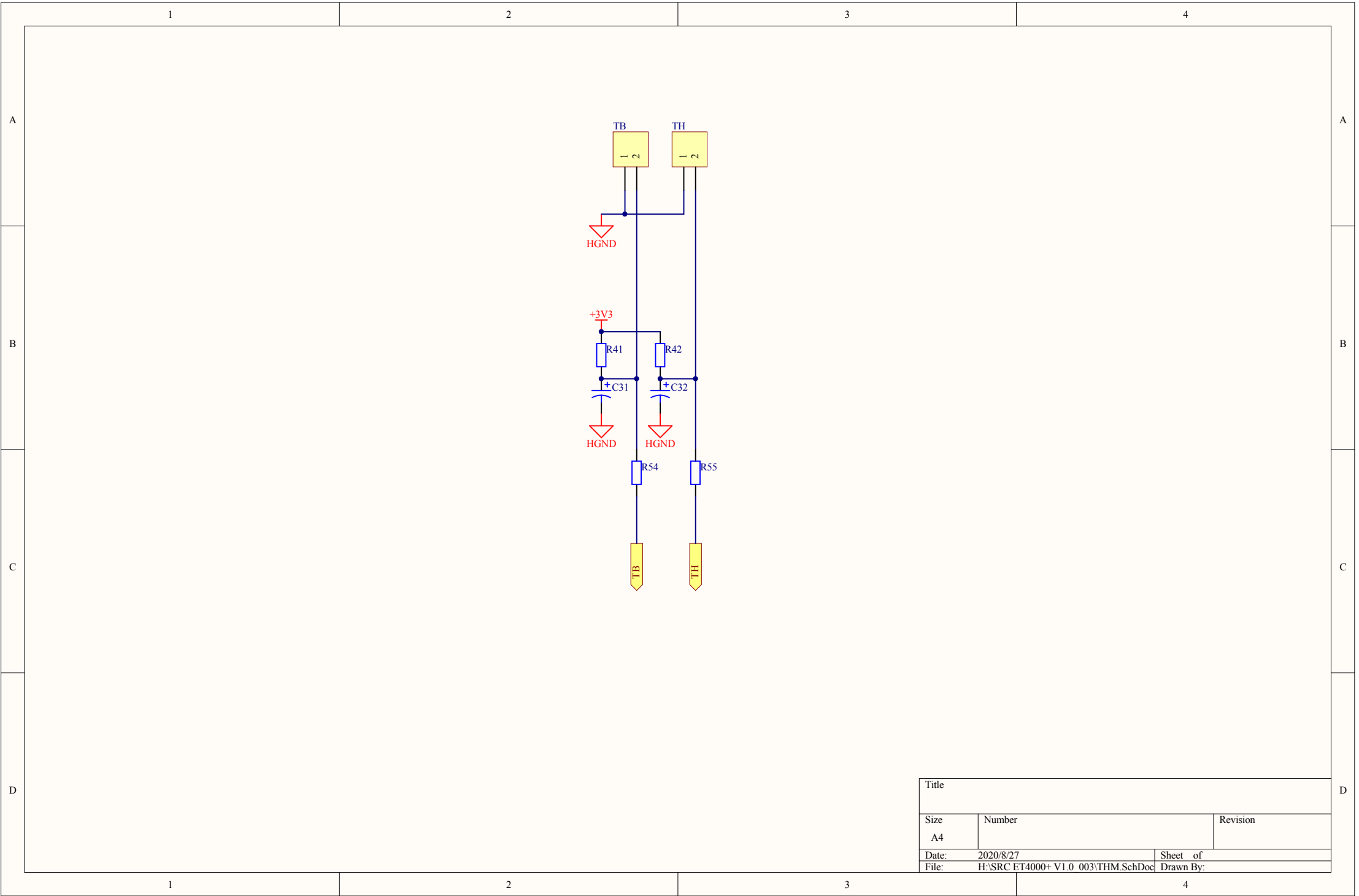


Title		
Size A4	Number	Revision
Date: File:	2020/8/27 H:\SRC ET4000+ V1.0_003\Power.SchDoc	Sheet of Drawn By:



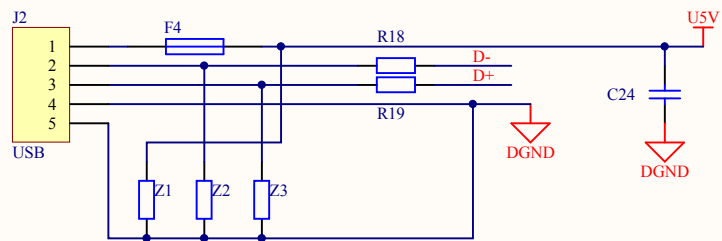
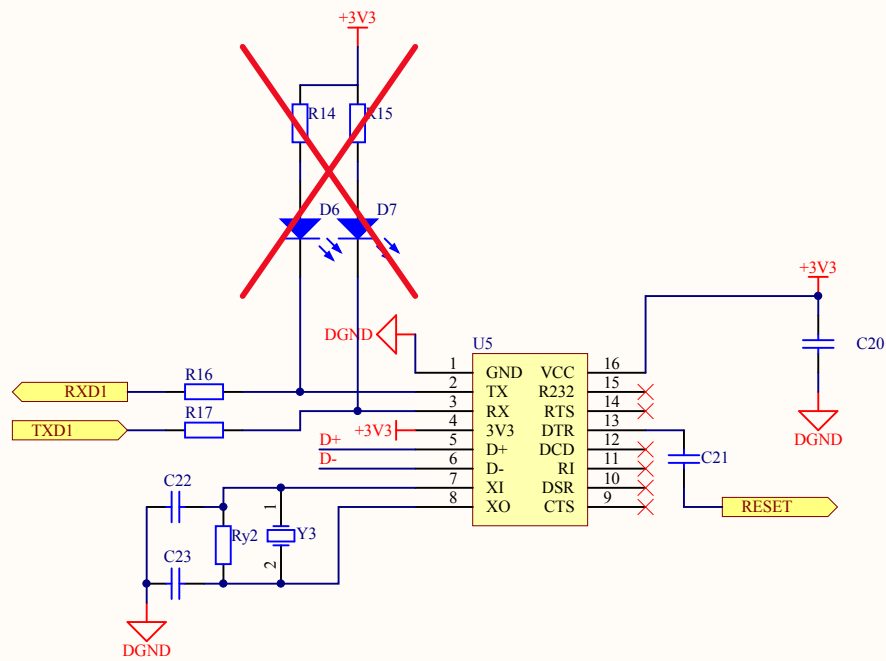


Title		
Size	Number	Revision
A3		
Date:	2020/8/27	Sheet of
File:	H:\SRC ET4000+ V1.0 003_STM32 SchDoc	01 of 01

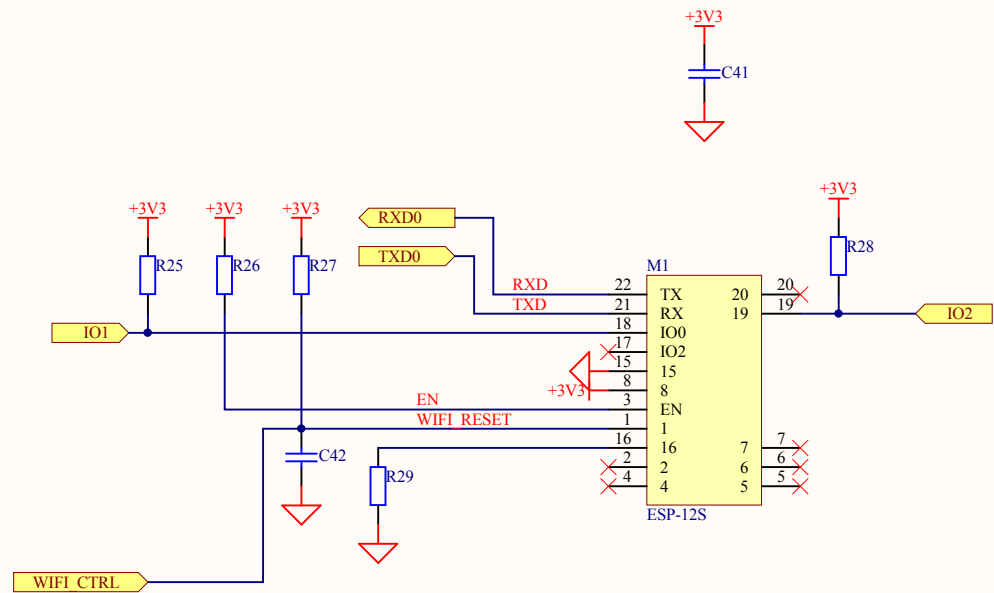


Title		
Size	Number	Revision
A4		
Date:	2020/8/27	Sheet of
File:	H:\SRC ET4000+ V1.0 003\THM.SchDoc	Drawn By:





Title			
Size	Number		Revision
A4			
Date:	2020/8/27		Sheet of
File:	H:\SRC ET4000+ V1.0 003\USB.SchDoc		Drawn By:



Title		
Size	Number	Revision
A4		
Date:	2020/8/27	Sheet of
File:	H:\SRC ET4000+ V1.0 003\WIFI.SchDoc	Drawn By: