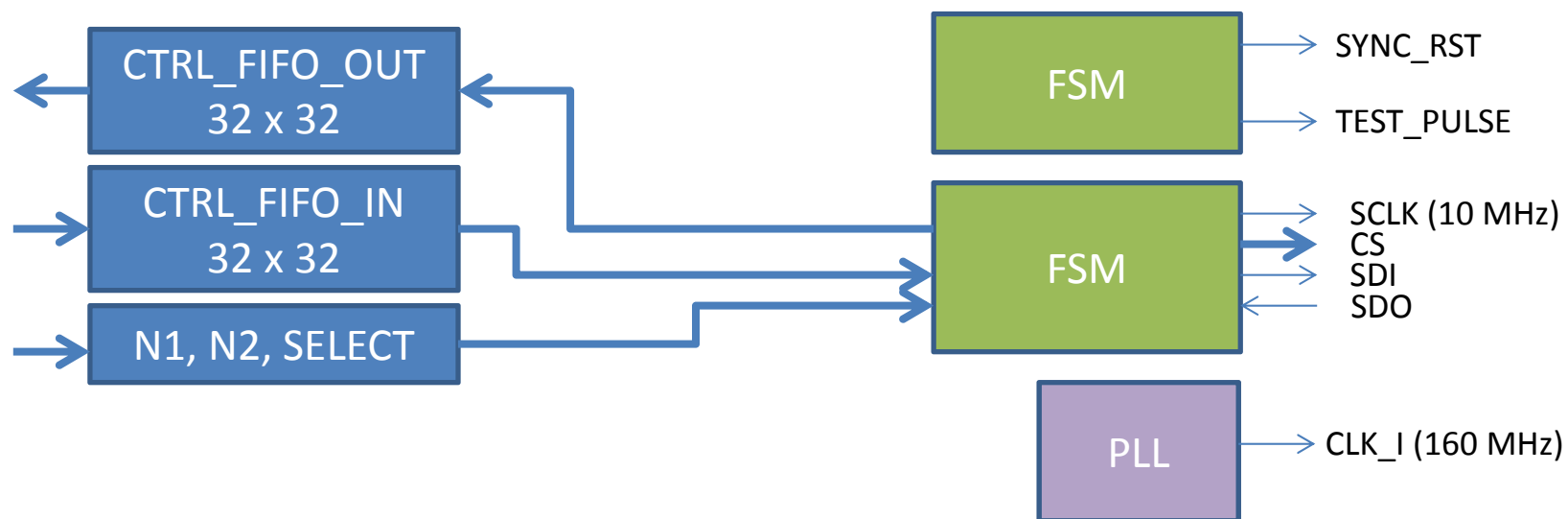
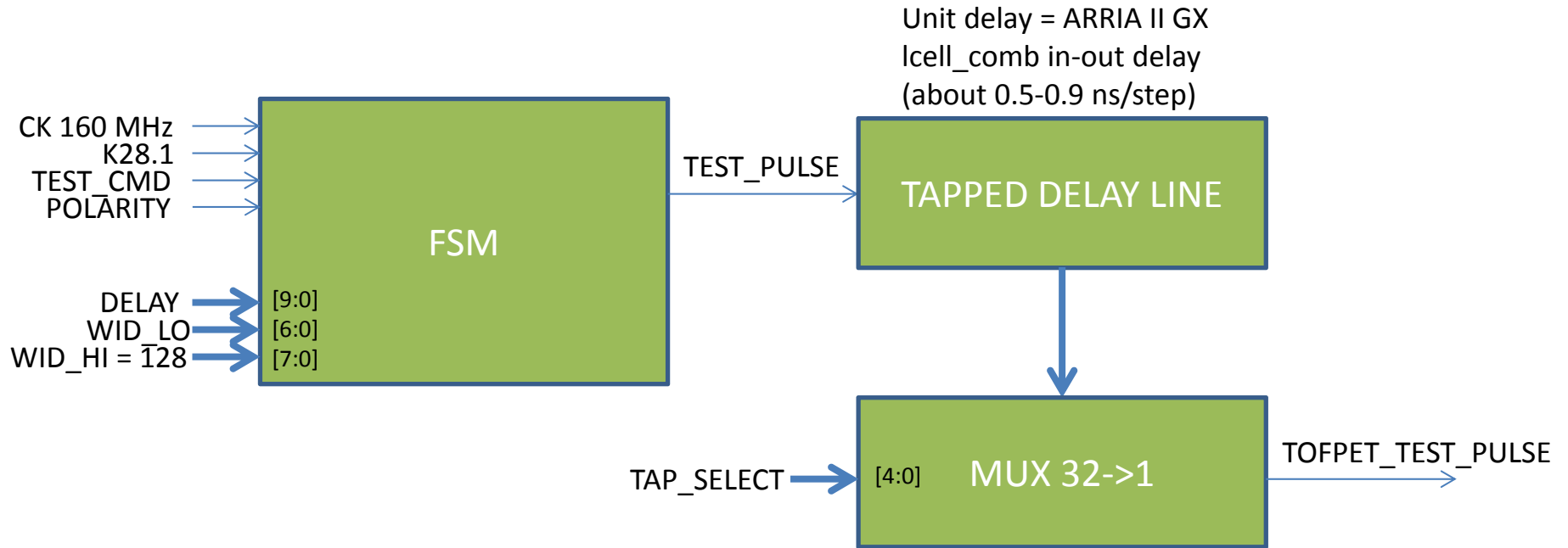


TOFPET ASIC CONTROLLER BLOCK DIAGRAM



TEST MODULE BLOCK DIAGRAM



K28.1 synchronize the machine to the start of TOFPET frame

If(TEST_CMD & K28.1)

{

<wait DELAY CK 160 MHz cycles>

TEST_PULSE = POLARITY

<wait WID_HI CK 160 MHz periods>

TEST_PULSE = ~POLARITY

<wait WID_LO K28.1 periods>

}