

BRAC UNIVERSITY

Department of Computer Science and Engineering CSE 260: Digital Logic Design

Examination: Quiz II
Semester : Fall 2025

Duration: 25min
Full Marks: 15

Name:	ID:	Section:
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1	Find the complement of the boolean expression using duality . No need to simplify the expression. Make sure to place the brackets correctly. $AB(BC' + (C'D')(A'+C))$	3
2	Reduce the given equation to minimum literals and draw the simplified circuit using NAND gates only: $F(A,B,C) = \sum(0,4,6)$	6
3	Find out POS for the following expression: $F(A,B,C,D)=A+B'C$	6

Rubric

Dual form correct Complement each literal correct	1.5 1.5
Simplification correct Circuit correct	3 3
Properly OR all the missing variables Expansion correct Sort Remove duplicates All the answers correct	1 1 1 1 2