

Control-Signal Crosstalk in Flip-Chip Superconducting Quantum Processors

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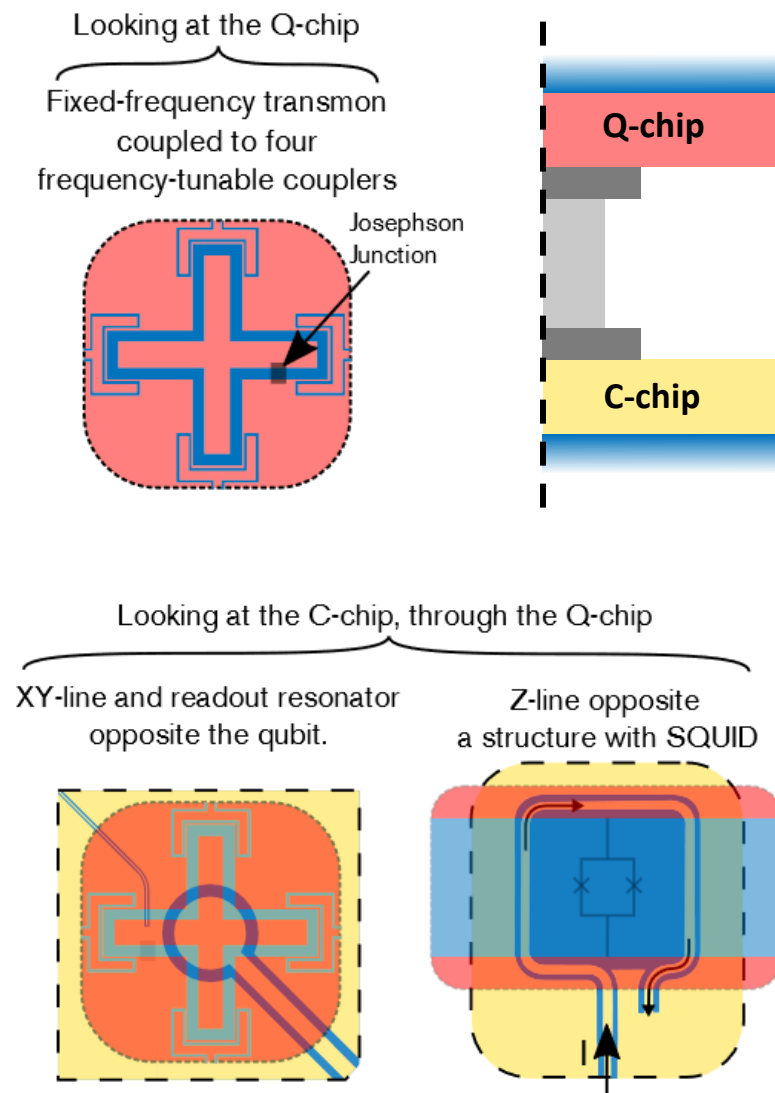
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Packaged Flip-Chip Module

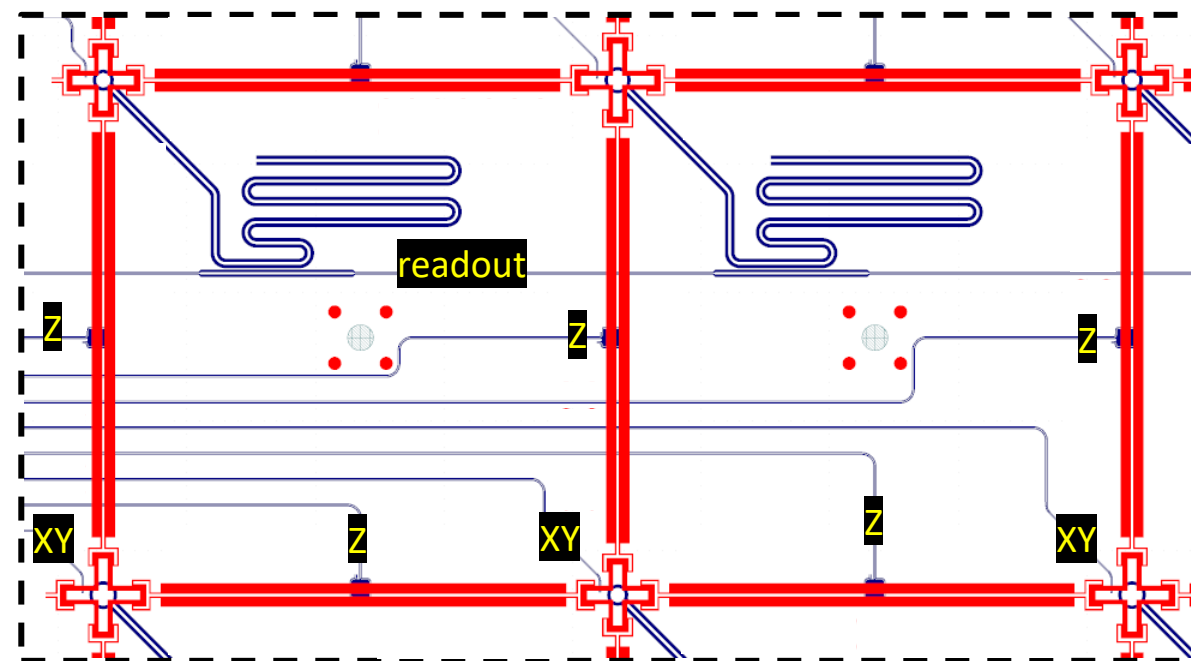
Chip Design & Fabrication by Chalmers
Flip-Chip Packaging by VTT
Sample Holder & PCB concept by Chalmers

1st generation flip-chip processors with a scalable layout and routing strategy

Basic elements



Routing corridor between two qubit rows



Automation of design and simulation

