

Muzaffer Nizam

Sr. Electronic Hardware Design Engineer

Contact

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- Istanbul , Turkey
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Personal Details

Total years of experience: 7 Years 6 Months

Eligible to work in: Need to Working Visa

Education and Training

Akdeniz University ----- Oct. 2016

@Antalya,Turkey

Bachelor's of Science, Electrical & Electronics Engineering

<https://eng.akdeniz.edu.tr/>

British Culture Language Schools --- 2023

@Antalya,Turkey

English Language B2-Upper Intermediate

Certificate Code: 8882201740

<https://verifiedportfolios.com/>

Technical Competence

Altium, Kicad, LTSpice, Matlab Simulink, Gerbv, Visual Studio, Visual Studio Code, STM32CubeIDE, Code Composer Studio, Github, yEd, Trello, MS Office, Libreoffice, Linux-Ubuntu.

Hardware Competence

Planning Design Block, Simulation, MCU Pin assignment, PCB Design Compliant to IPC-2221 standards, Design Compliant to EMC & EMI standards, Multi layer PCB Design at 2,4,6 and more layers, Decide different temperatures density PCB Materials FR4 TG130-140-170-180, Poly amide etc, Create Symbol and Footprint of Component, Power Circuit Design as Switching Regulator, Switching Controller, PMIC, DC, BLDC, PMSM Motor Control Driver Design as discrete or using IPM module, Communication circuits design using SPI, I2C, DAC, RS485, UART, Ethernet, CAN Bus, GSM, GNSS, Bluetooth, RF Sub 1 GHz, 433 MHz, Wifi 2.4 GHz, FSK, Analogue circuit design such as ADC, DAC, Active and Passive Filter, Current Read, Voltage Read, Design with these MCU's & Modules ; STM32xx, CC13xx Ti, TM4Cxx Tiva Series MCU, RK3399, Quectel MC60xx (GSM), EC200xx (LTE), EG915xx(LTE), Creating Gerber Files, Pick & Places Files, DXF and Step files for Mechanical Design, Prepare Bill of Materials List.

Firmware Competence

C Programming Language, Generating code with STM32CubeIde and prepare low level driver library, Hardware Verification with Firmware, Firmware Testing with pooling mode, Interrupt Mode, DMA Mode, UART Communication, Mod Bus ,CAN Bus ,SPI, I2C Communication, Analogue Input, DAC Output, PWM Generation, General Inputs and Outputs.

Profile

I am an experienced Electronic Design Engineer with over seven years of expertise in R&D departments. My specialization includes Industrial Electronics, Lift Control, Robotics, Consumer Electronics, Automotive, and Biomedical fields. My work is driven by a deep passion, and I approach each project with enthusiasm and an unwavering commitment to excellence. Over the course of my career, I have consistently delivered innovative solutions in electronic design. My extensive technical knowledge and proficiency empower me to address complex challenges and find optimal solutions. I thrive in collaborative environments, placing a high value on teamwork and effective communication. The prospect of joining your esteemed company fills me with immense joy, and I am genuinely excited about the opportunity to contribute my skills and expertise to the success of your organization.

Professional Experience

➡ **GMT Industrial Electronic Ltd. ----- Jul. 2023-Present**

- Sr. Hardware Design Engineer**

- <https://www.gmtcontrol.com/>

- Responsible for designing Schematic & PCB, Hardware Testing, Report, Documentation

- Projects;** Programmable Logic Controller (PLC)

➡ **Acri Industrial Ltd. ----- Apr. 2022-Jun.2023**

- Sr. Hardware Design Engineer**

- <https://www.acritechnology.com/>

- Responsible for designing Schematic & PCB, Hardware Testing, Report, Documentation

- Projects;** Lift BLDC Door Control Driver, Tracker Fault Errors and Remote Control with LTE , Lift Main Controller , Test Machine Main Board for Manufacturing.

➡ **Ake Elevator & Escalator Ltd. ----- Feb. 2021-Mar. 2022**

- Sr. Hardware Design Engineer**

- <https://ake.com.tr/>

- Responsible for designing Schematic & PCB, Hardware Testing, Report, Documentation

- Projects;** Lift DC Door Control Driver, Tracker Fault Errors and Remote Control with GSM, Lift Cabinet Button Main Board , Top and Bottom Lift Cabinet Control and Communication Boards, Lift Calling Boards, Water Vending Controller, Doppler Radar Sensor Board for Escalator.

➡ **Desird Design R&D A.S. ----- May. 2018- Feb. 2021**

- Mid. Hardware Design Engineer**

- <https://desird.com/en/>

- Responsible for designing Schematic & PCB, Hardware Testing, Report, Documentation

- Projects;** Test Machine Main Board for Manufacturing, Lift BLDC Door Control Driver, Linear Motor Projects for Lifts, Railway Peron Separator using BLDC Door Driver and with Safety Relay (SIL-2 certified).

➡ **Desird Design R&D A.S. ----- Aug. 2015- Oct. 2017**

- Jr. Hardware Design Engineer**

- <https://desird.com/en/>

- Responsible for designing Schematic & PCB, Hardware Testing, Report, Documentation

- Projects;** Lengthening Nails (Biomedical), IOT projects (Consumer Electronics), Telemetry Projects (Energy Services), Test Machine Main Board for Manufacturing, Lift DC Door Control Driver.

➡ **Turkcell Communication A.S ----- Jul. 2013- Sep. 2013**

- Intern Electronics Engineer**

- <https://www.turkcell.com.tr/>

- Responsible for GSM Base Station Location Planning, Analysis GSM Signals

My Sample Projects Link

- <https://github.com/muzaffernizam/My-Projects.git>

Certificates

- TUV NORD ----- Apr. 2019**

Safety Integrity Level for RAILWAY SYSTEM ELECTRONICS

Hobbies

Latin Dances (Salsa & Bachata), Theatre, Chess, Bicycle