


MUZAFFER NIZAM

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Profile

Electronic Hardware Design Engineer from 2015 to now. Senior in Industrial Electronic, Lift Control, Robotic, Consumer Electronic, Automotive, and Biomedical. An enthusiastic and ambitious engineer. Willing to work with different projects. Team worker and researcher. Experienced electronics design engineer with a demonstrated history of working in R&D Departments for more than 7 years. Skilled in Digital and Analogue electronics design. Possess the analytical abilities and technical skills necessary for engineering innovative designs and applications. A solid track record of researching, designing, debugging, and producing high precision components and systems. Flexible and focused with an in-depth understanding of electronics engineering theory, industry-standard circuit design, complex design.



Education and Training

- Akdeniz University @Antalya,Turkey October. 2016
Bachelor's of Science, Electrical & Electronics Engineering
<https://eng.akdeniz.edu.tr/>

- British Culture Language Schools @Antalya,Turkey March. 2023
English Language B2-Upper Intermediate
Certificate Code: 8882201740
<https://verifiedportfolios.com/>



Professional Experience

✓ Aciri Industrial LTD - Sr. Hardware Design Engineer Apr. 2022-Present
<https://www.acritechnology.com/>
*Responsible for designing Schematic, PCB and Hardware Testing, Report
*Responsible for these projects; Lift DC Door Control Driver project, Tracker Fault Errors and Remote Control with LTE Project, Lift Main Control Board , Test Machine Main Board for Manufacturing

✓ Ake Elevator & Escalator LTD - Sr. Hardware Design Engineer Feb. 2021-Mar. 2022
<https://ake.com.tr/>
*Responsible for designing Schematic, PCB and Hardware Testing, Report
*Responsible for these projects; Lift DC Door Control Driver project, Tracker Fault Errors and Remote Control with GSM Project, Lift Cabinet Button Main Board , Top and Bottom Lift Cabinet Boards, Floor Calling Boards, Water Vending Control Boards, Escalator Doppler Radar Projects.

✓ Desird Design R&D A.S. - Mid. Hardware Design Engineer May 2018- Feb 2021
<https://desird.com/en/>
*Responsible for designing Schematic, PCB and Hardware Testing, Report
*Responsible for these projects; Test Machine Main Board for manufacturing, Lift BLDC Door Control Driver project, Linear Motor Projects for Lift Motors, Railway Peron Separator BLDC Door Control with Safety Relay (SIL-2 certified)

✓ Desird Design R&D A.S. - Jr. Hardware Design Engineer Aug 2015- Oct 2017
<https://desird.com/en/>
*Responsible for designing Schematic, PCB and Hardware Testing, Report
*Responsible for these projects; Lengthening Nails (Biomedical), IOT projects (Consumer Electronics), Telemetry Projects (Energy Services), Test Machine Main Board for manufacturing, Lift DC Door Control Driver project

✓ Turkcell Communication A.S - Intern Electronics Engineering Jun 2013- Sept 2013
<https://www.turkcell.com.tr/>



Personal Details

Total years of experience: 7 Years 3 Months
Eligible to work in: Need to Sponsorship Visa



Hardware Competence

Planning Design Block, Simulation, MCU Pin assignment, PCB Design Compliant to IPC-2221 standards, Design Compliant to EMC & EMI standards, Multi layer PCB Design at 2,4,6 and more layers, Decide different temperatures density PCB Materials FR4 TG130-140-170-180, Poly amide etc, Create Symbol and Footprint of Component, Power Circuit Design as Switching Regulator, Switching Controller, PMIC, DC, BLDC, PMSM Motor Control Driver Design as discrete or using module, Communication circuits design using SPI, I2C, DAC, RS485, UART, Ethernet, CAN Bus, GSM, GNSS, Bluetooth, RF Sub 1 GHz, 433 MHz, Wifi 2.4 GHz, FSK, Analogue circuit design such as ADC, DAC, Active and Passive Filter, Current Read, Voltage Read, Design with these MCU's & Modules ; STM32xx, CC13xx Ti, TM4Cxx Tiva Series MCU, RK3399, Quectel MC60xx (GSM), EC200xx (LTE), EG915xx(LTE), Creating Gerber Files, Pick & Places Files, DXF and Step files for Mechanical Design, Prepare Bill of Materials List.



Firmware Competence

C Programming Language, Generating code with STM32CubeIde and prepare low level driver library, Hardware Verification with Firmware, Firmware Testing with pooling mode, Interrupt Mode, DMA Mode,UART/USART Communication, Mod Bus ,CAN Bus ,SPI, I2C Communication, Analogue Input, DAC Output, PWM Generation, General Inputs and Outputs.



My Sample Projects Link

<https://github.com/muzaffernizam/My-Projects>



Certificates

-> **TUV NORD** Apr 2019
Safety Integrity Level for RAILWAY SYSTEM ELECTRONICS
-> **ENTEK EDUCATION TECH** Dec 2012
Humanoid Robots Industrial Automation



Hobbies

Latin Dances (Salsa & Bachata), Theatre, Chess, Bycle