Question: I: Given lage size=5 tytes [Social address Address Apace = 8 fragmen Physical address Apace = 16 frames Logical address is devided into 2 parts: ?) page number ii) page offset De the other hand, the physical address also have 2 parts: ii) frame affect So, clusing conservious, the page affect is directly copied, and for the frame no., the page table is charled for the frame no to couch the page is mapped to. Let us take a dummy page table here: lage no. Frame no. 1 13 the page no. I is mapped to 2 24 frame no. 11 3 5 4 7 do, the corresponding physical 5 9 address will be 6 10		2. 0		
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y 7 So the corresponding physical 9 addren will be 10	7	1	13	the page no. I is mapped to
S 9 addren will be	-	2	24	Trume no. 11
S 9 addrew will be	-		S(n)	· Carlotte - Market -
6 10		,	7	' 1 1 / 1
	-		9	addren will be
N 41 K	7		10	N 41 N
7 11 frame no. Afpet	1	7		
	1		(1	
1011100 7 92 Ingrical address	1			1211100 Typical address
7 1011100 7 42 7 max alex	AX		7	10111100 17 12

[DATE]
$(1, \emptyset)$
As enplained in the previous part. The page effect will be 1.
the page offset will be 1.
1 C 00
We will check the premions page table only . So, The page no. 2 is mapped to frame no. 4.
So, the physical address will be frame no. offset
7 4 1
Also And
7 000 001 9 33
Juestion-2:
logical address space + 8 pages. Page size = 512 words (1 word = 2 bytes)
Page lize = 512 words (1 word = 2 bytes)
= 1024 bytes.
= 1024 bytes. Physical memory has 64 frames.
the state of the s
a) bits in logical addrew space:
Am. logical address space has 8 pages, each of size toxy bytes
So, total logical address space size = 8 x 1024
- 0132 bytes.
= 213 bytes
So, there will be 13 bits in the eogical address space

b) bits in physical address space:
Physical deddrey space have 64 frames.
Physical dddres space have 64 frames. frame xice will be same as that of page size.
€ No. Net Physical address space size = 64×1024 bytes
= 65(36 bytes.
= 2" bytes"
-1 16 bits in the Physical adoleru space
c) luge table size:
lage table will have the entires of every page, with its corresponding
We have in total 8 pages in logical address space,
as page table will have entires.
The size of each page table entry will be the size of I frame no. We have in total 64 frames. So, each frame can be represented using
We have in total 64 framer. No, each frame can be represented using
= 6 bits. = 05, Each page table entry size = 6 bits.
' 10
So, lage table size = 8x6 bits
= 48 bits or 6 bytes