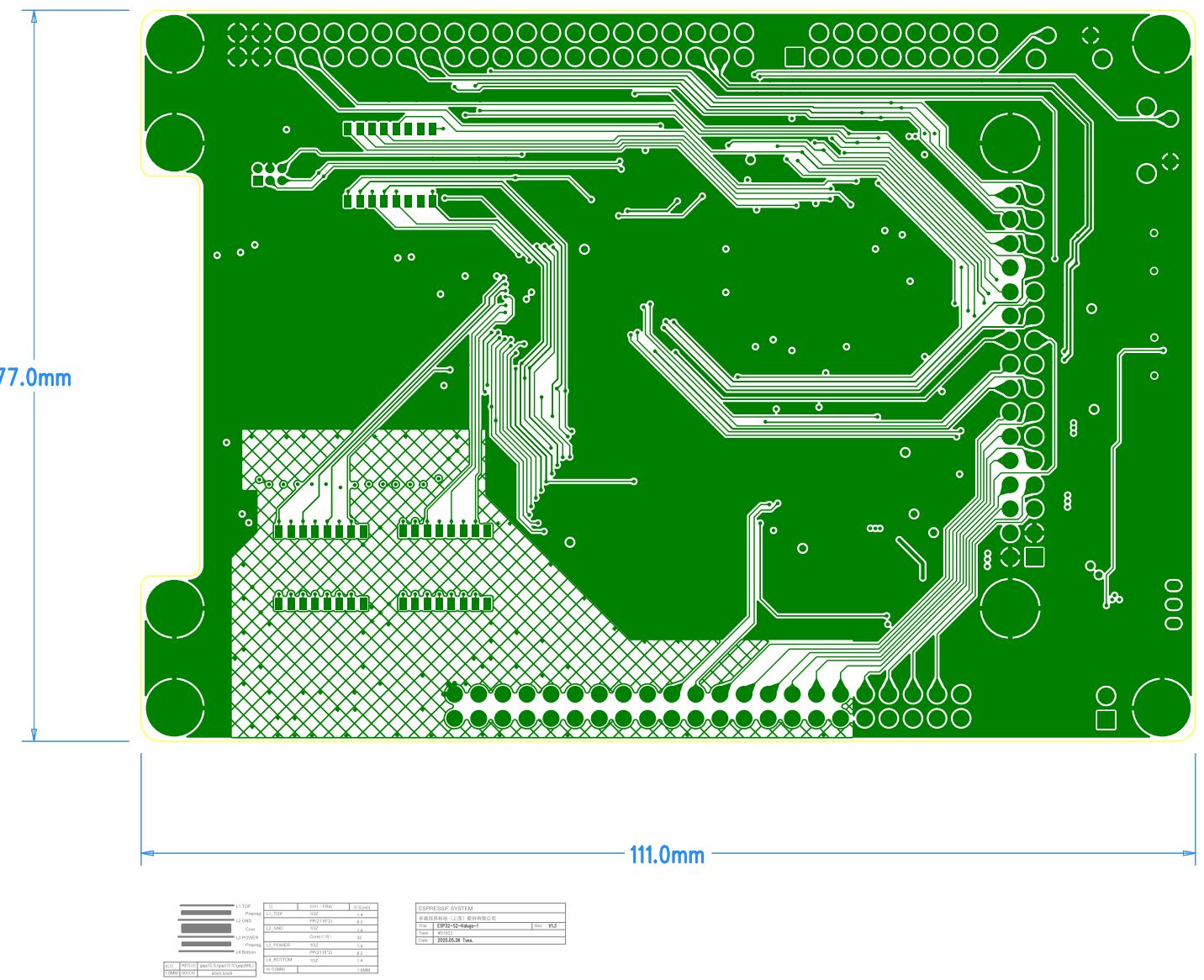
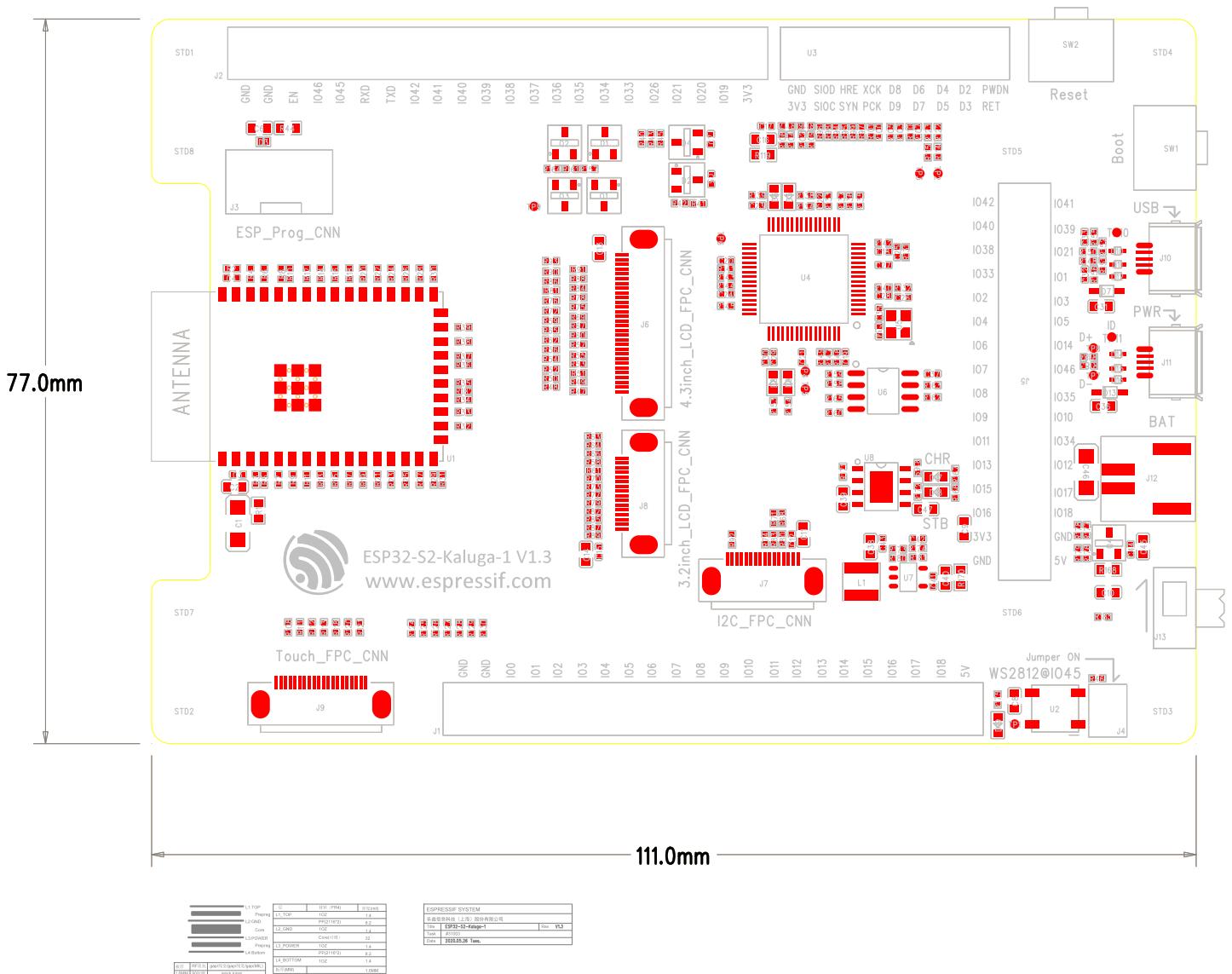


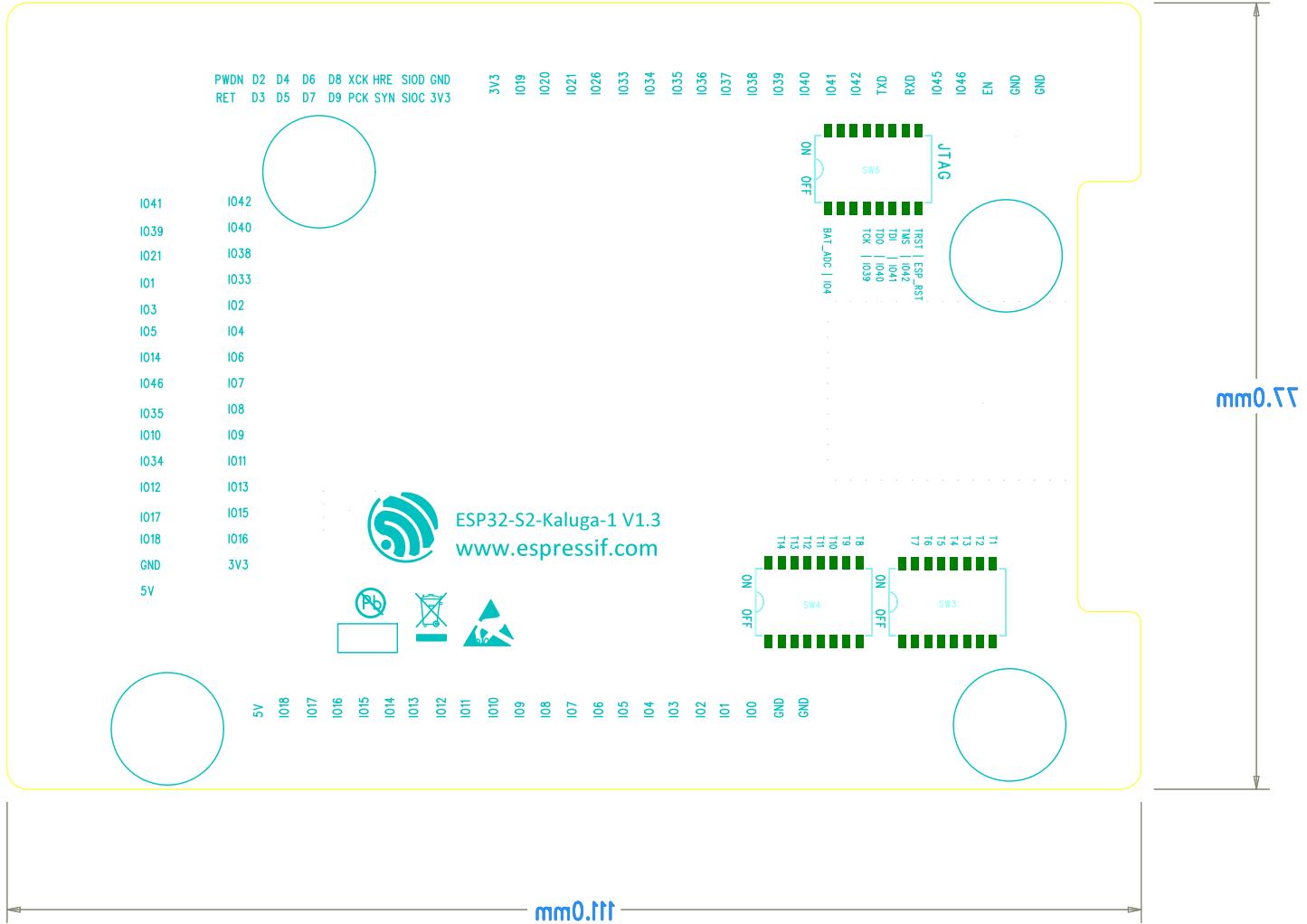
L1 TOP	L1 TOP	H11 (PAD)	0.050mm
L2 (NO)	L2 (NO)	10Z	1.4
L3 (NO)	L3 (NO)	PP2119(2)	6.2
L3 (POWER)	L3 (POWER)	CORE (T-B)	32
L4 (POWER)	L4 (POWER)	SOT	1.4
L4 (POWER)	L4 (POWER)	PP2119(2)	3.2
L4 BOTTOM	L4 BOTTOM	10Z	1.4

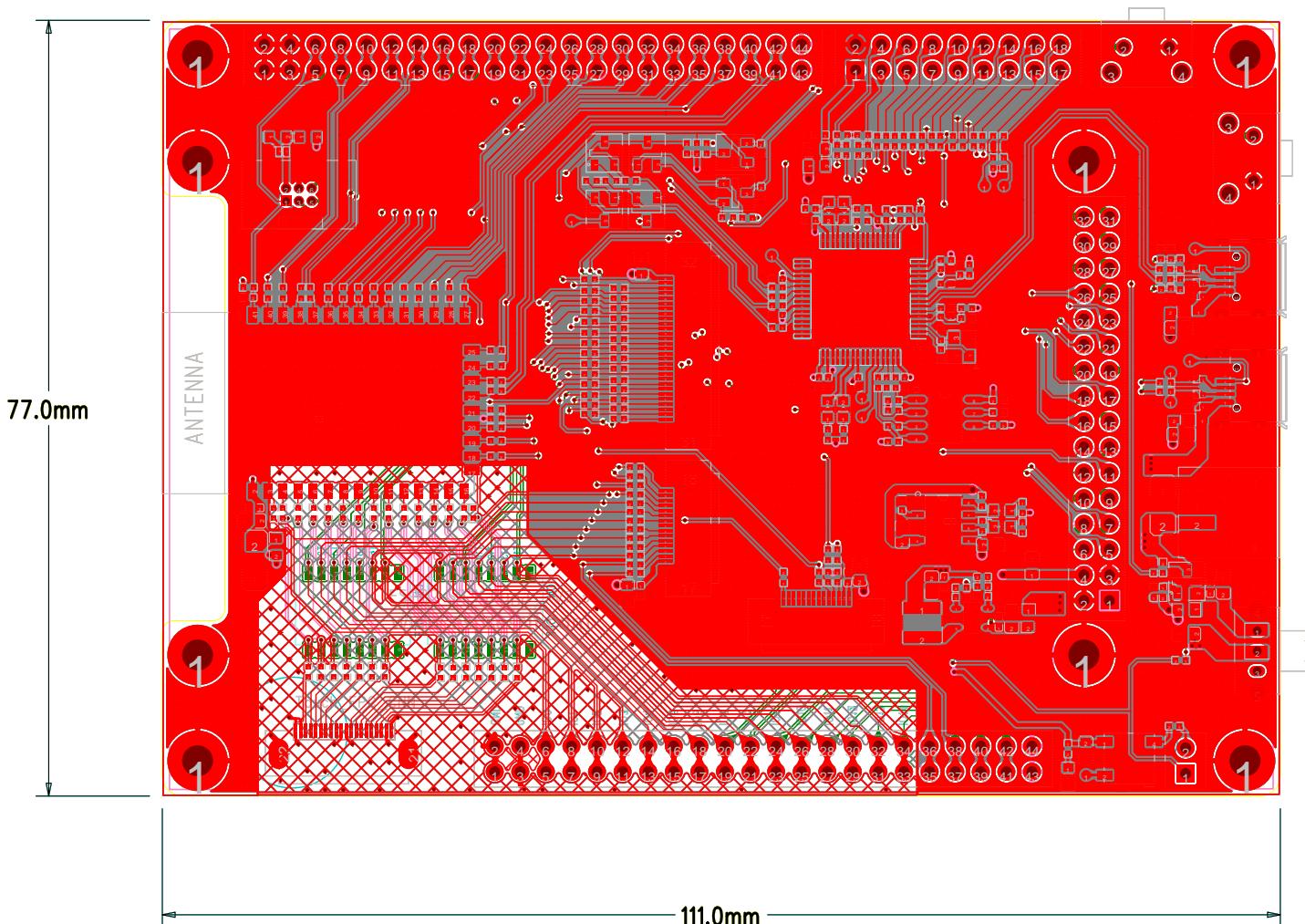
ESPRESSIF SYSTEM			
引脚名称	功能	引脚号	备注
ESP32-42-Debug-1		113	
Pin 1	3V3	1	
Pin 2	203.65.38 Ties.		

Pin 1
3V3
Pin 2
203.65.38 Ties.









	Q	(E1 / PR4)	SiO ₂ /nSi
L1_TOP Prep	Q1	10Z	1
L2_GND	Q2	PT21101(2)	2.5
Core	Q3	10Z	1.4
L3_POWER Prep	Q4	Core11(B)	22
L4_Bottom	Q5	10Z	1.4
	Q6	PT21101(2)	2.5
L4_BOTTOM	Q7	10Z	1.4
Bottom	Q8	RF100	1.0μm