

				Inputs				Control Outputs						Input Bits				Output Bits			
INUM	Instruction	Mnemonic	Format	opcode	func13	func17	func16	RFW	ALUOp	ALUSrcA	ALUSrcB	PCsel	WDsel	opcode	func13	func17	func16	Part1		Part2	
0	addi	ADDI	I	0010011	000	xxxxxxx	xxxxxxx	1	000	0	01	0	0	0b0010011	0b000	0bxxxxxxx	0bxxxxxxx	0b100001	0x21	0b100000100	0x104
1	add	ADDR	R	0110011	000	0000000	xxxxxxx	1	000	0	00	0	0	0b0110011	0b000	0b0000000	0bxxxxxxx	0b100000	0x20	0b100000000	0x100
2	jal	JAL	J	1101111	xxx	xxxxxxx	xxxxxxx	1	000	1	10	1	1	0b1101111	0bxxx	0bxxxxxxx	0bxxxxxxx	0b100010	0x22	0b100011011	0x11B
3	jalr	JALR	I	1100111	000	xxxxxxx	xxxxxxx	1	000	0	01	1	1	0b1100111	0b000	0bxxxxxxx	0bxxxxxxx	0b100001	0x21	0b100000111	0x107