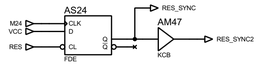

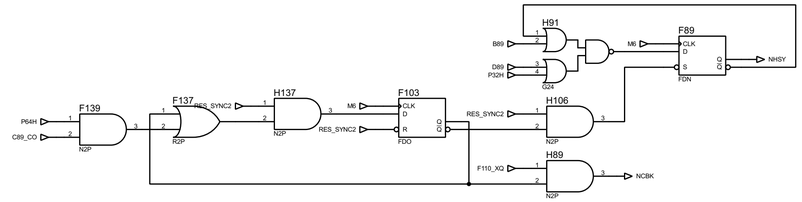

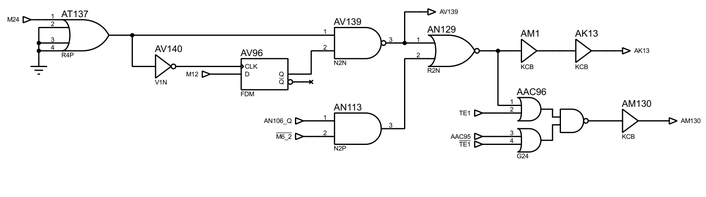

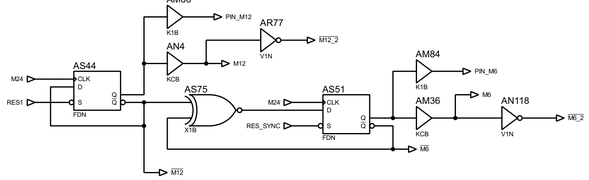

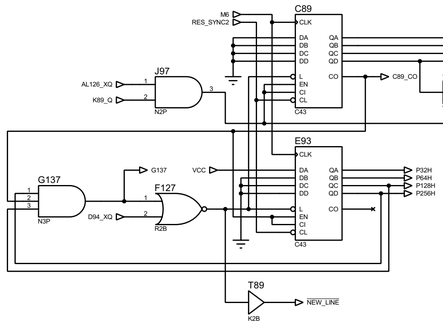

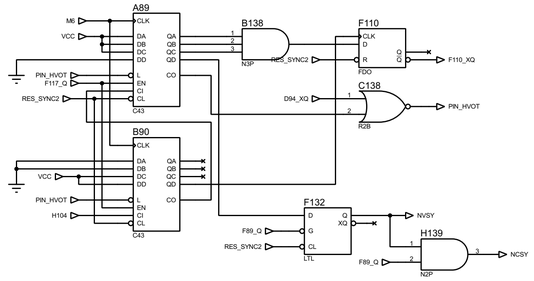

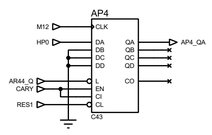

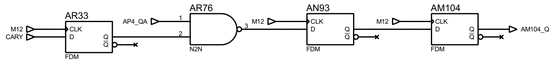

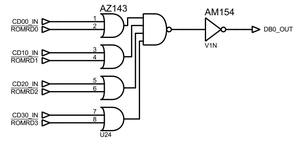

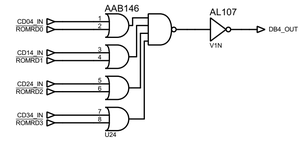

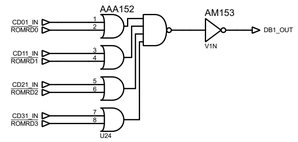

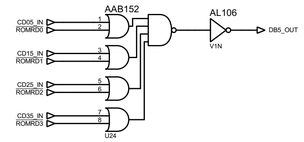

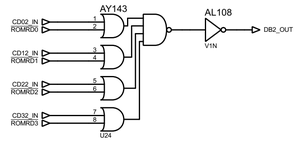

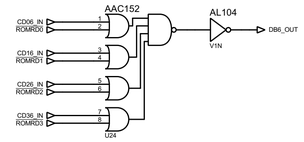

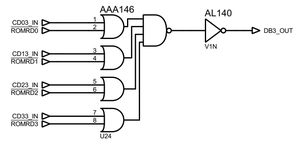

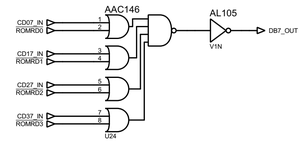

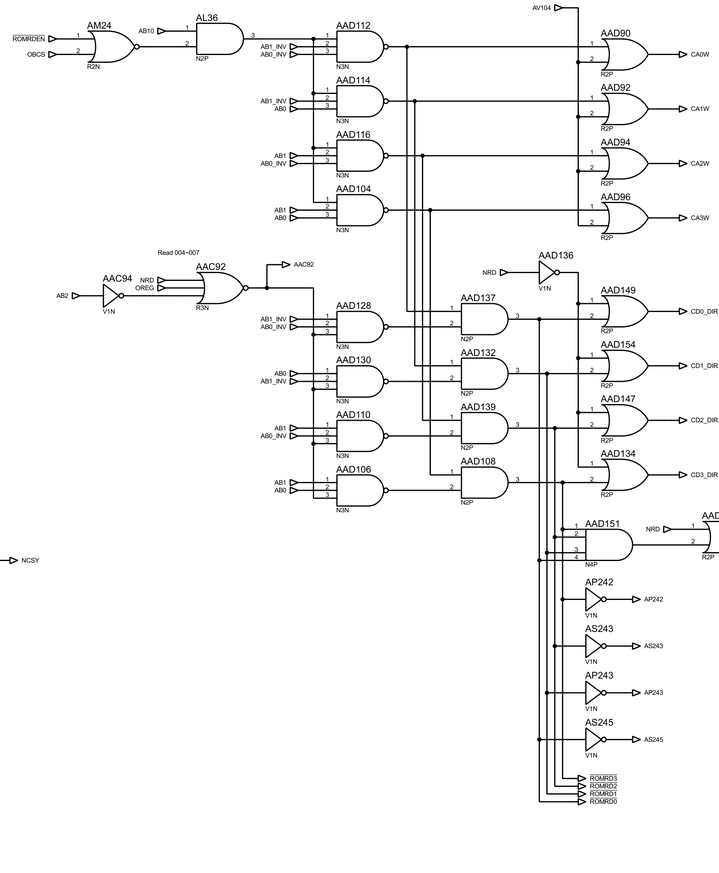

Never seen used on schematics
Write signals to possible GFX RAM ?

ROM data direction
4 bytes

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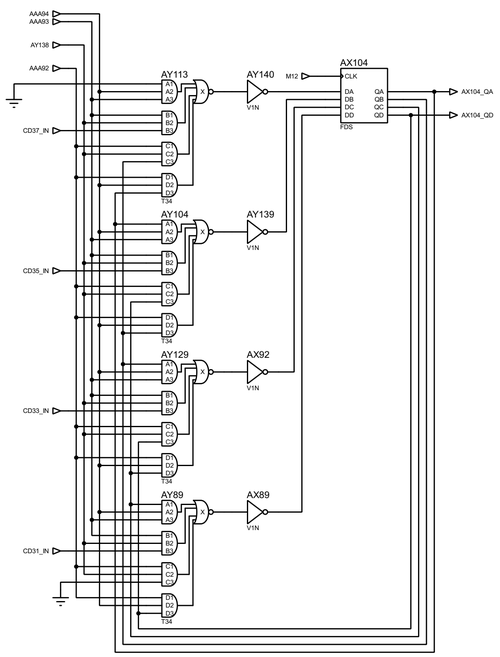
3 ODD



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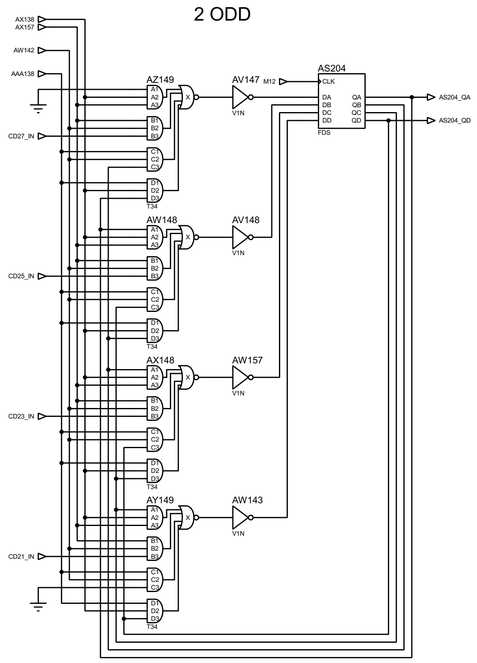

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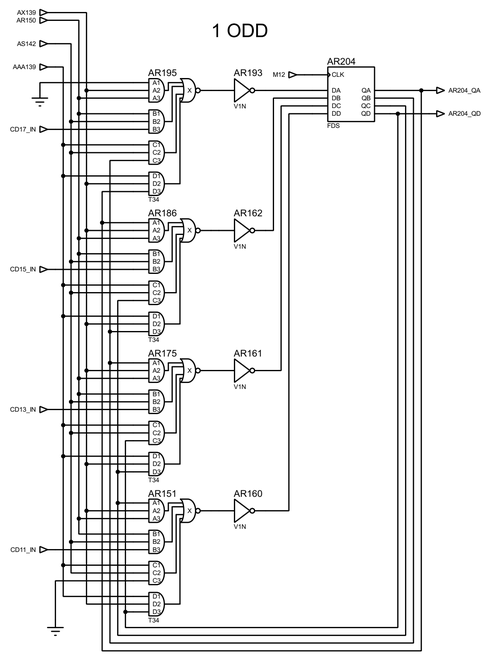

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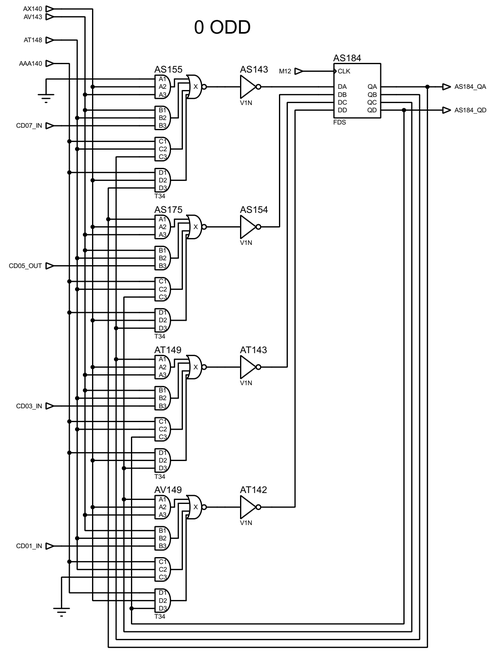

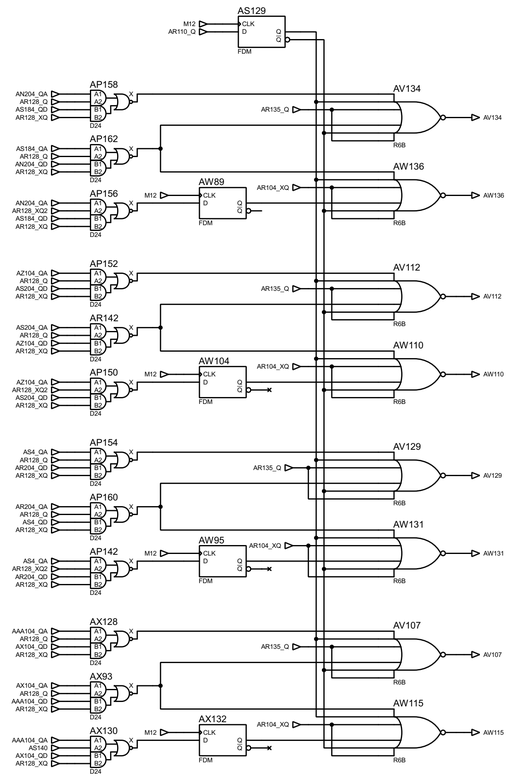

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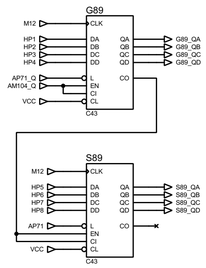

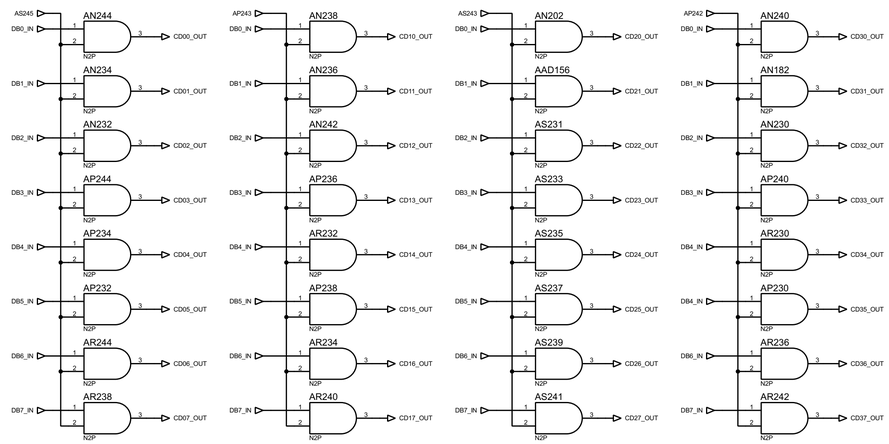

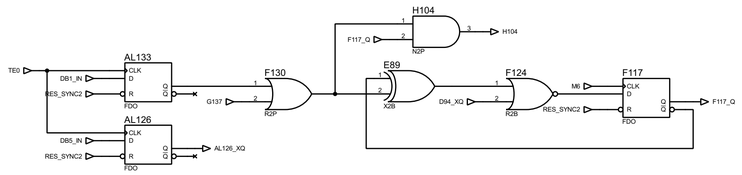

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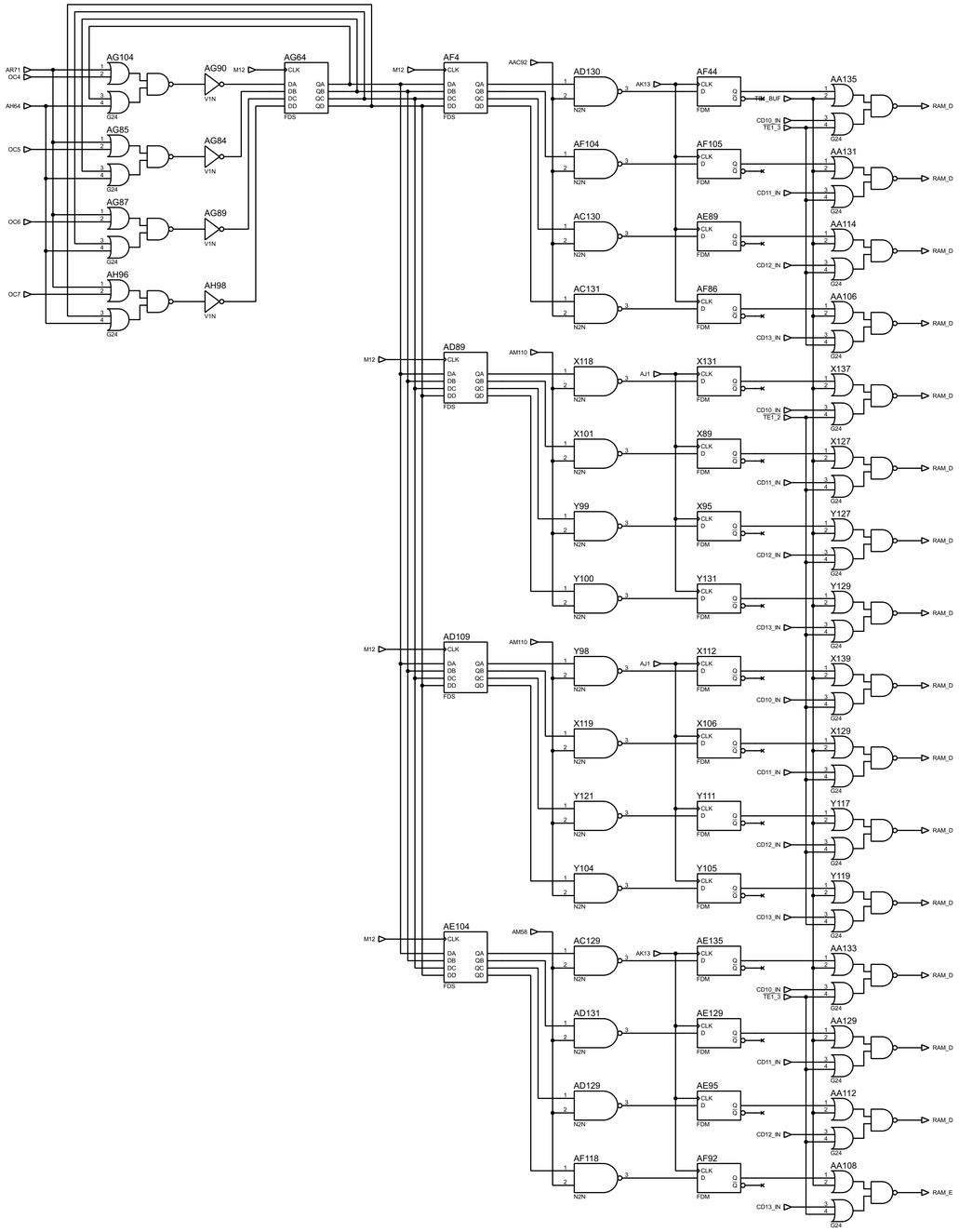

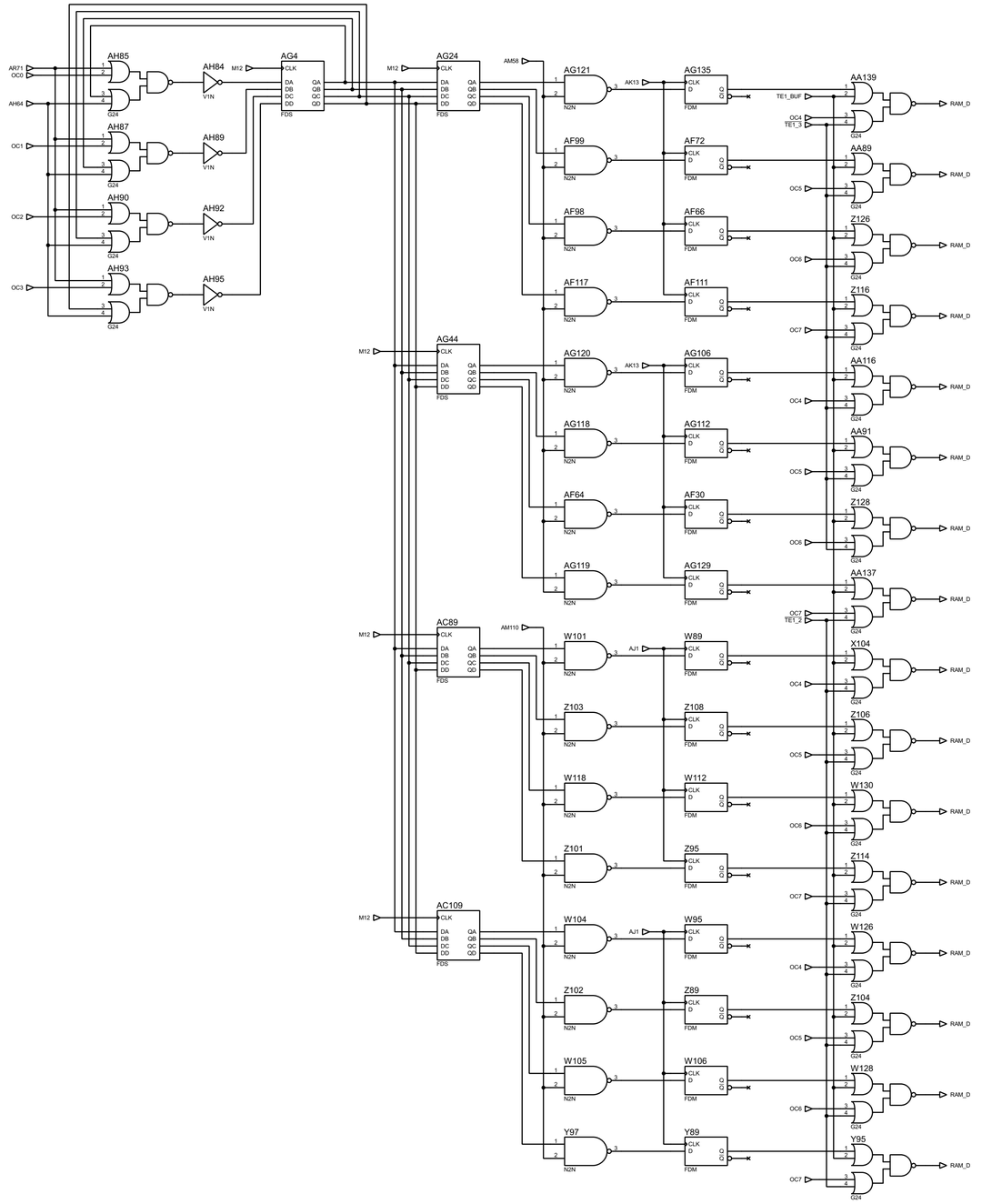

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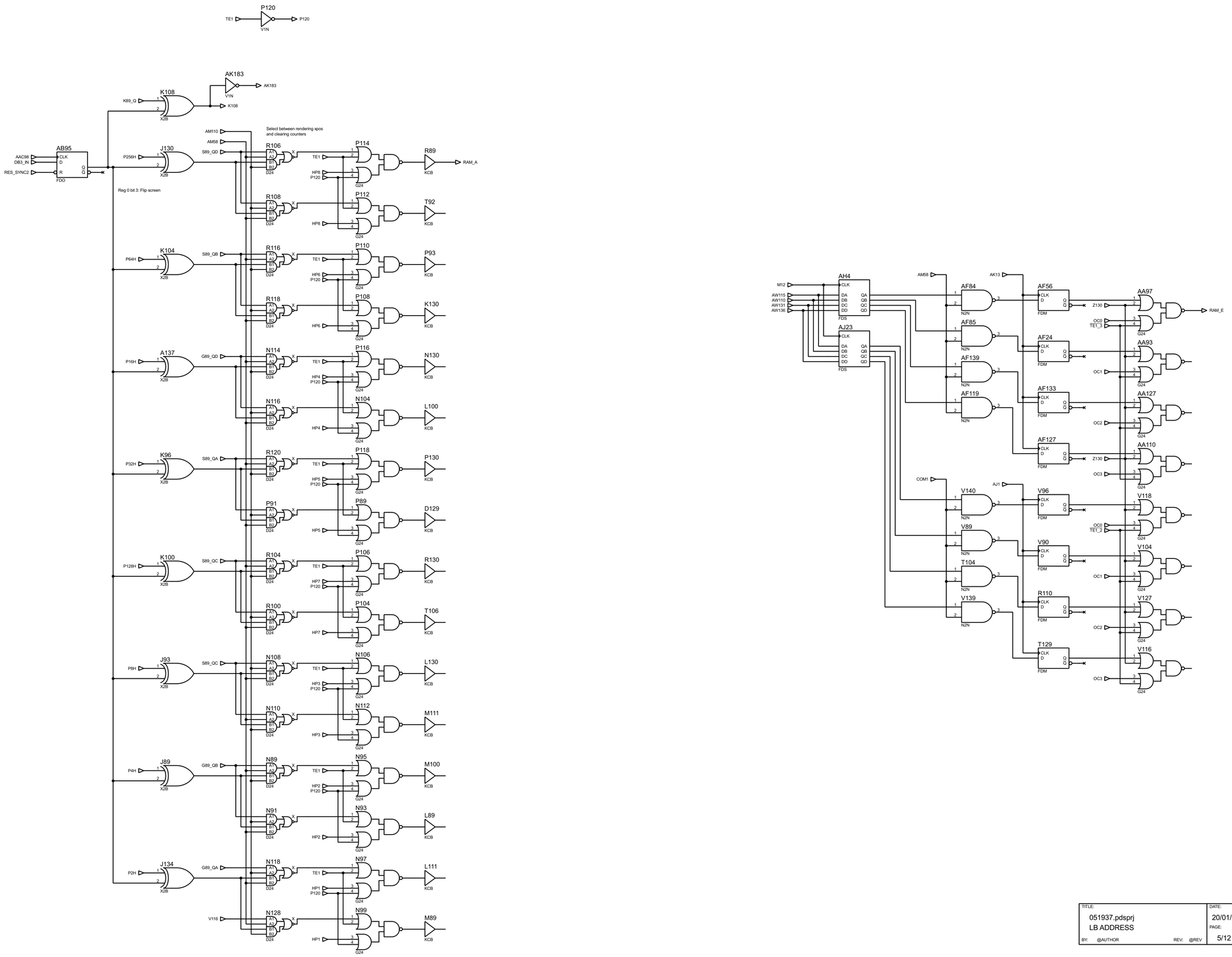

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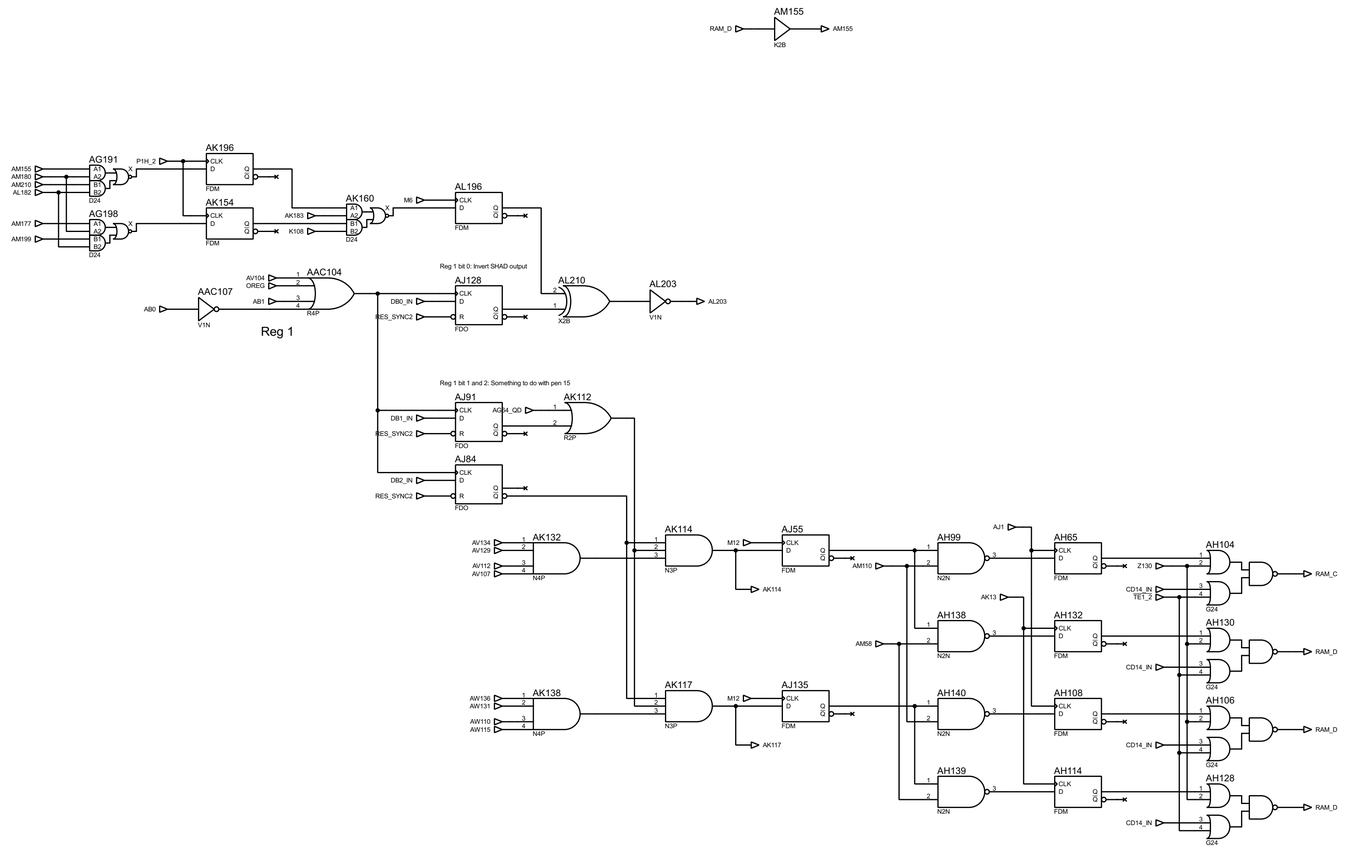

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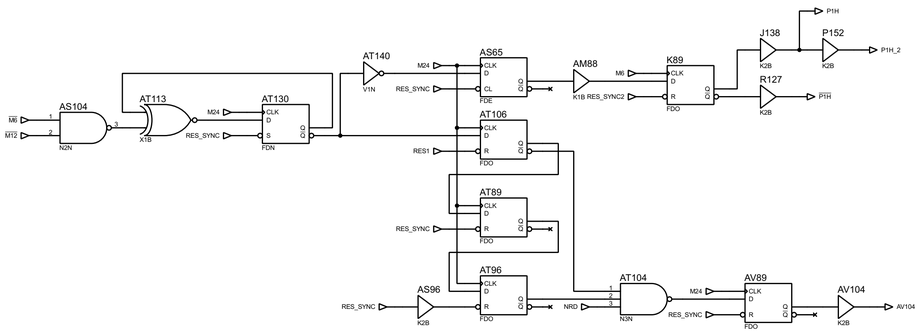
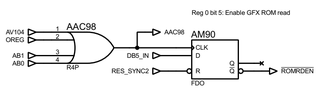
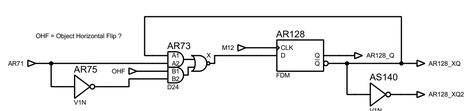
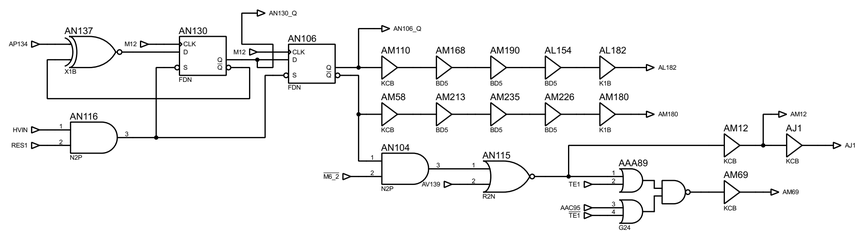
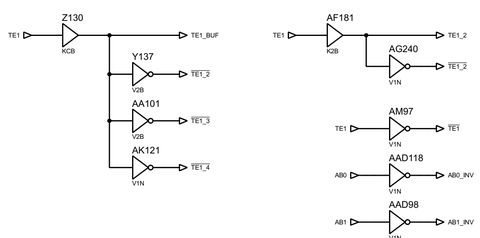


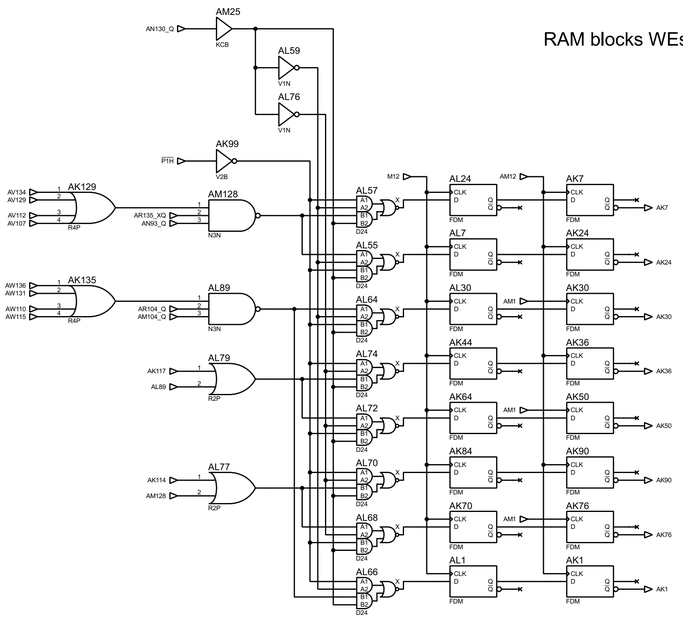


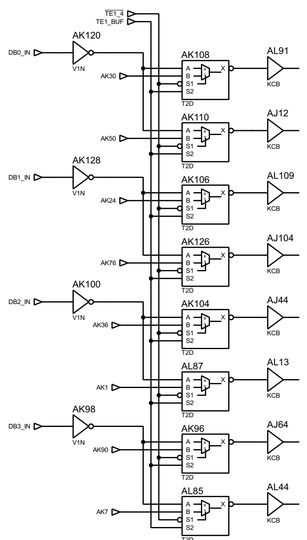
Reg 0 bit 5: Enable GFX ROM read

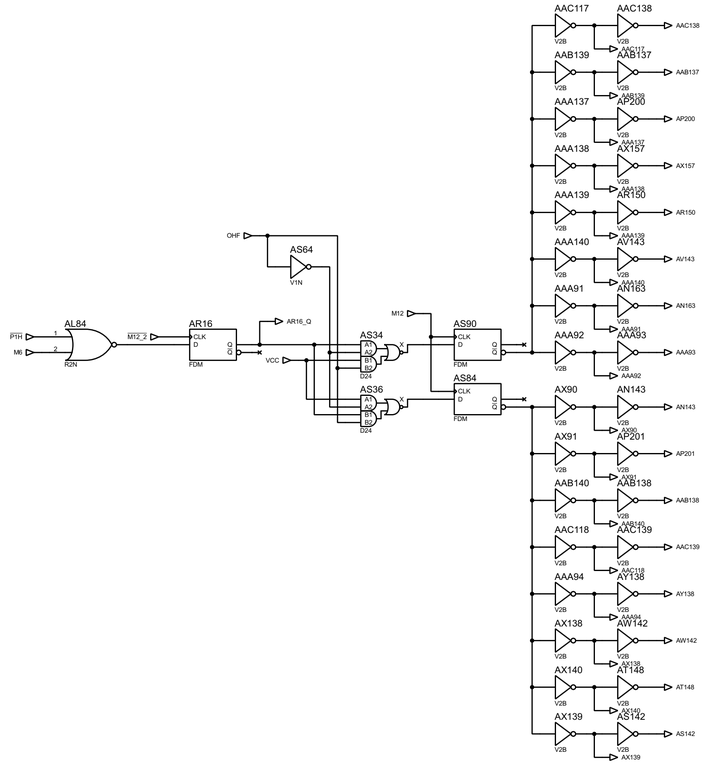


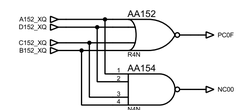












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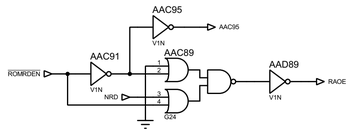
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ROMRDEN
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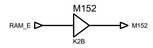
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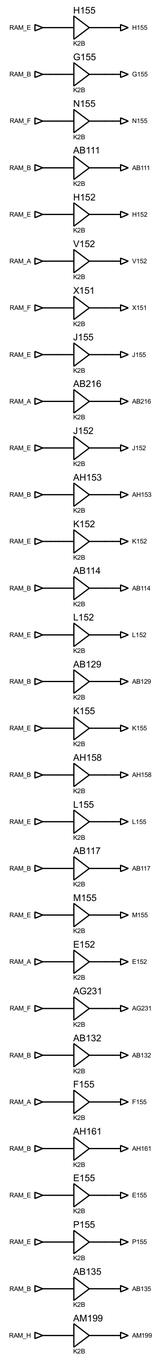
RAM_E
M152
K2B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RAM_E
H155
K2B
G155
RAM_B
G155
K2B
N155
RAM_F
N155
K2B
AB111
RAM_B
AB111
K2B
H152
RAM_E
H152
K2B
V152
RAM_A
V152
K2B
X151
RAM_F
X151
K2B
J155
RAM_E
J155
K2B
AB216
RAM_A
AB216
K2B
J152
RAM_E
J152
K2B
AH153
RAM_B
AH153
K2B
K152
RAM_E
K152
K2B
AB114
RAM_B
AB114
K2B
L152
RAM_E
L152
K2B
AB129
RAM_B
AB129
K2B
K155
RAM_E
K155
K2B
AH158
RAM_B
AH158
K2B
L155
RAM_E
L155
K2B
AB117
RAM_B
AB117
K2B
M155
RAM_E
M155
K2B
E152
RAM_A
E152
K2B
AG231
RAM_F
AG231
K2B
AB132
RAM_B
AB132
K2B
F155
RAM_A
F155
K2B
AH161
RAM_B
AH161
K2B
E155
RAM_E
E155
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AB135
RAM_B
AB135
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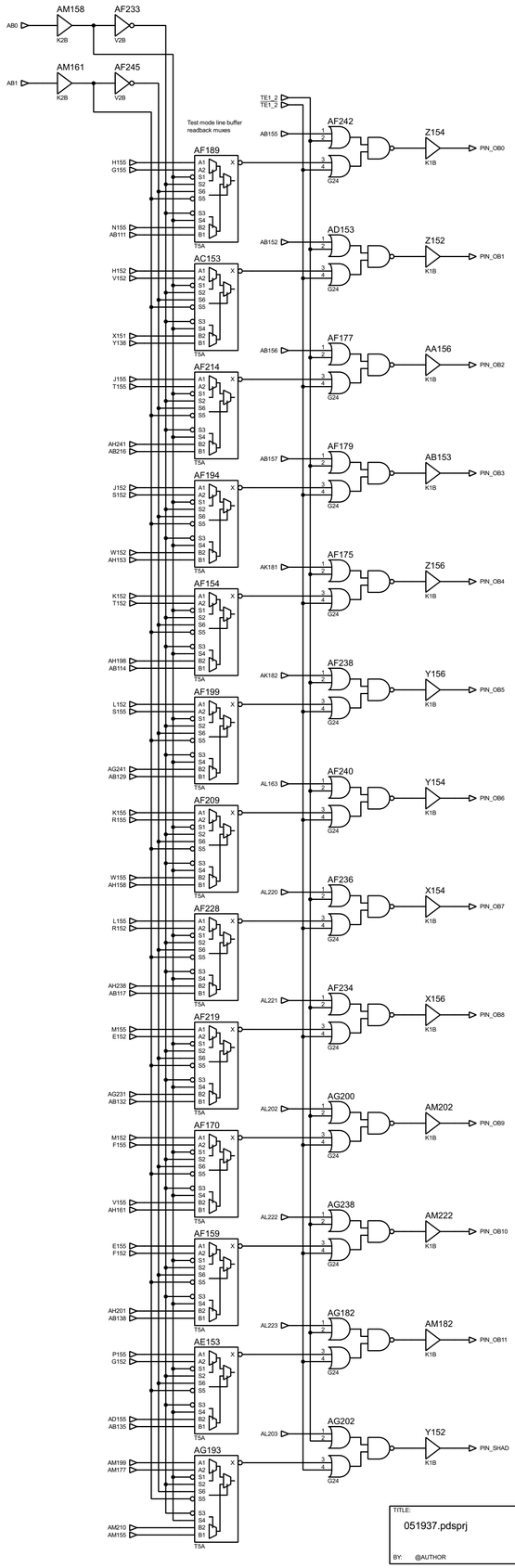
AM199
RAM_H
AM199
K2B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AF233
A80
K2B
V2B
AM161
AF245
AB1
K2B
V2B
Test mode line buffer readback muxes
TE1_2
TE1_2
AF242
AB155
Z154
PIN_OB0
K1B
AF189
H155
G155
N155
AB111
T5A
AD153
AB152
Z152
PIN_OB1
K1B
AC153
H152
V152
X151
Y138
T5A
AF177
AB156
AA156
PIN_OB2
K1B
AF214
J155
T155
AH241
AB216
T5A
AF179
AB157
AB153
PIN_OB3
K1B
AF194
J152
S152
W152
AH153
T5A
AF175
AK181
Z156
PIN_OB4
K1B
AF154
K152
T152
AH198
AB114
T5A
AF238
AK182
Y156
PIN_OB5
K1B
AF199
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AG241
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AL163
Y154
PIN_OB6
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AF209
K155
R155
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AH158
T5A
AF236
AL220
X154
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AF228
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AB117
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AL221
X156
PIN_OB8
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AF219
M155
E152
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AL203
Y152
PIN_SHAD
K1B
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AM199
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B1
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S3
S4
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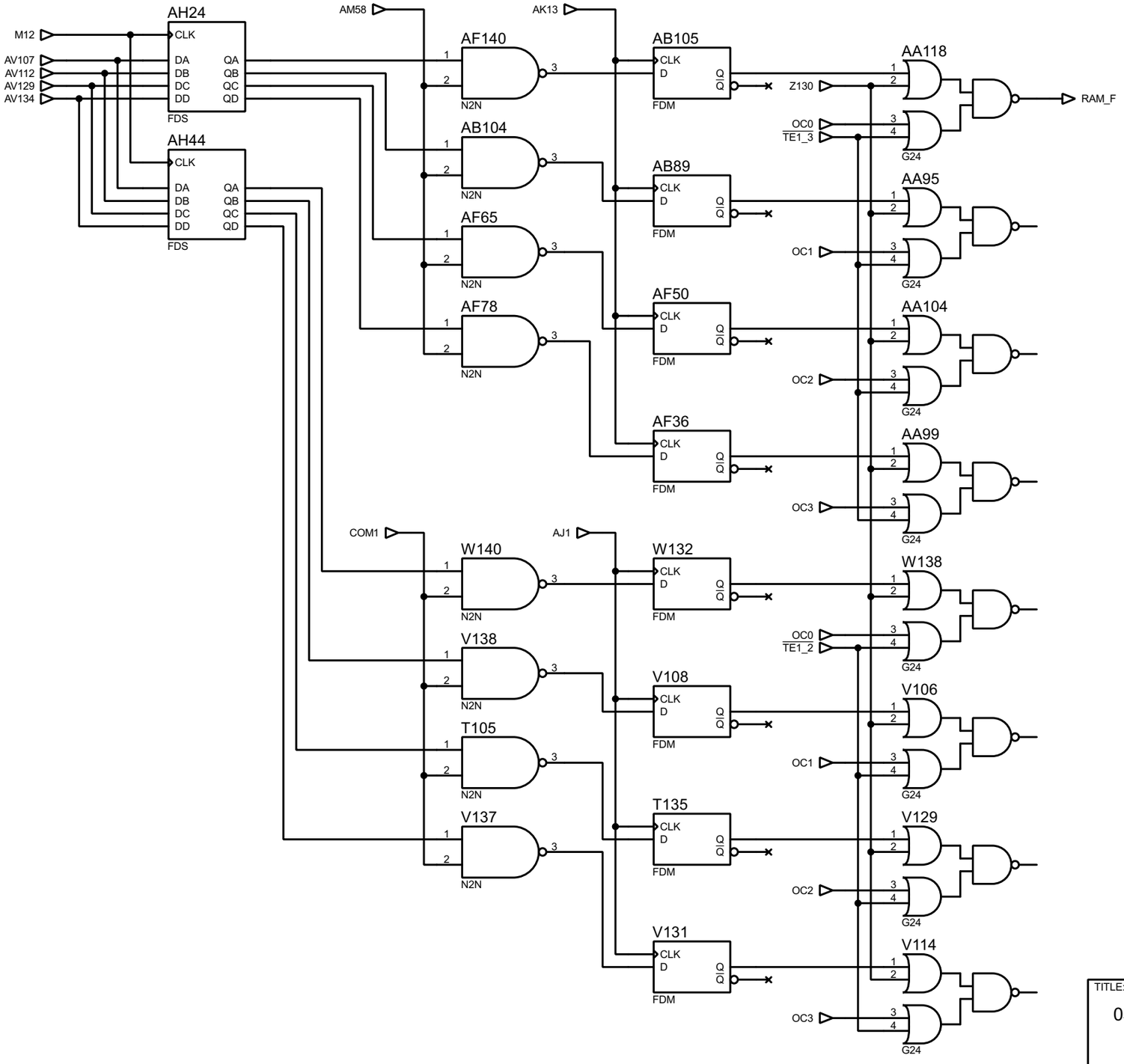

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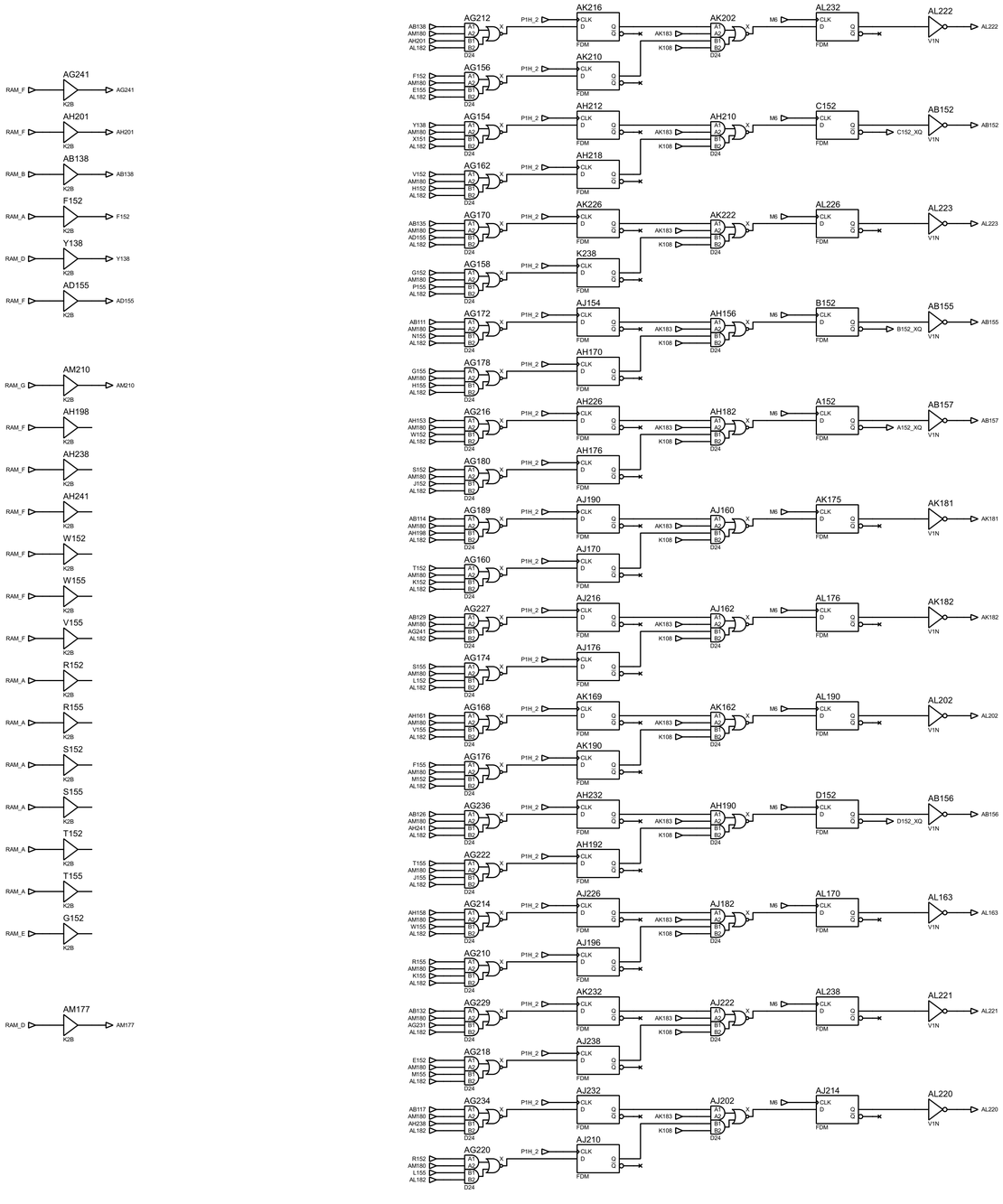

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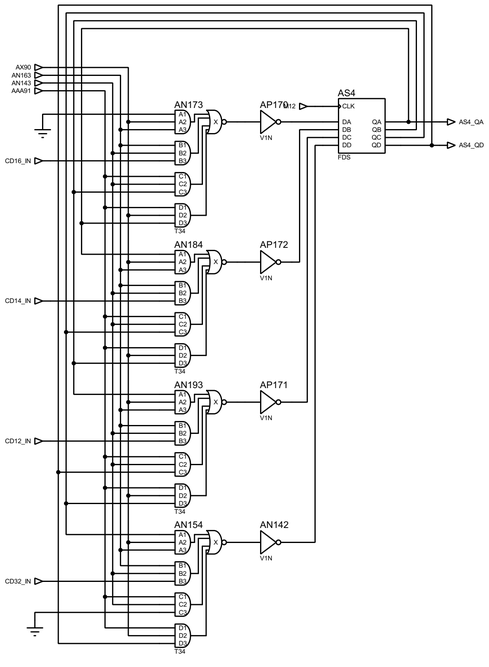

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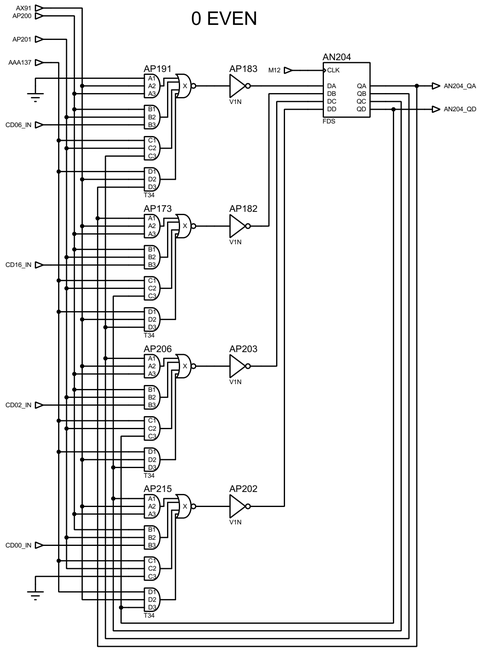

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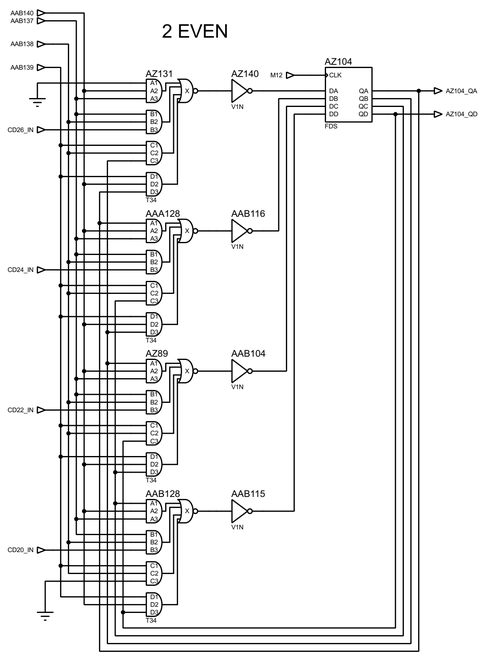

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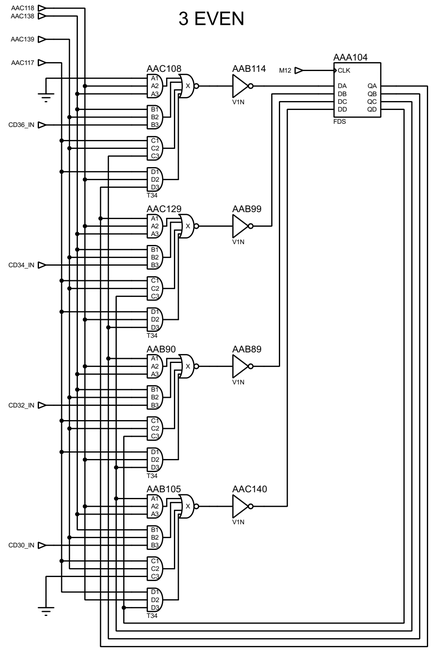

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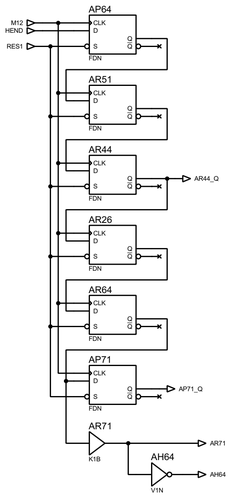

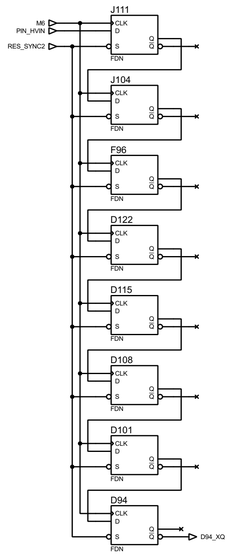

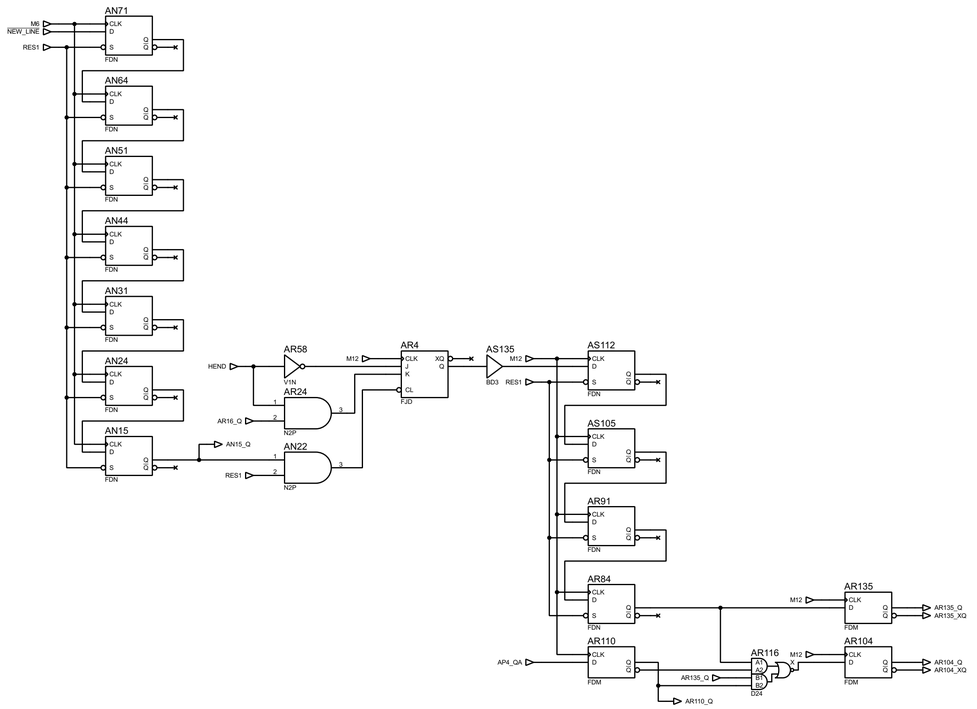

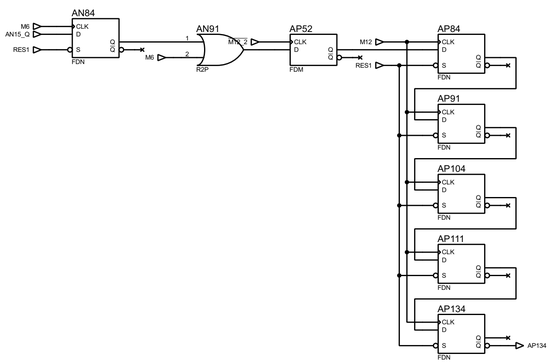

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