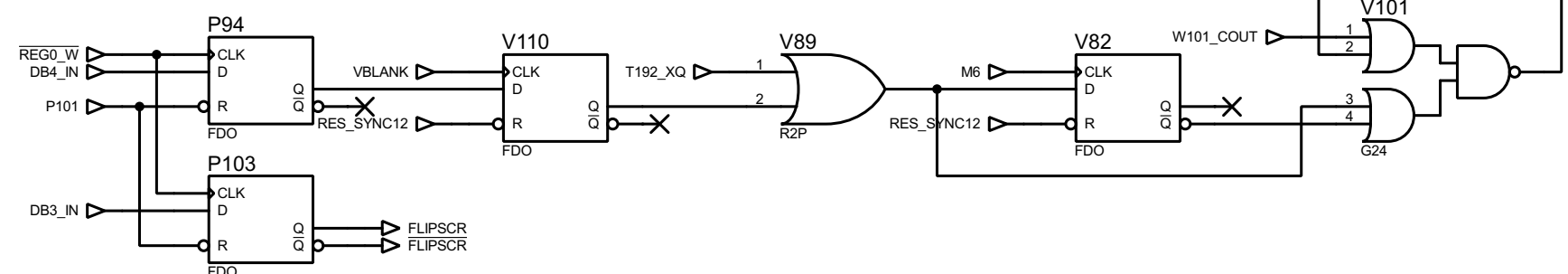
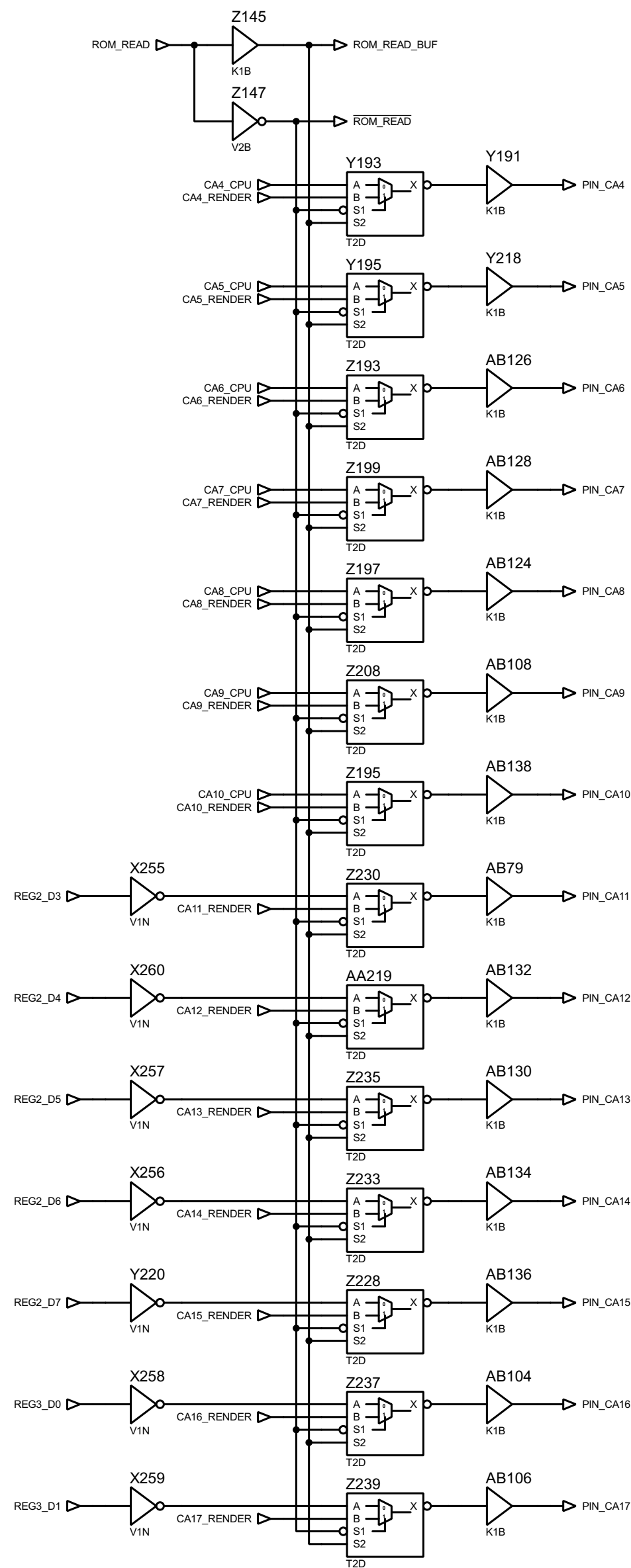
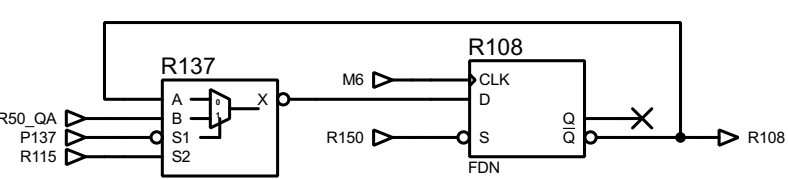
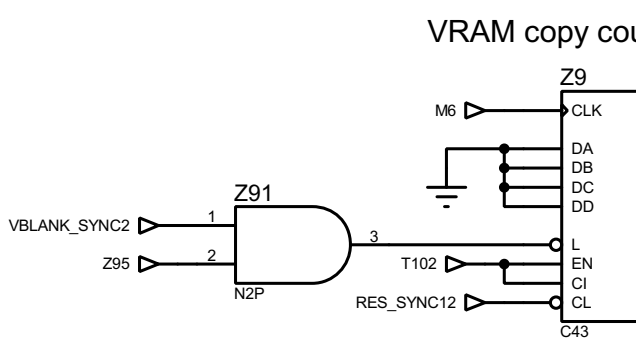
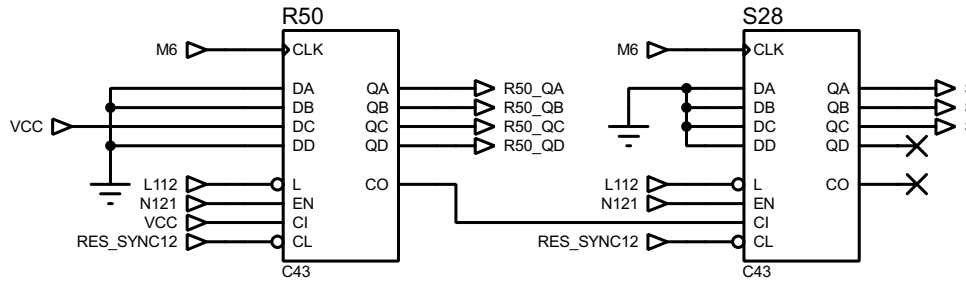
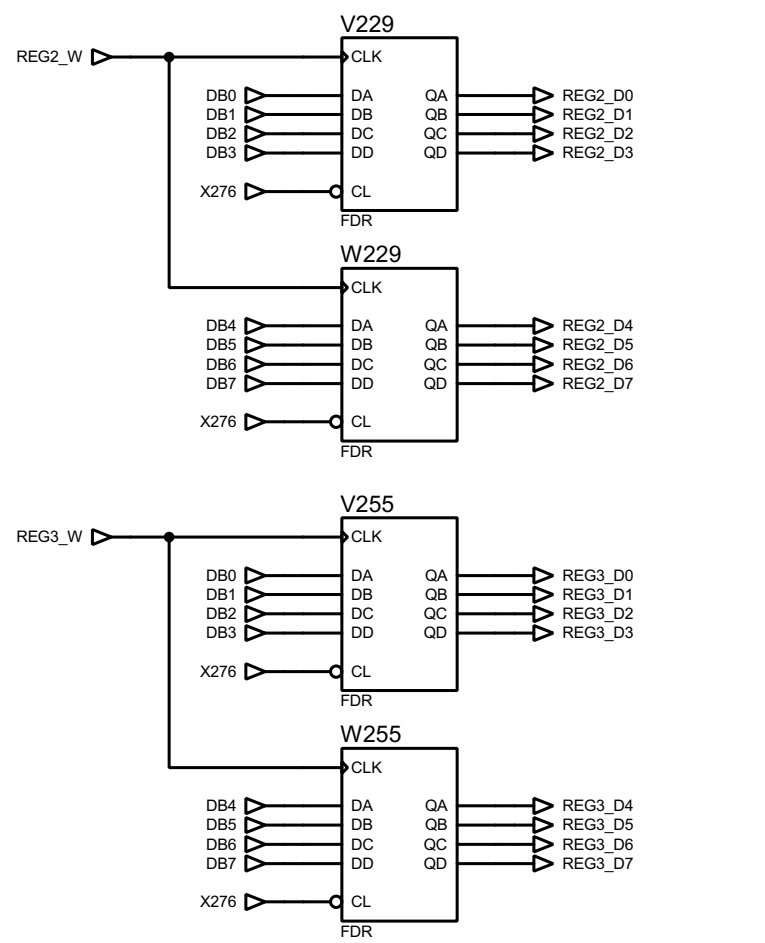
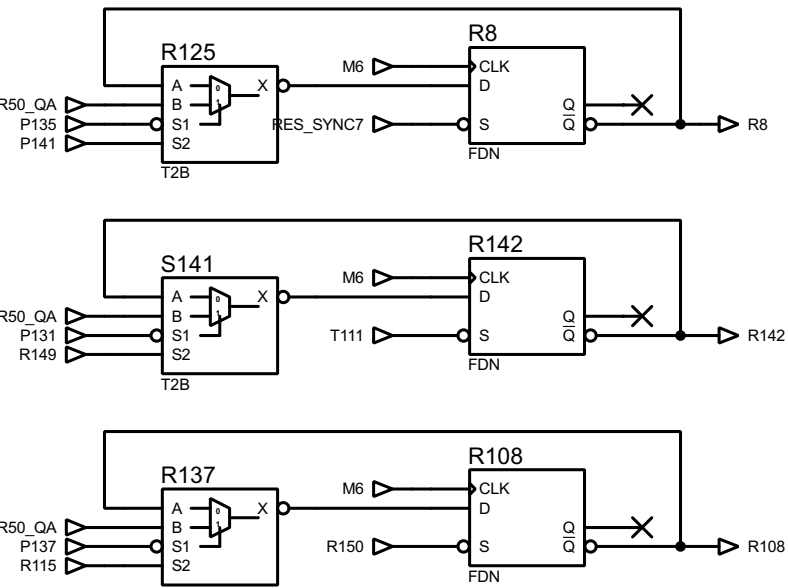
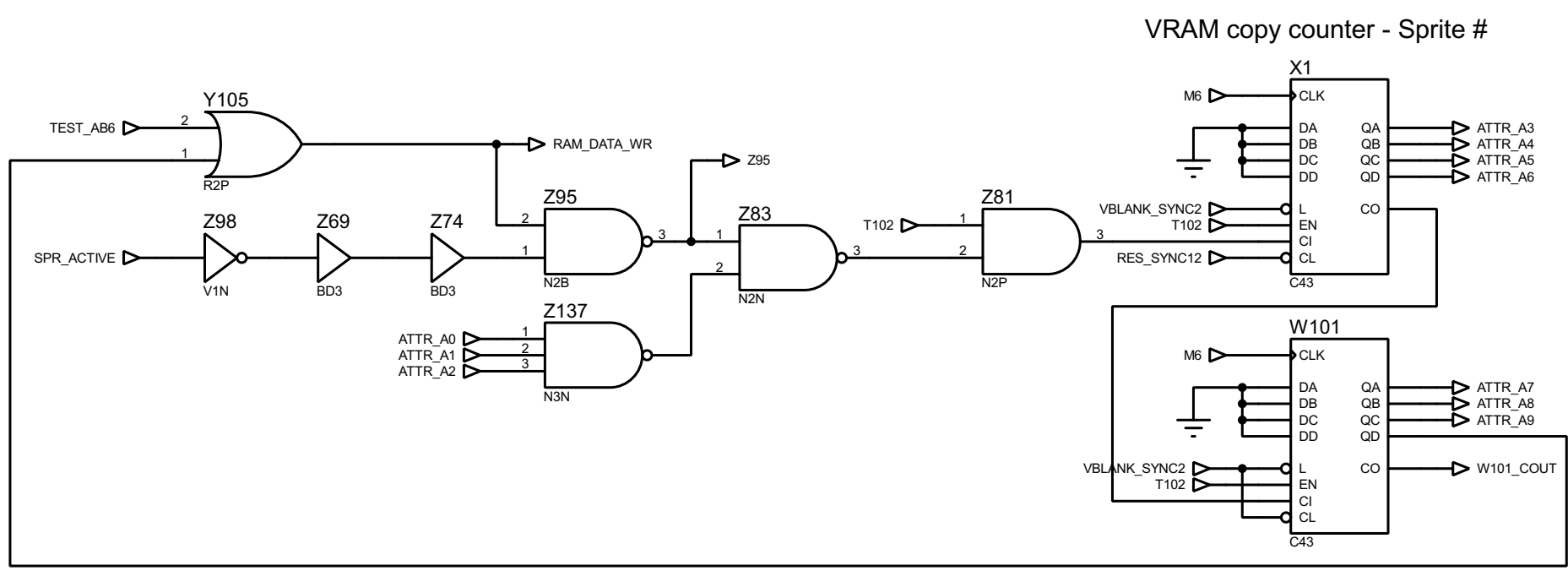
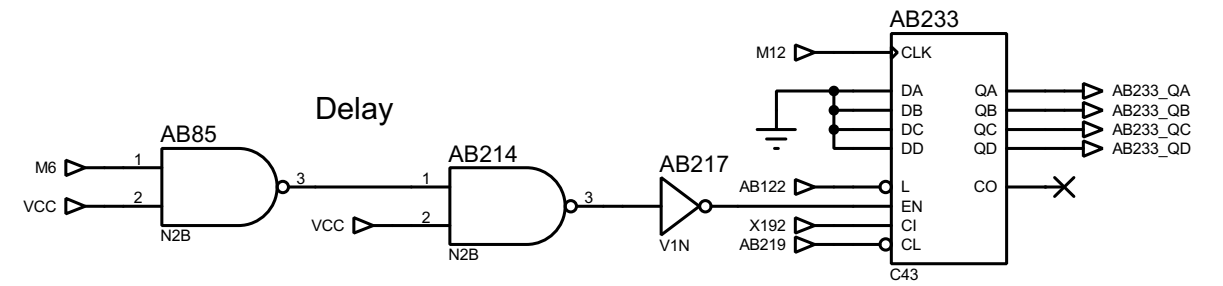
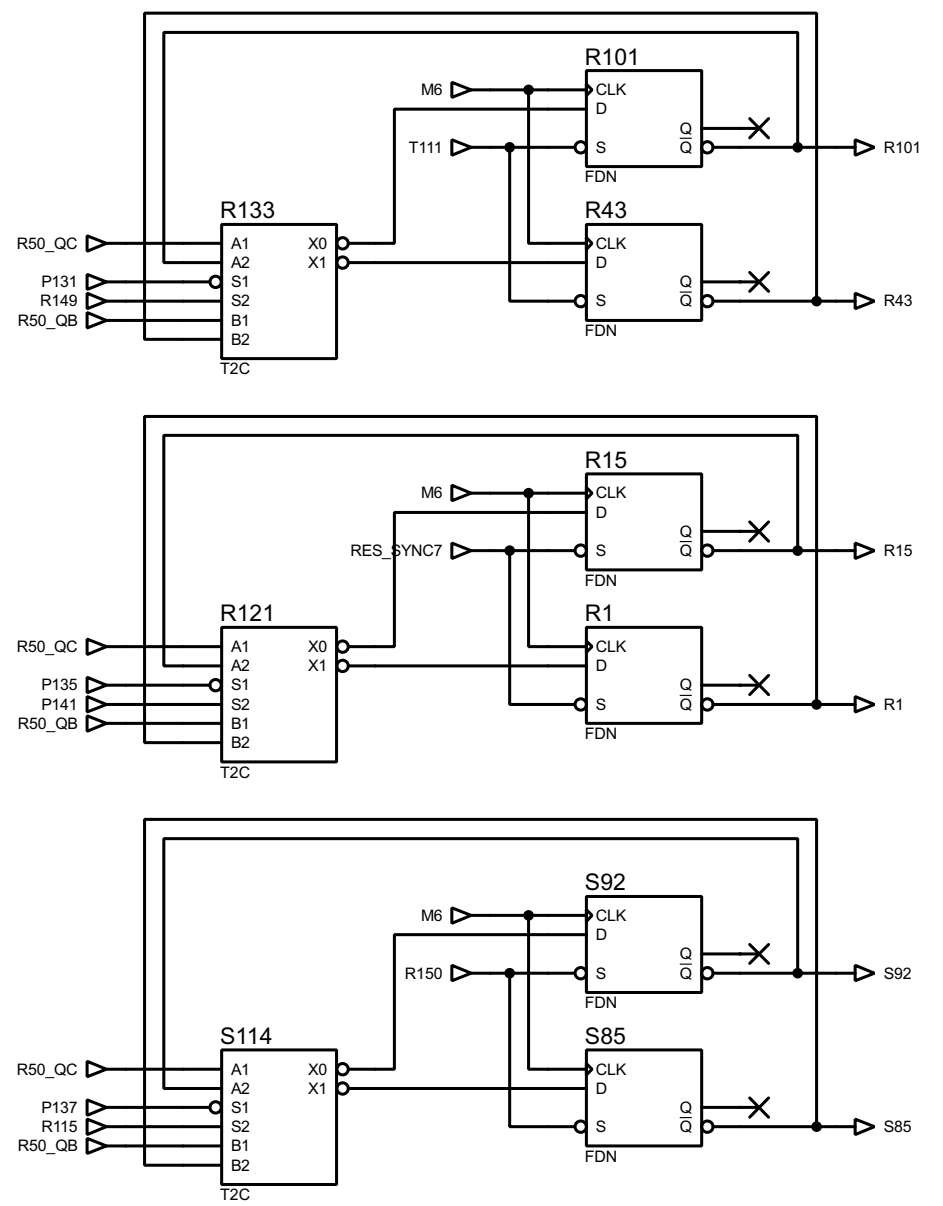
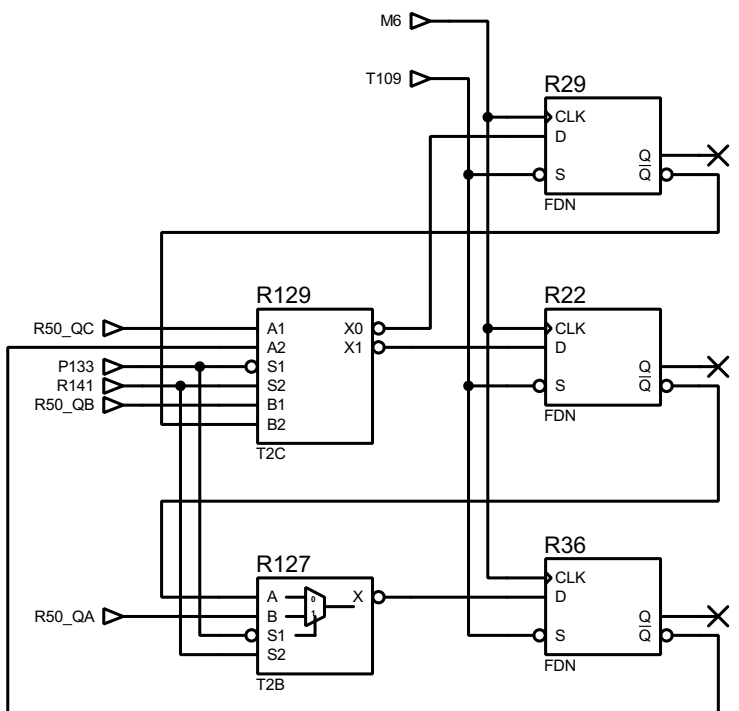
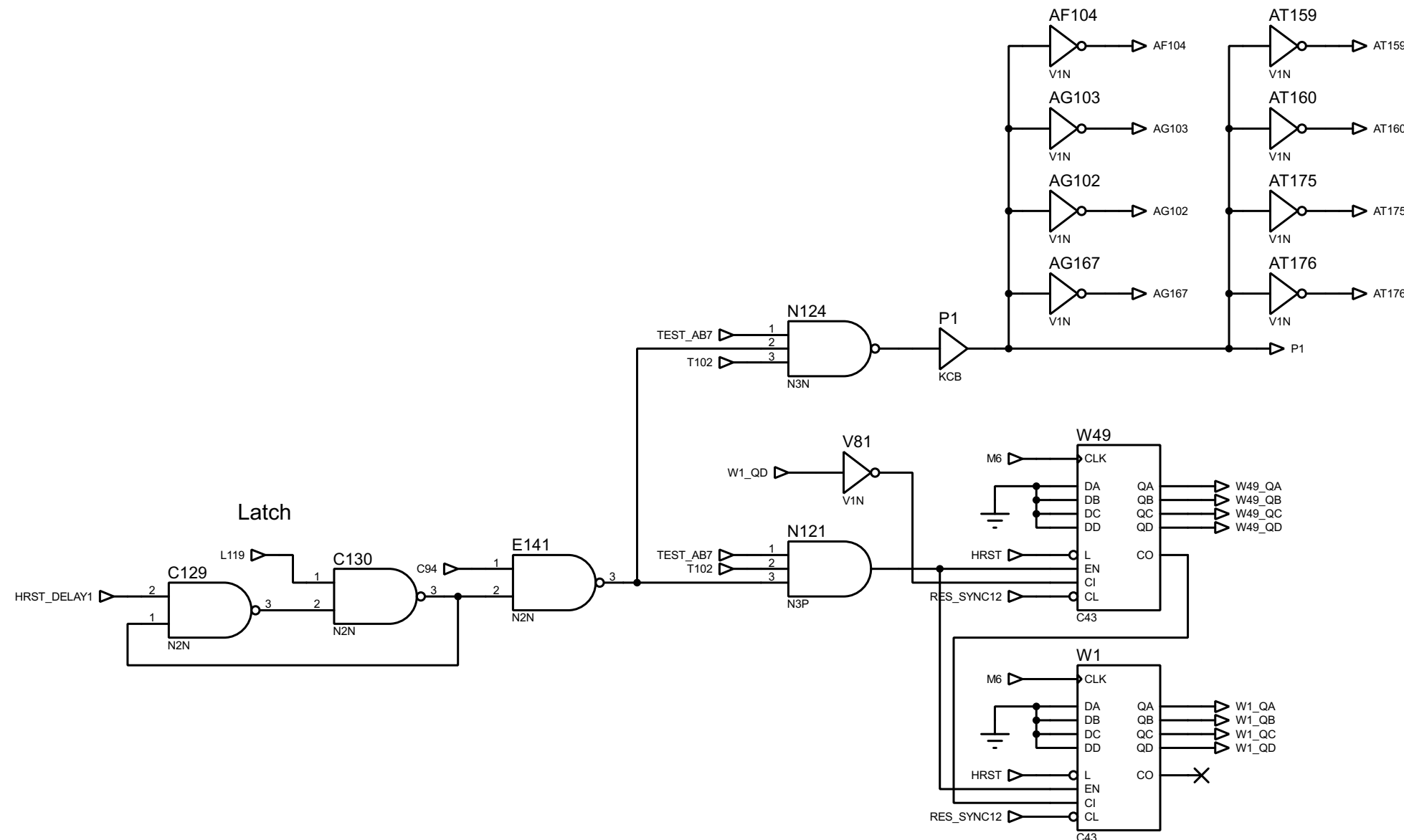
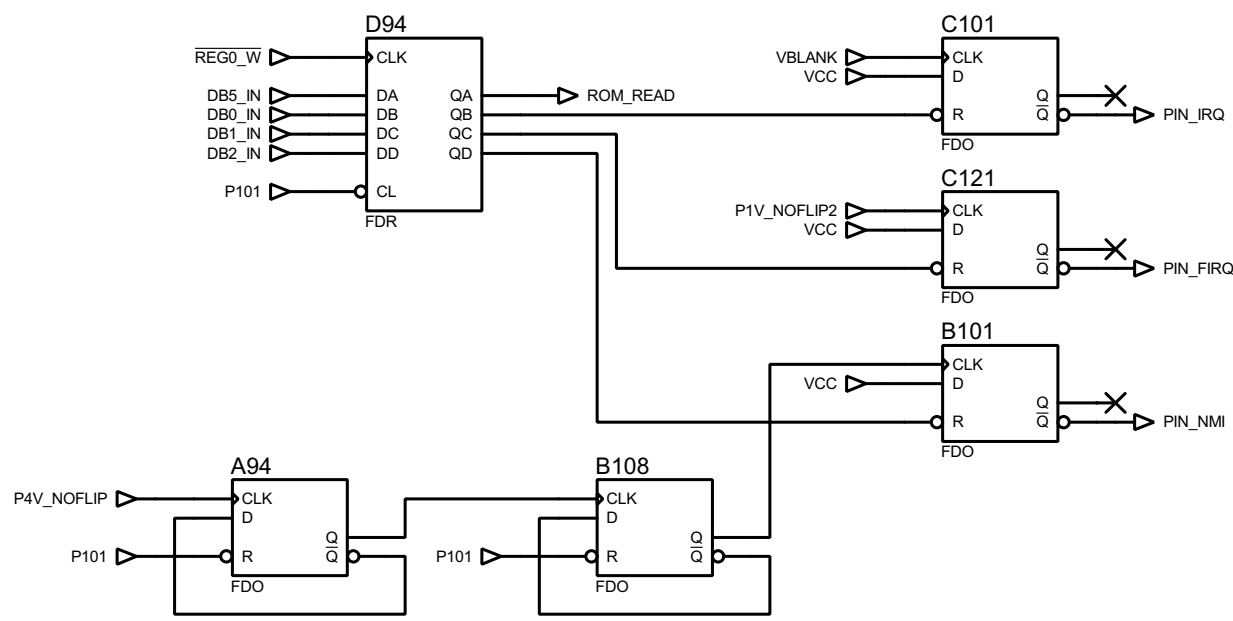


Reg0 D3 and D4 not documented in MAME

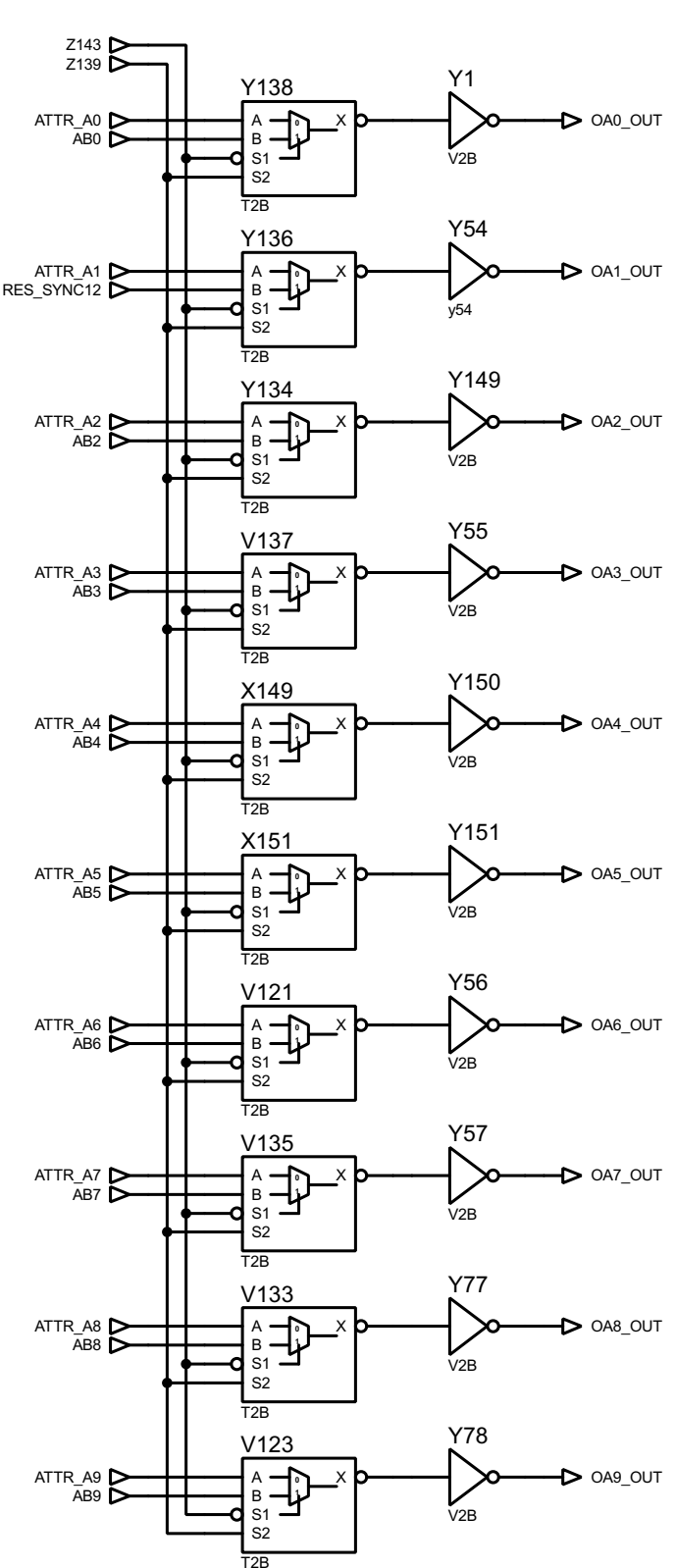


Select address normal mode/ROM read

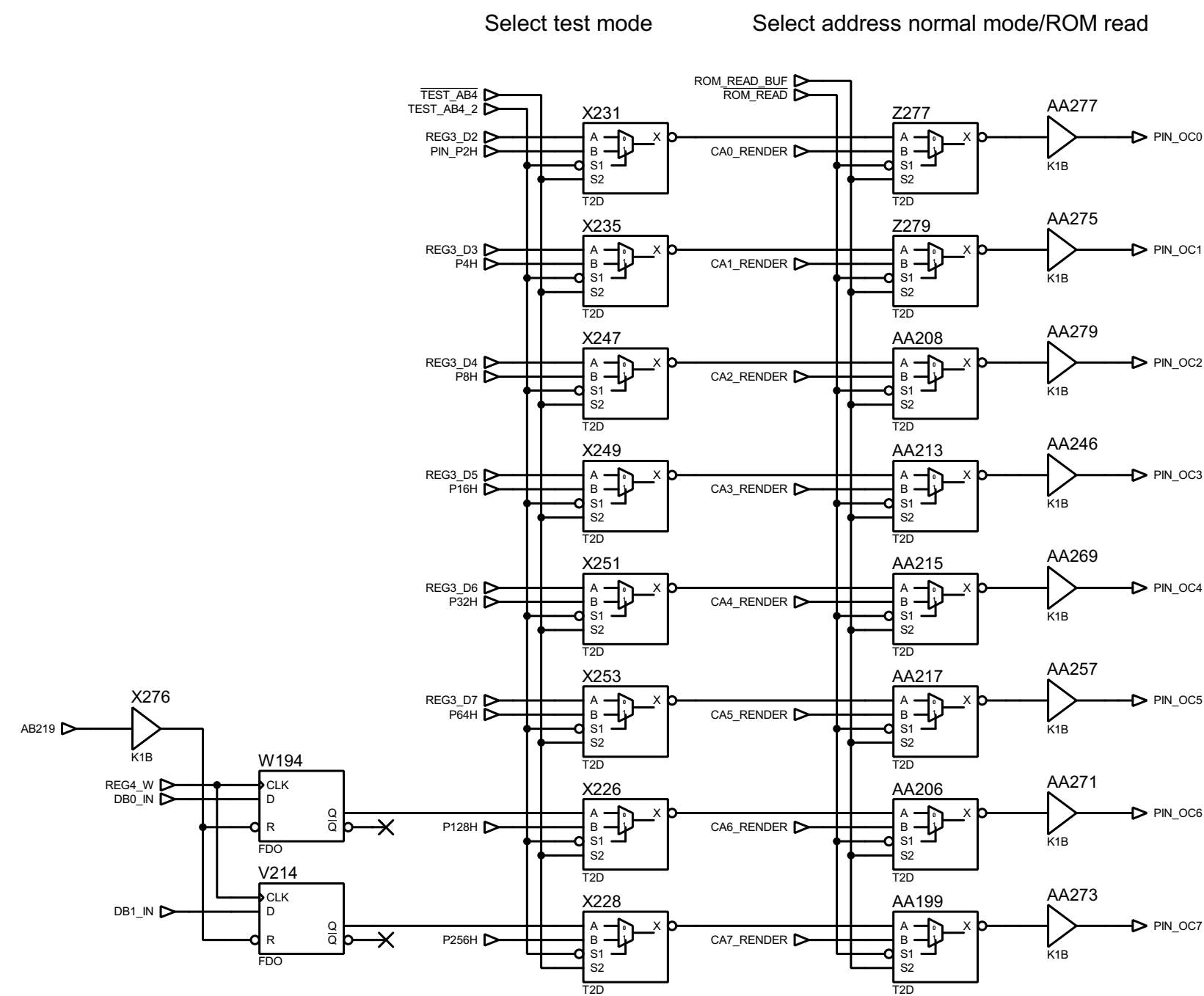
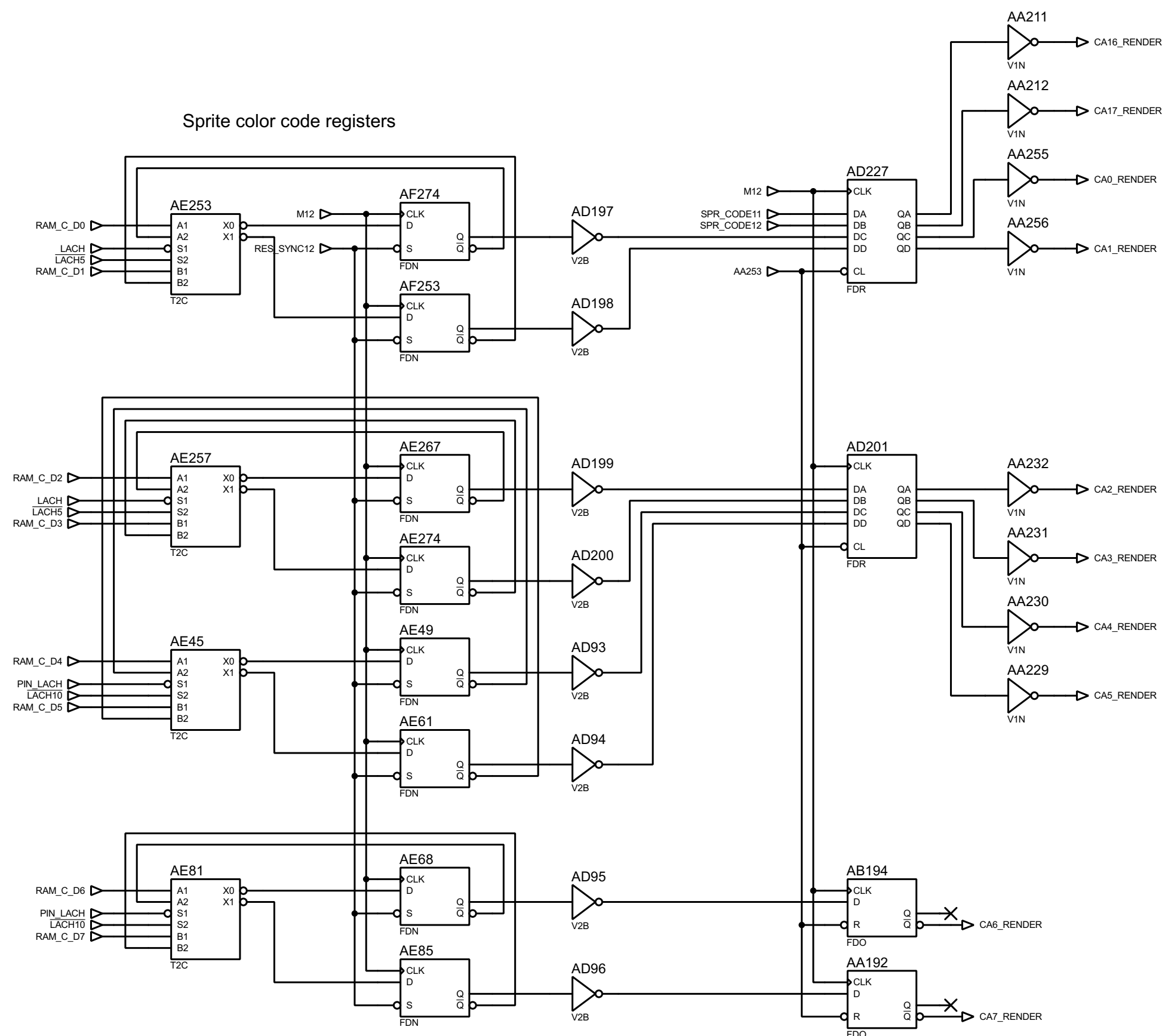
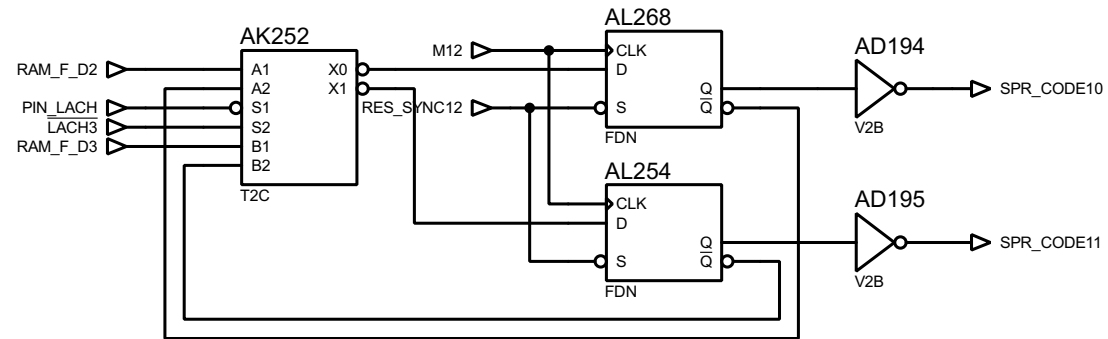
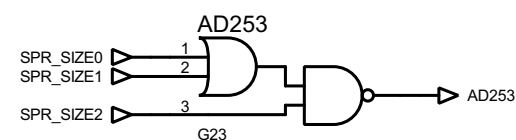
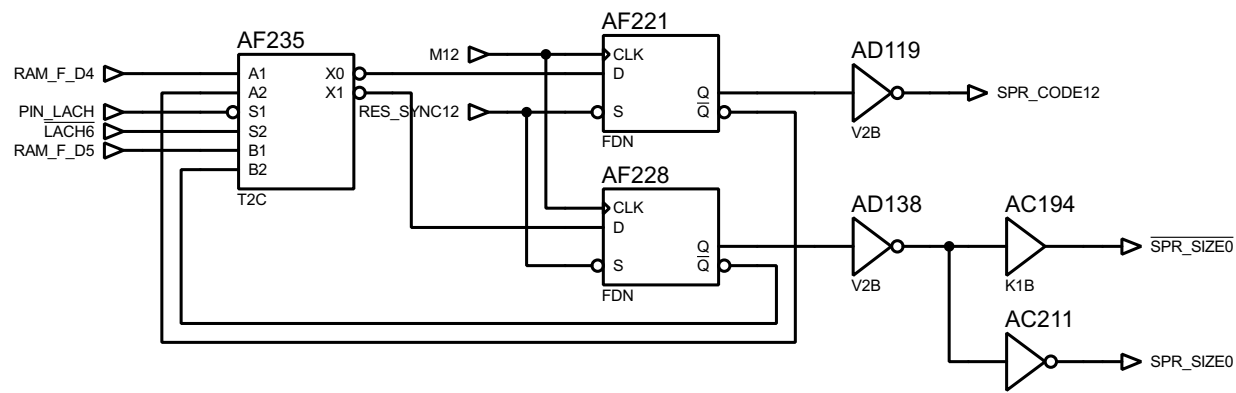
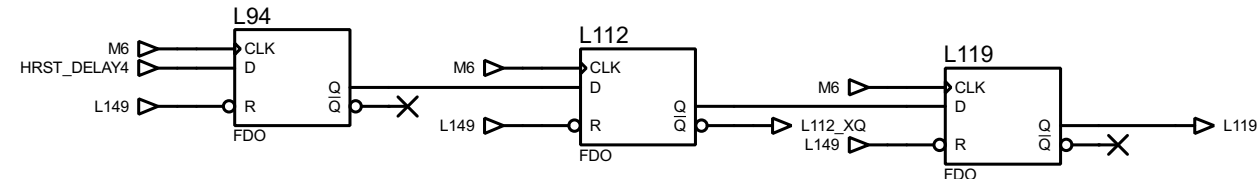
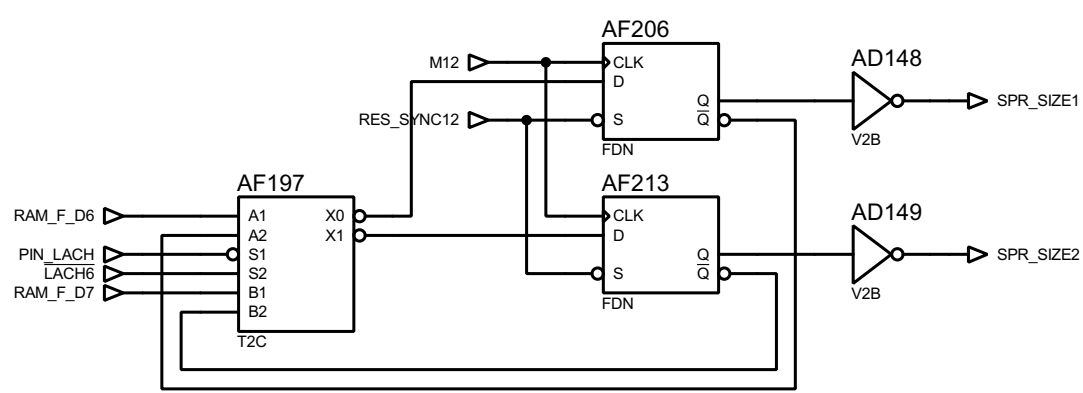


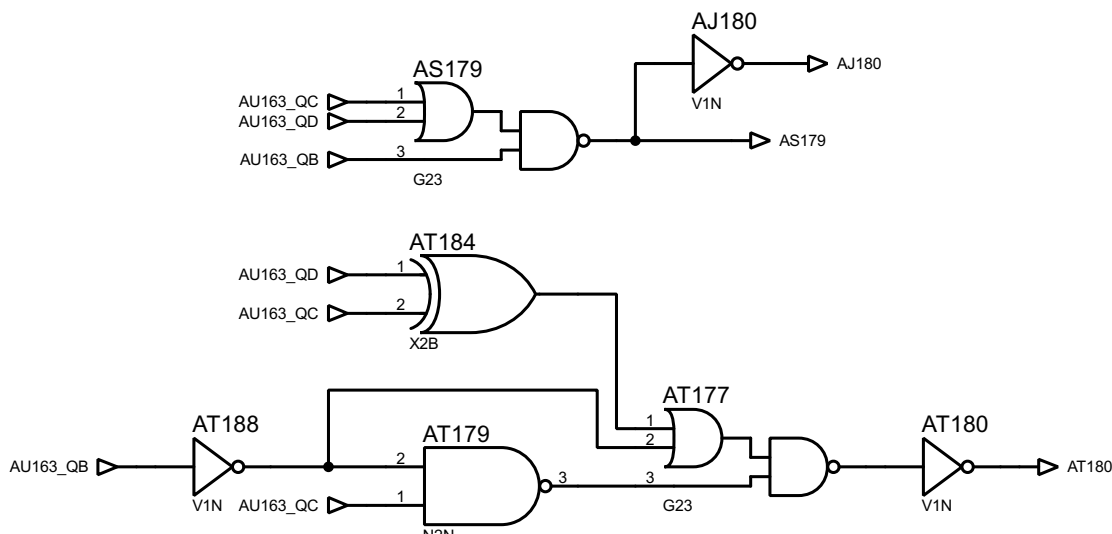
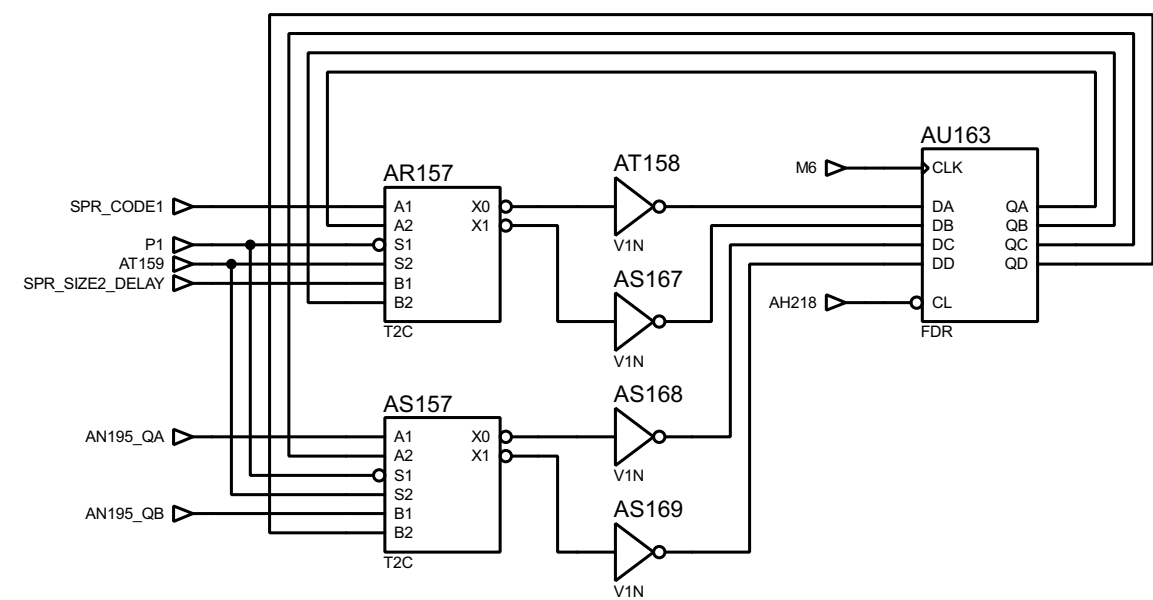
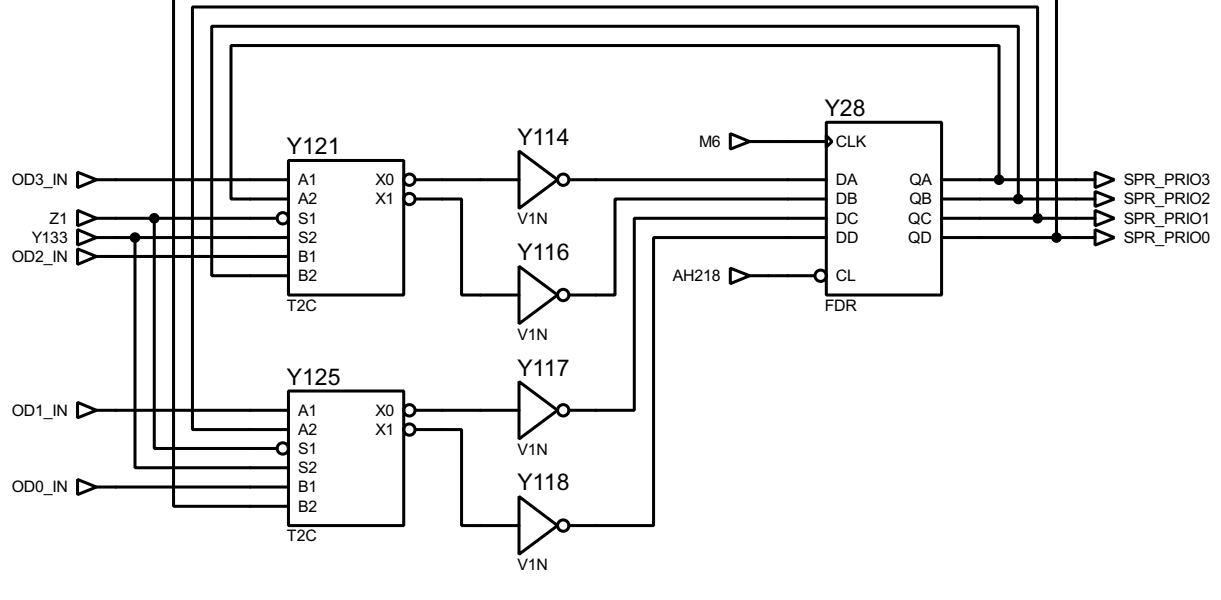
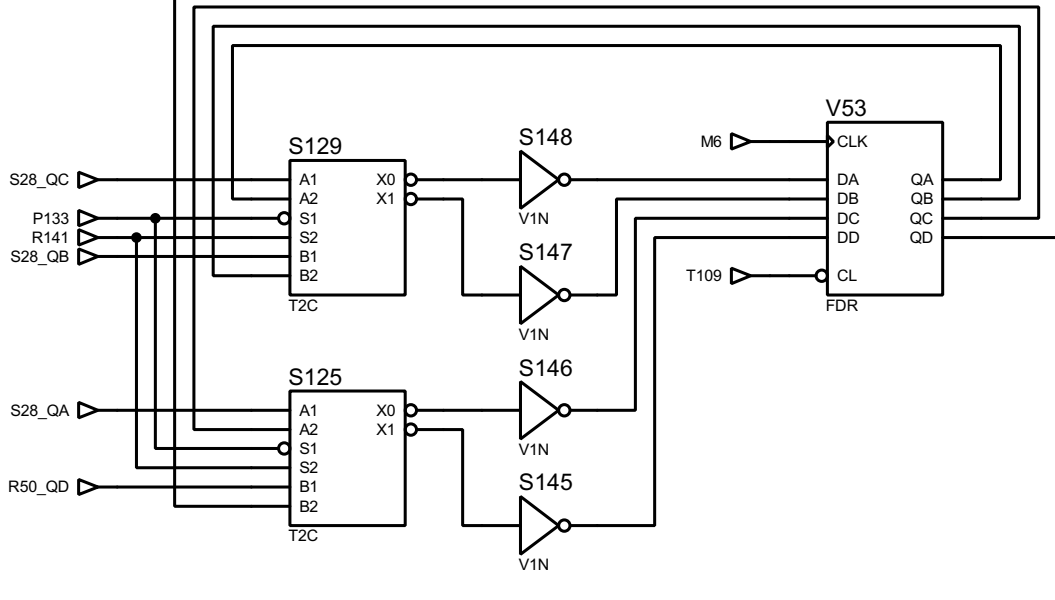
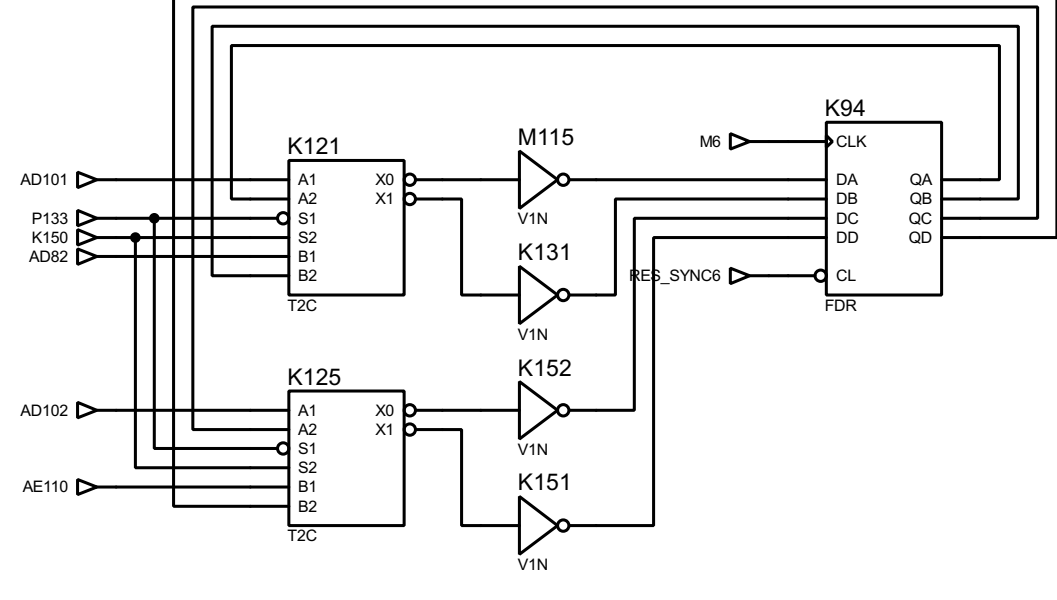
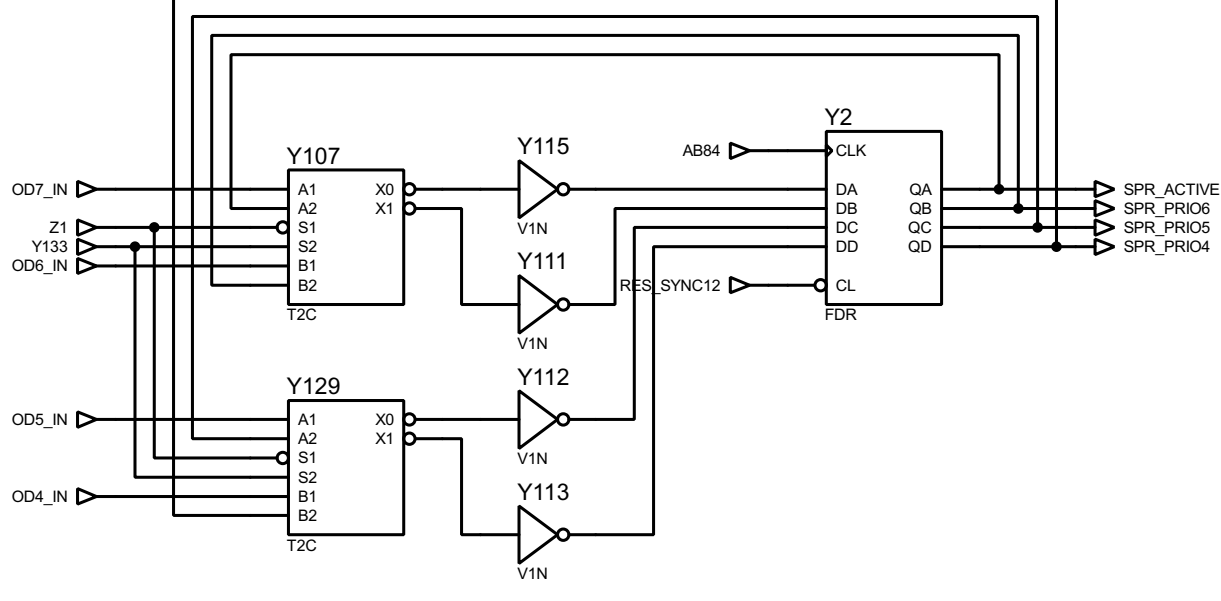
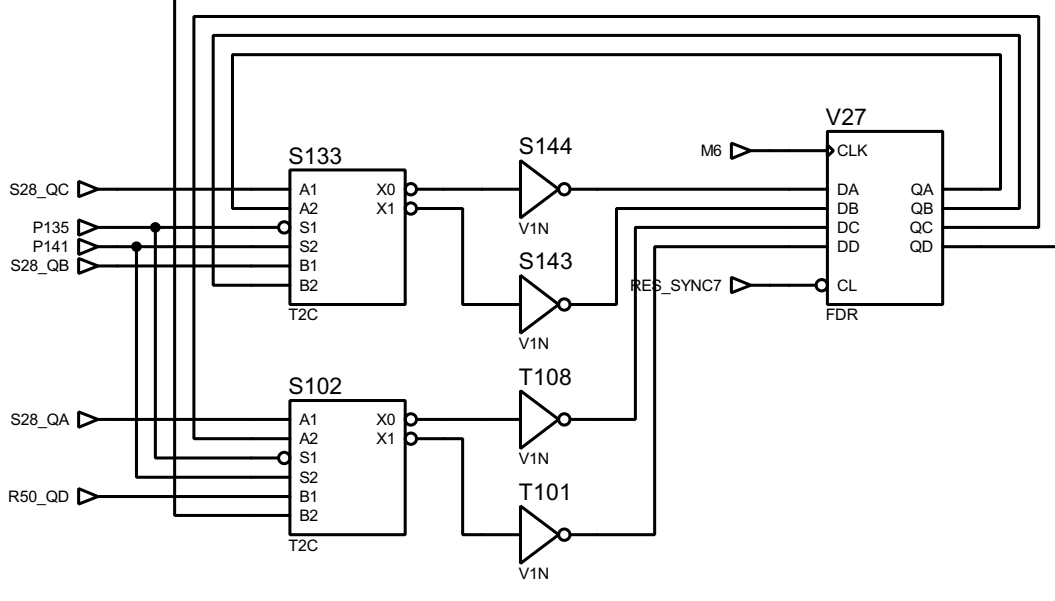
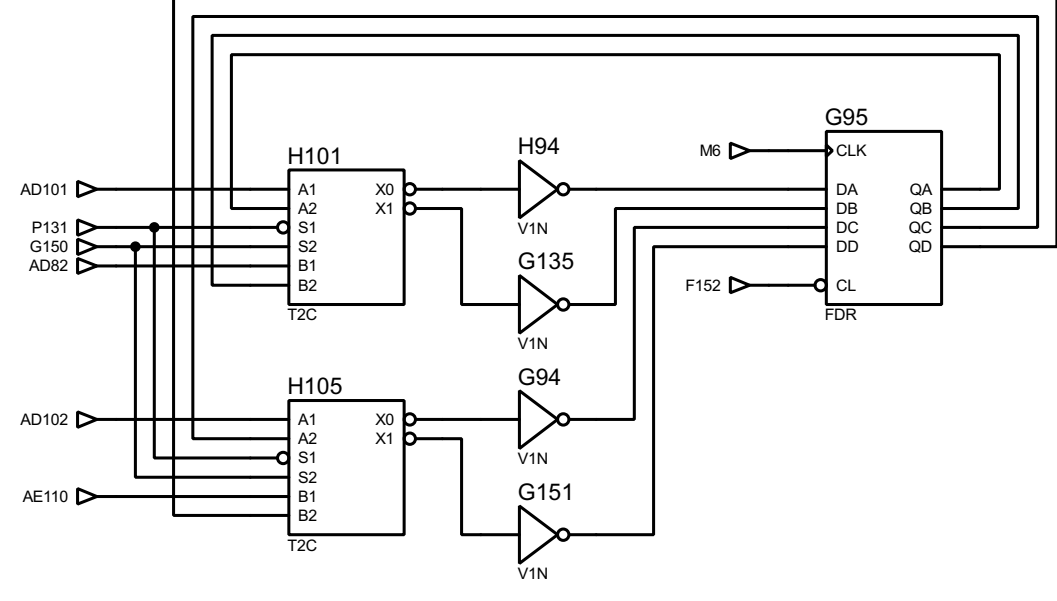
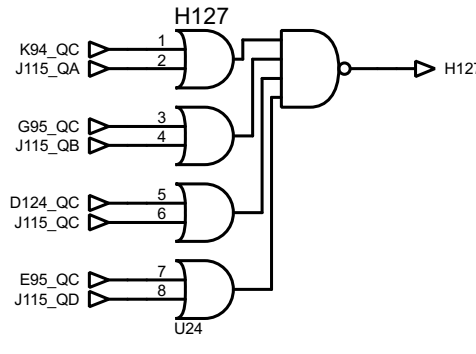
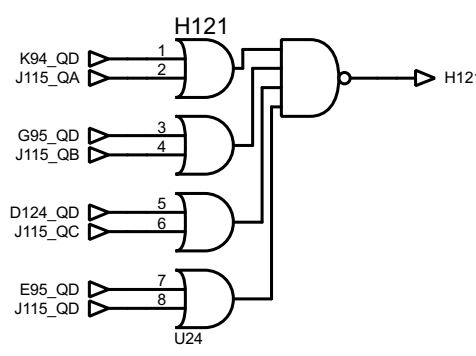
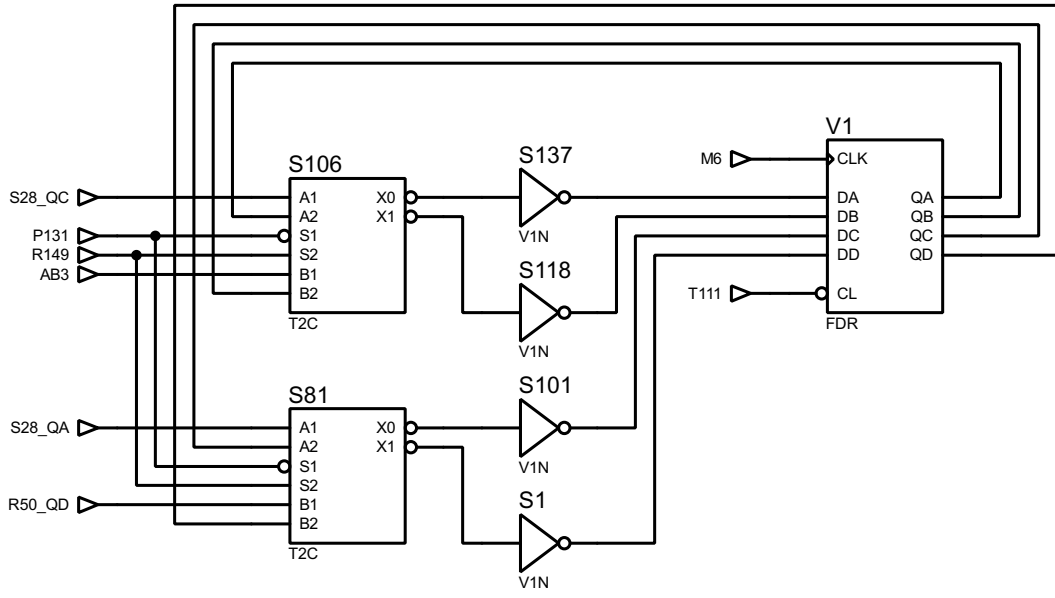
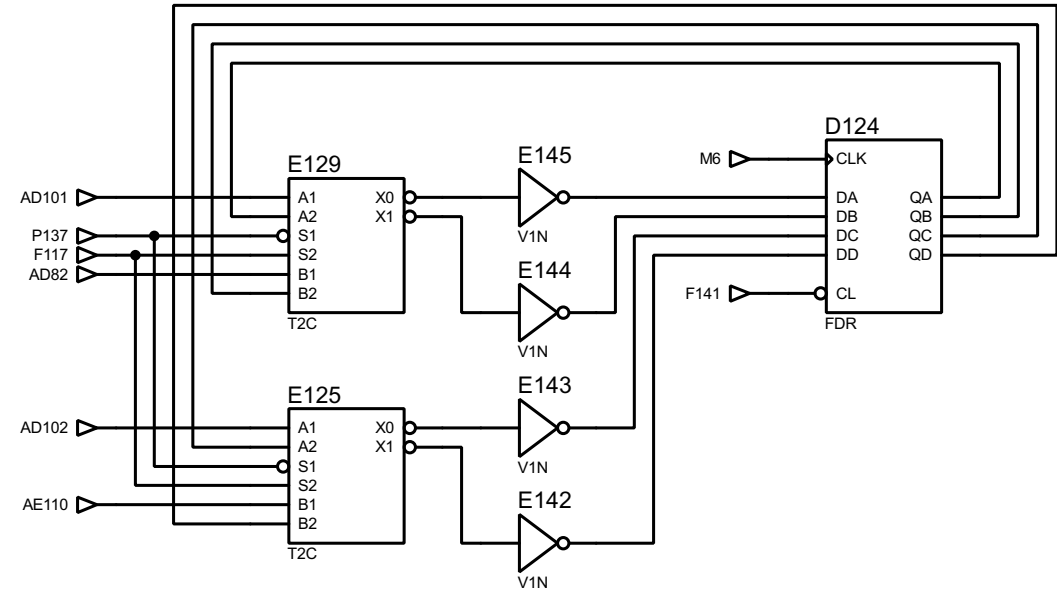
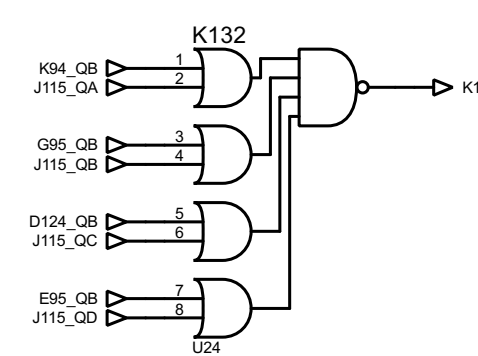
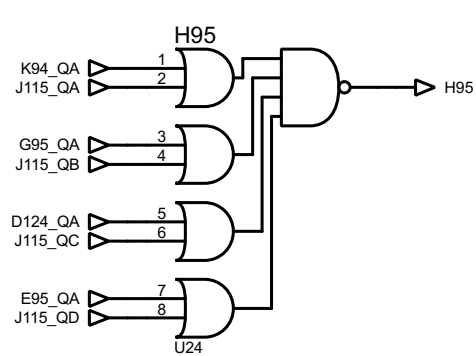
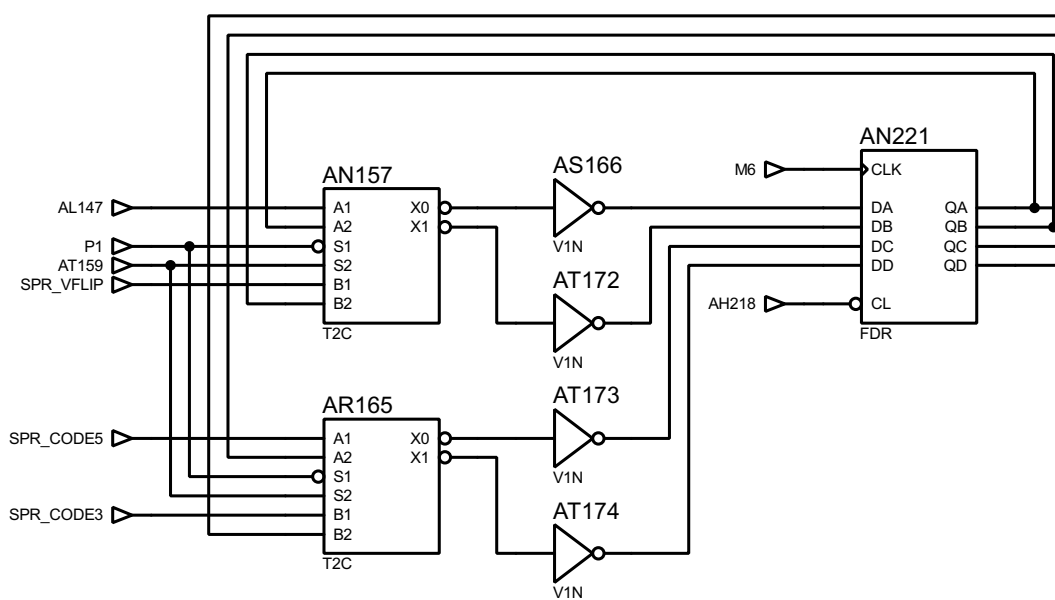
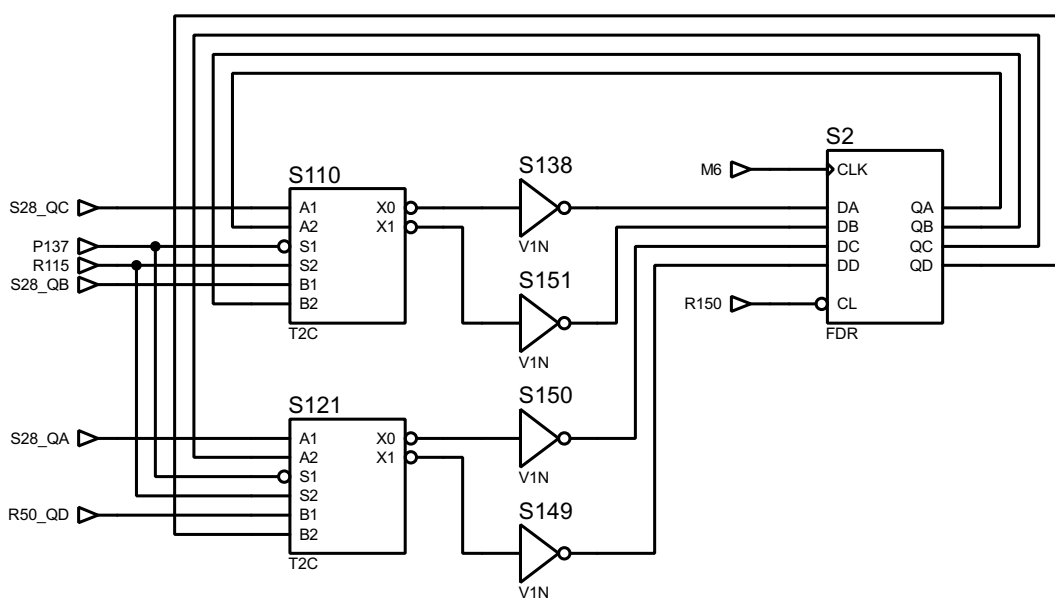
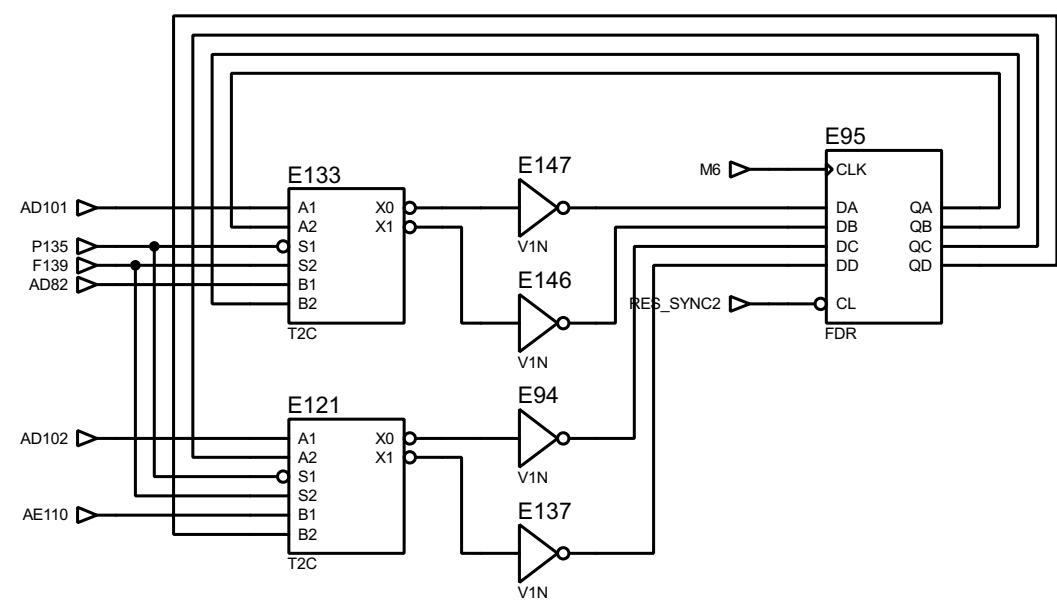


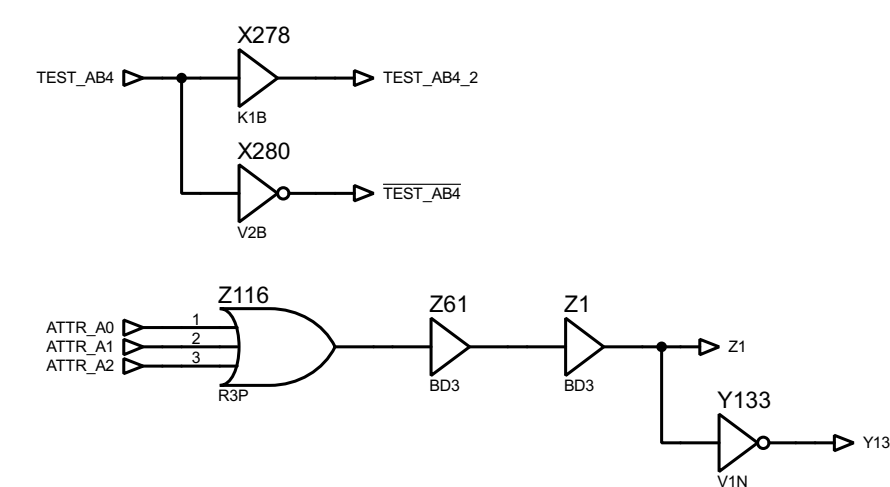
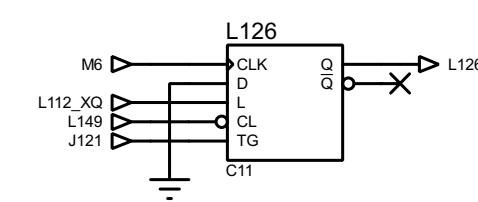
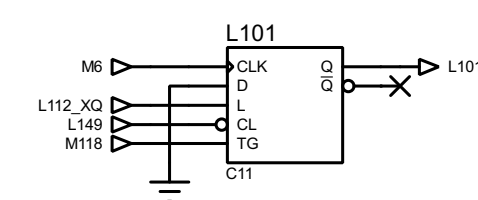
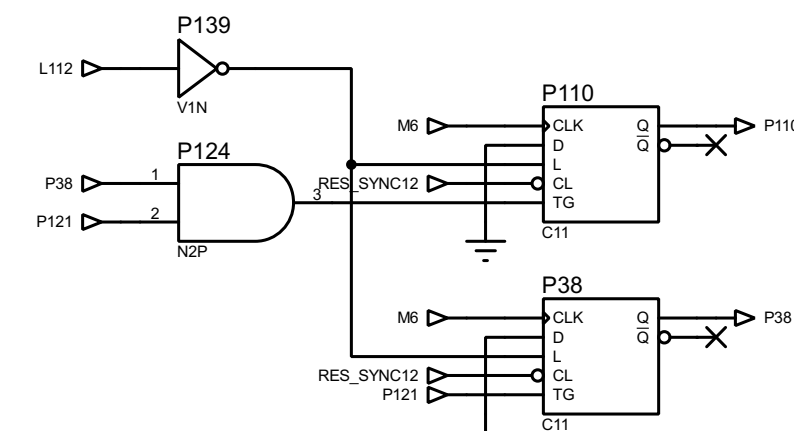
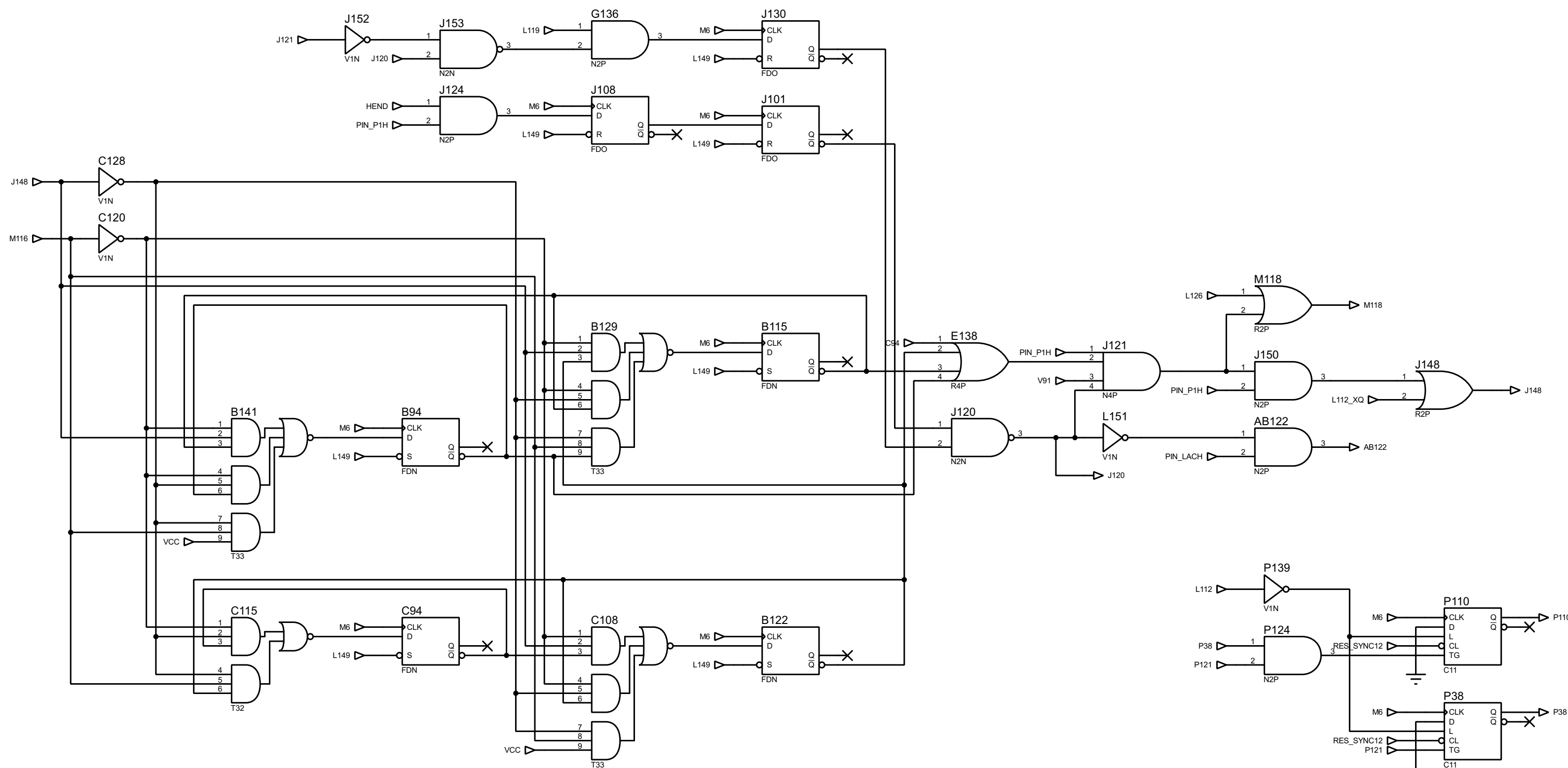
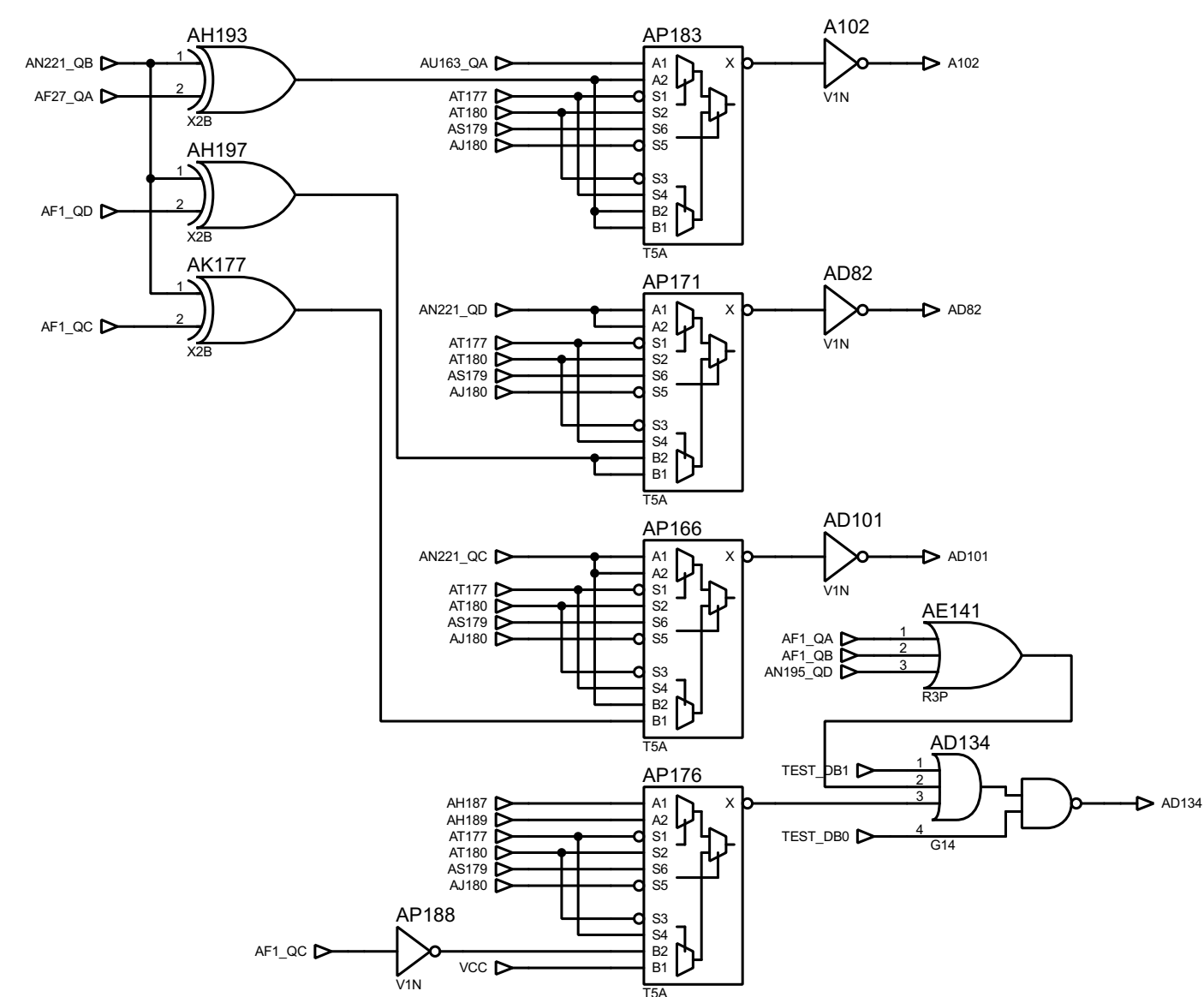
VRAM address select CPU/Video



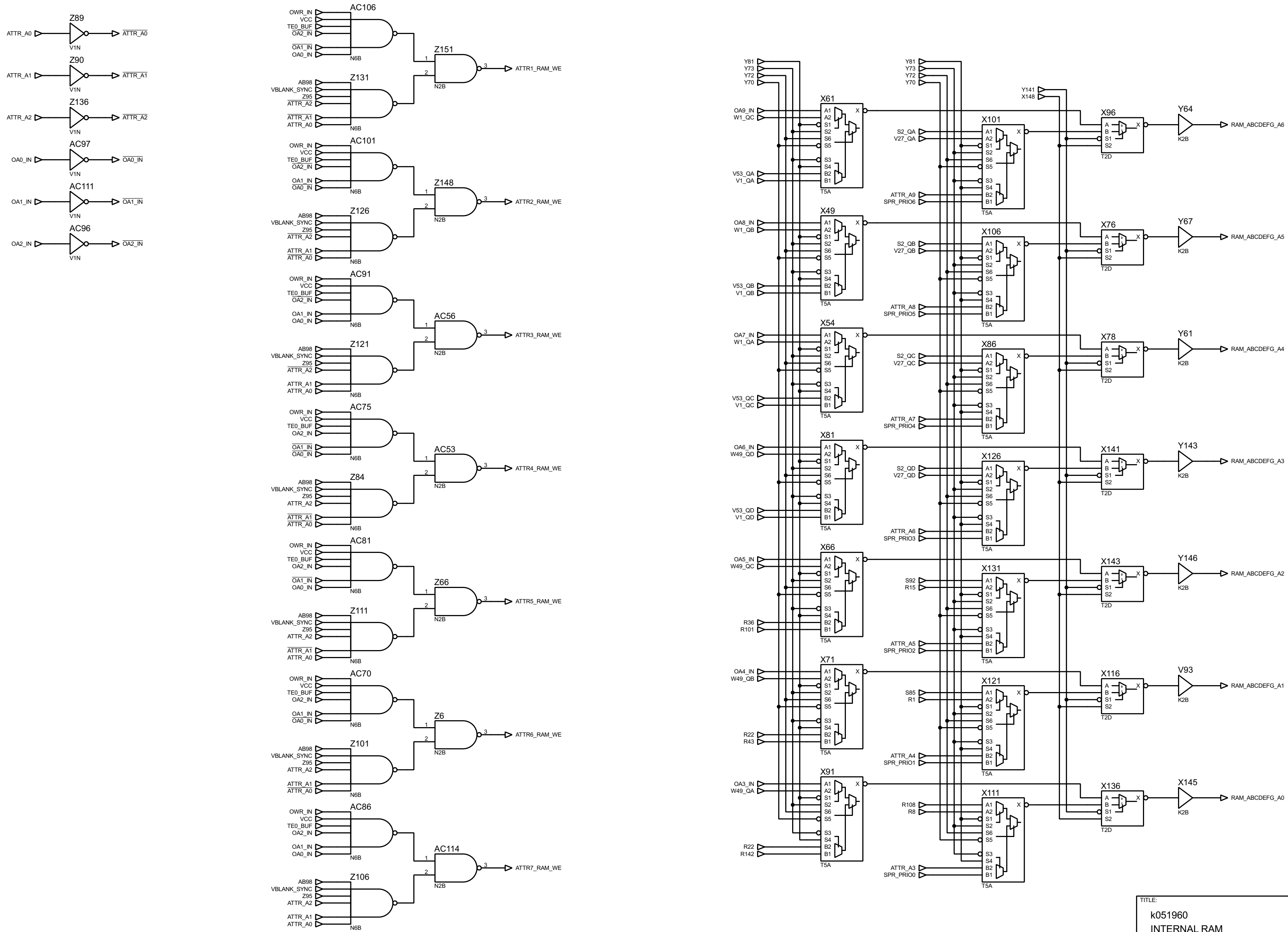


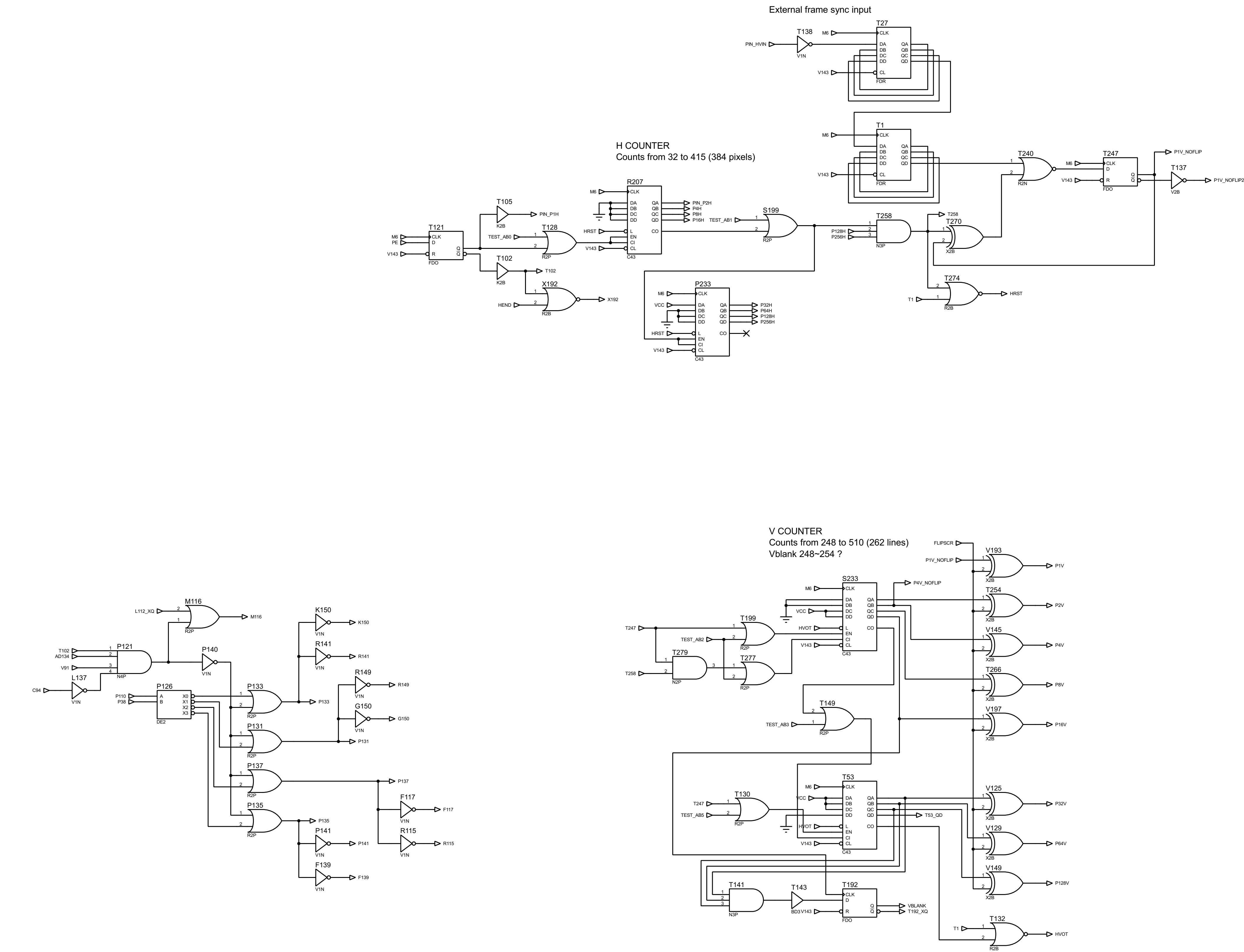






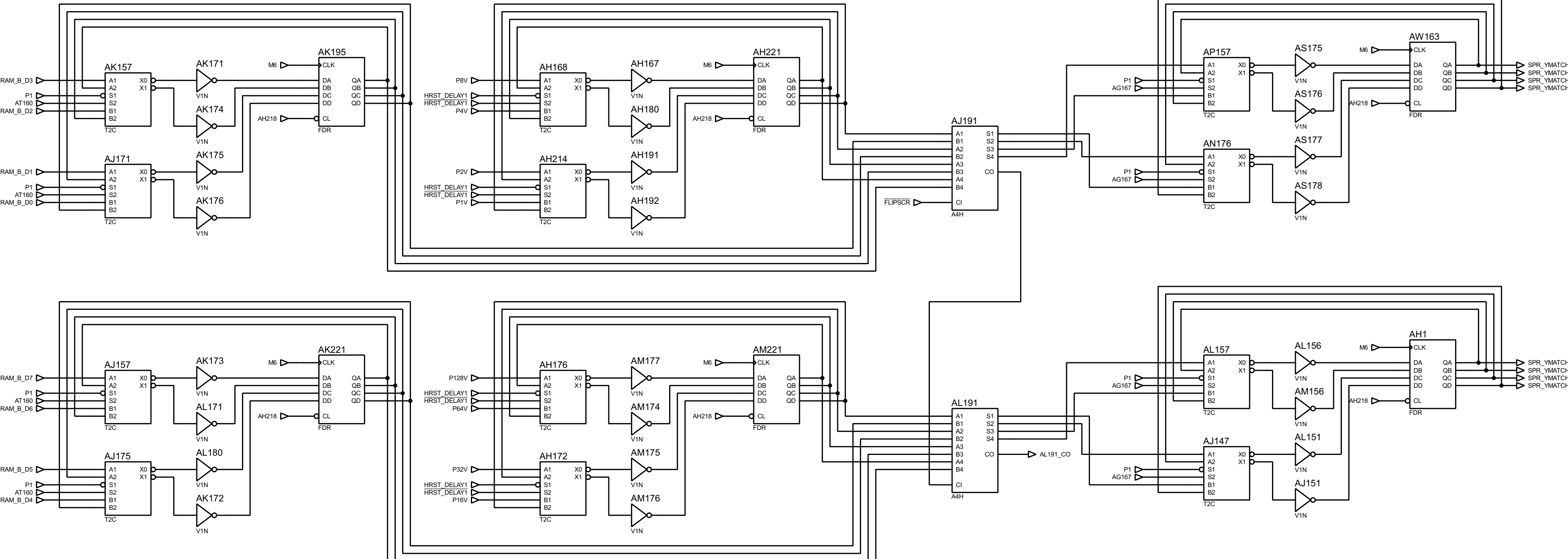
RAM block write enables
Top N6B is for test mode



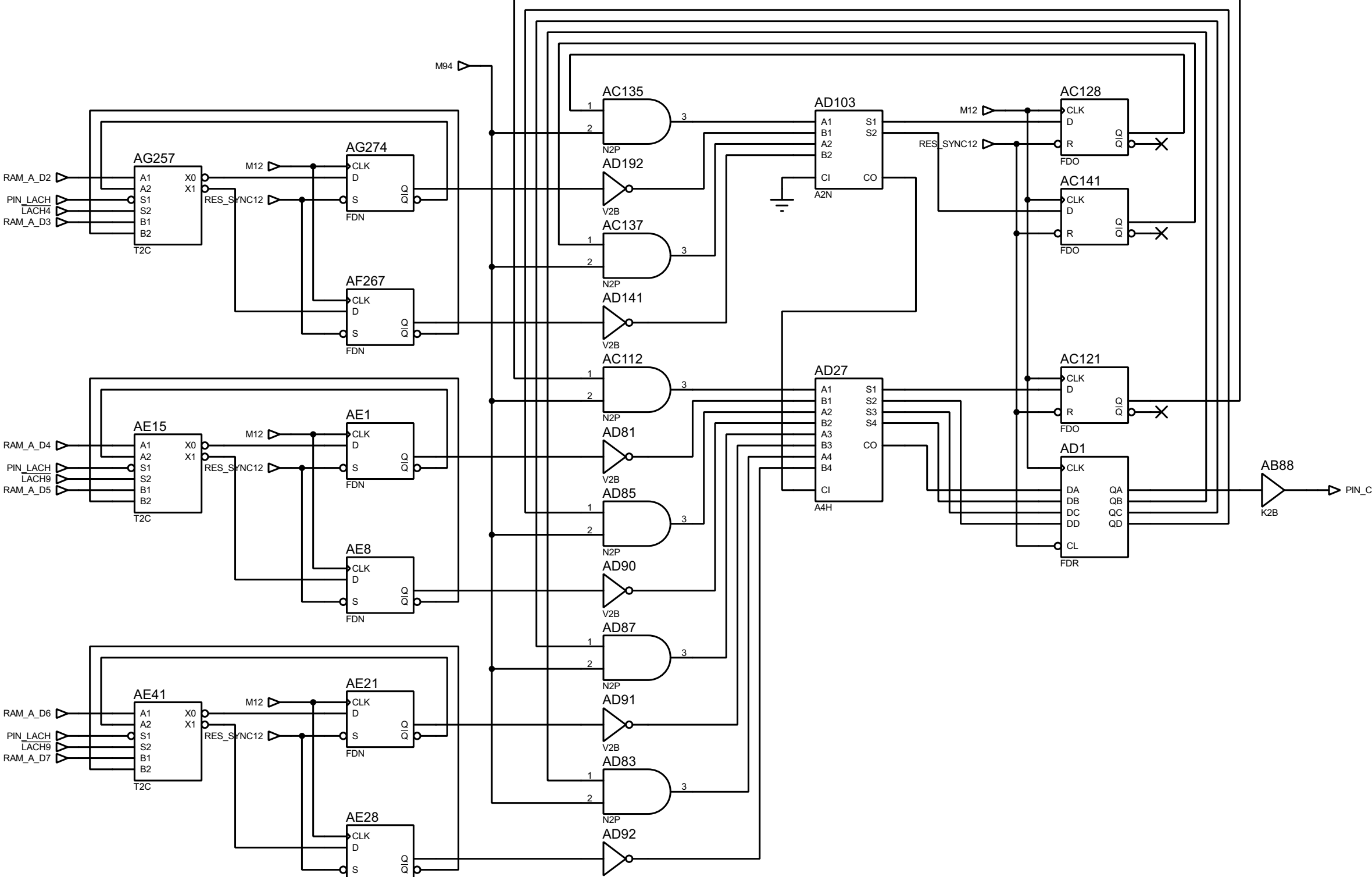


TITLE:	DATE:
K051960	20/01/2022
H/V COUNTERS	PAGE:
BY: Sean "Turtak" Gonsalves	REV: A
	11/14

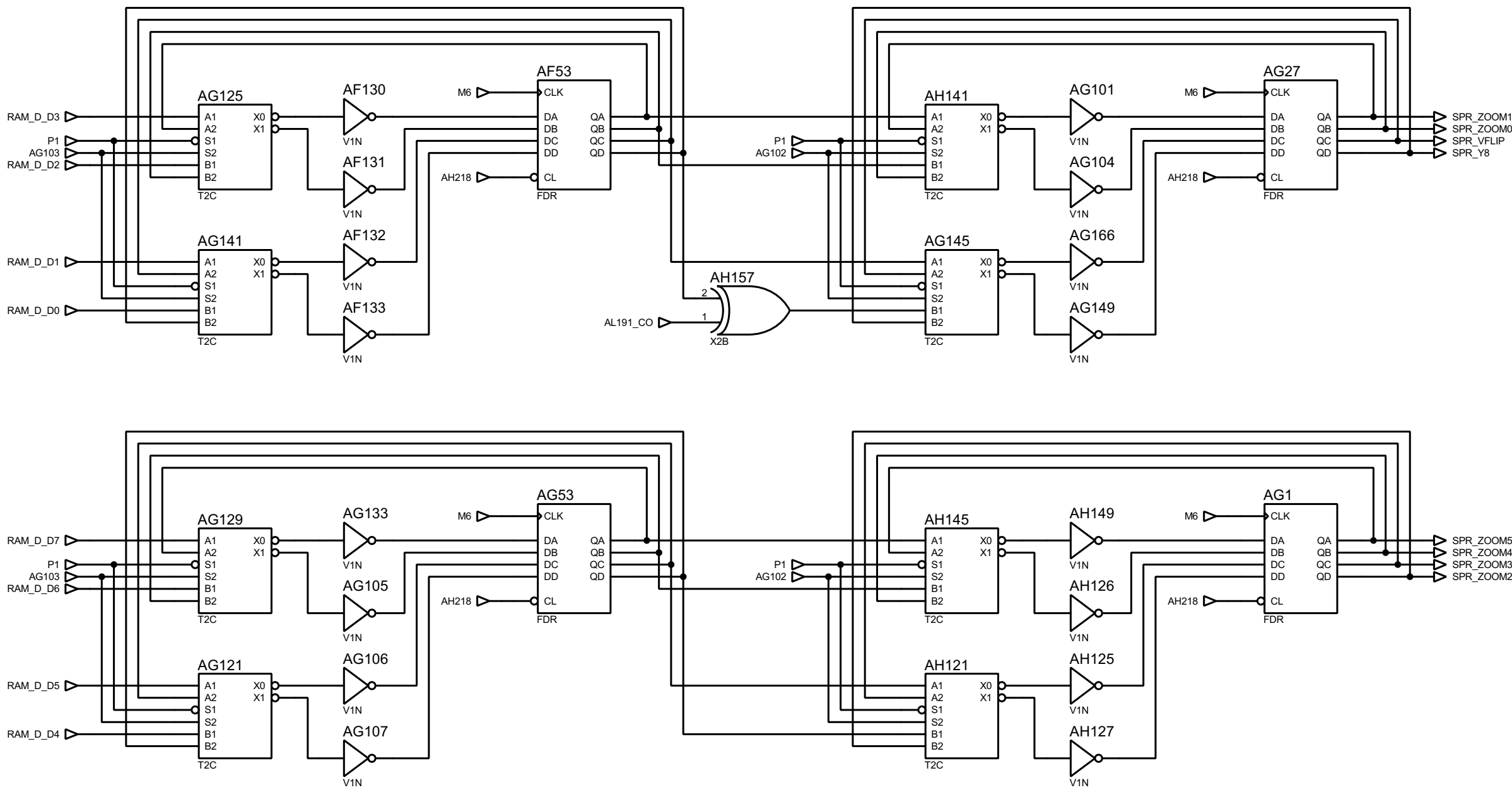
SPR Y POSITION



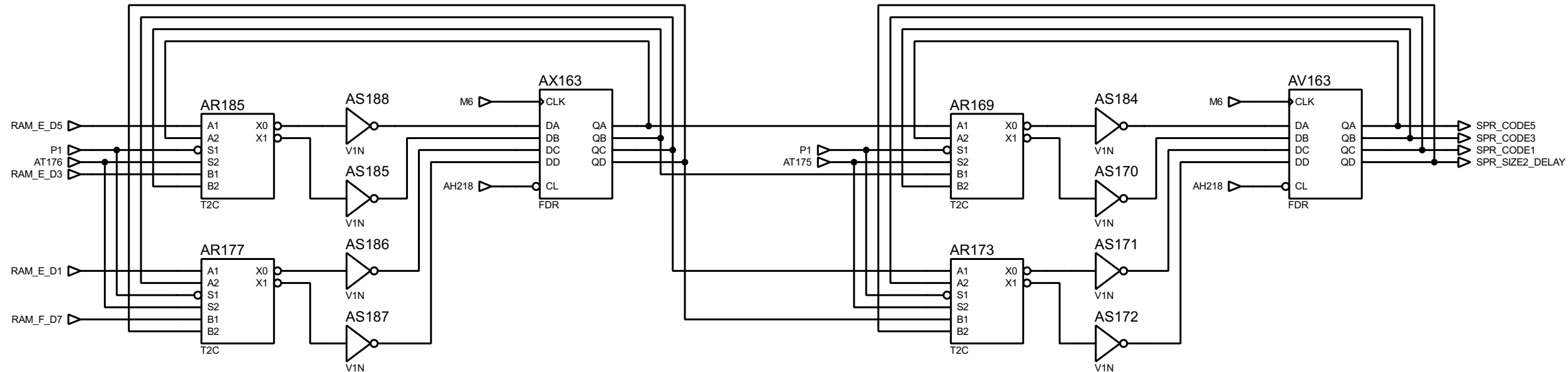
SPR X ZOOM



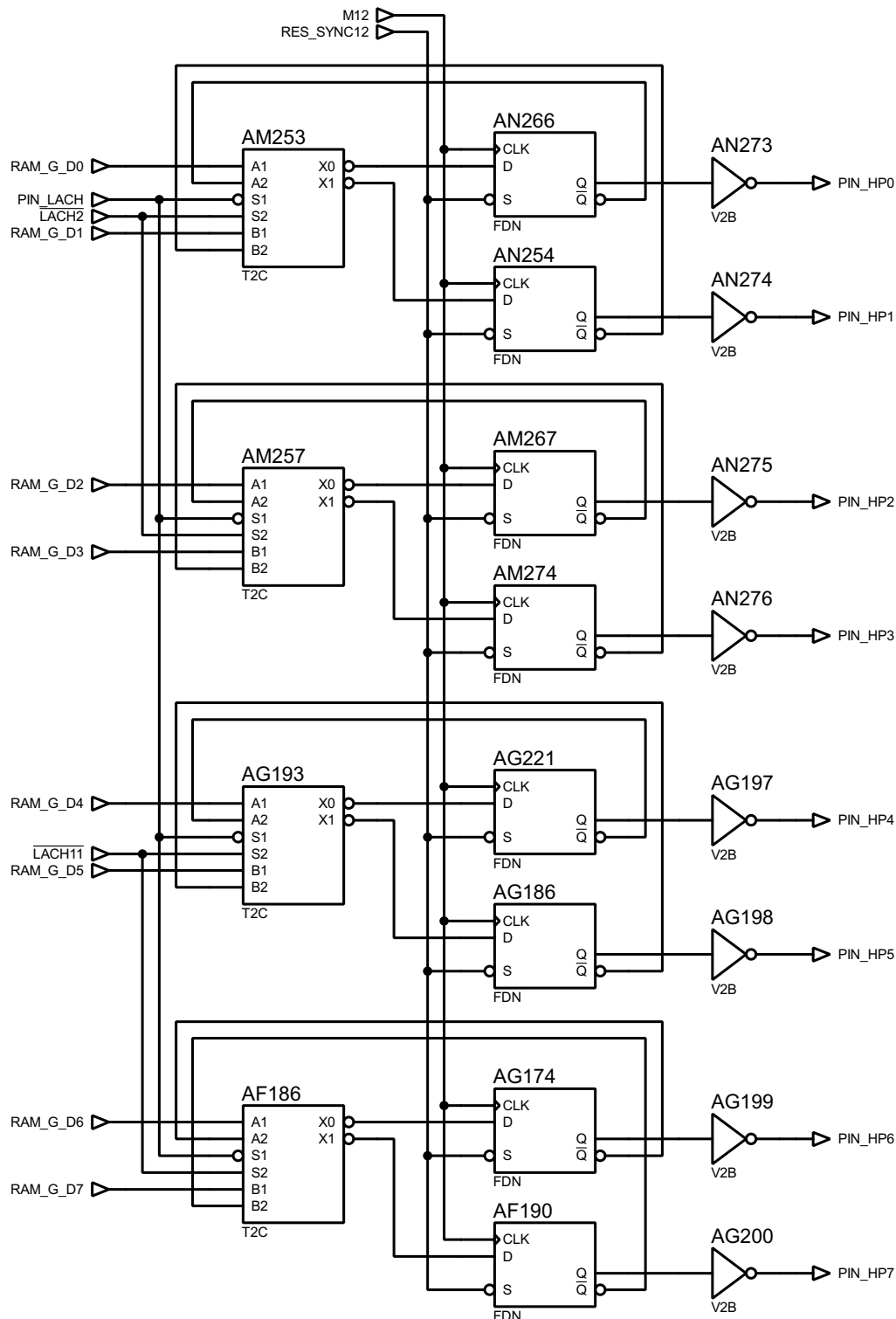
SPR ZOOM Y

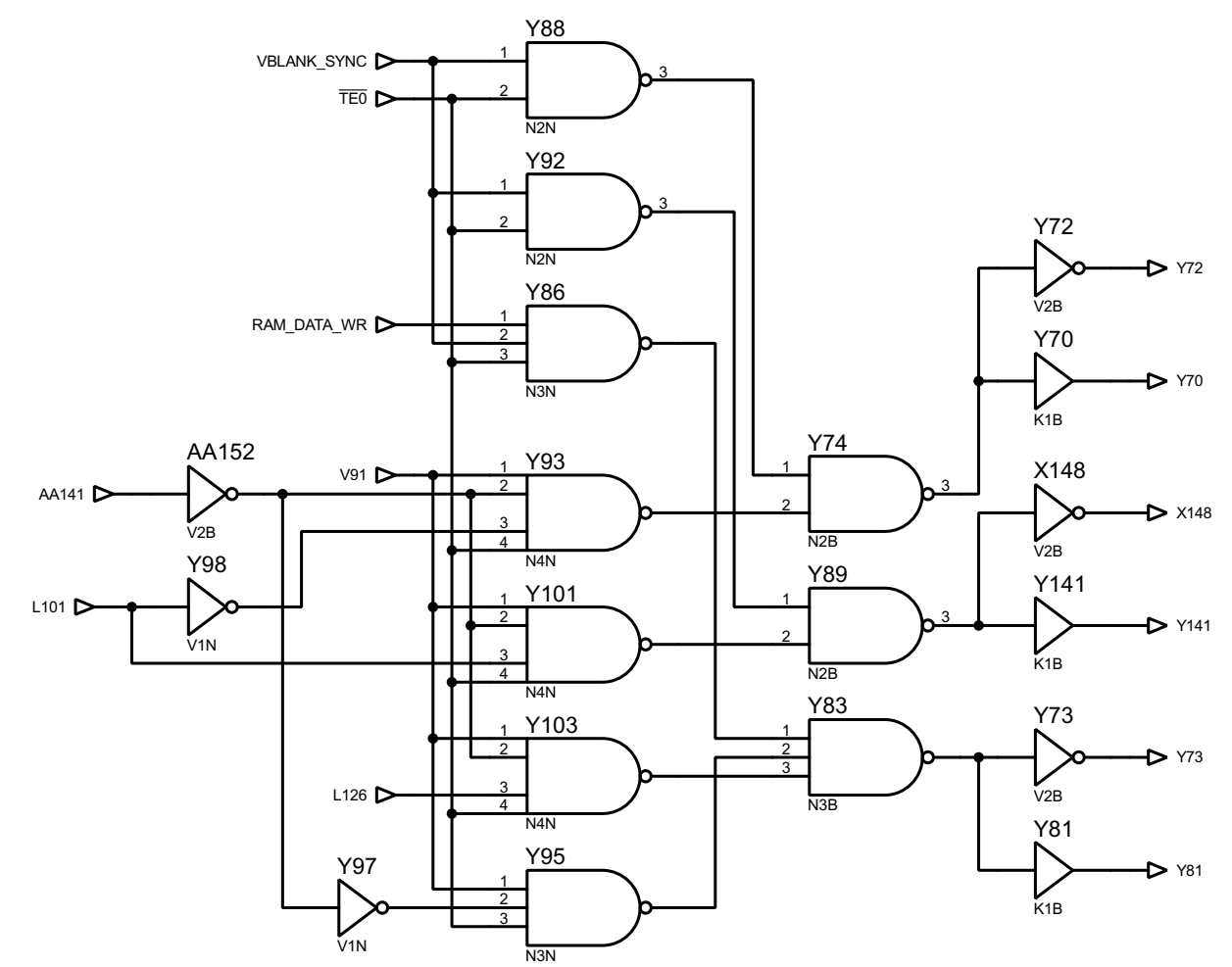


SPR TILE CODE



Sprite X position





TITLE:		DATE:
k051960 SEQUENCING		20/01/2022
BY: Sean "furrtek" Gonsalves		PAGE: 14/14
REV: A		