	OPCO	DDES																		
	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
AND	0	0	0	0		DS	T		SRC1				SR	C2		0	0	0	0	
ANDI	0	0	0	1		DS	)		JICI				IMM							
SUB	0	0	1	0	DST				SRC1				SR	C2		0	0	0	0	
SUBI	0	0	1	1		DS	)		JICI				IMM							
OR	0	1	0	0	DST				SRC1				SR	C2		0	0	0	0	
ORI	0	1	0	1					SINCI				IMM							
ADD	0	1	1	0	DST				SRC1				SR	C2		0	0	0	0	
ADDI	0	1	1	1					JICI				IMM							
XOR	1	0	0	0	DST				SRC1			SRC2			0	0	0	0		
XORI	1	0	0	1		DS	)		JICI				IMM							
JUMP	1	0	1	0	ADDRESS 16															
LD	1	0	1	1		DST	REG		ADDRESS 12											
ST	1	1	0	0		SRC	REG						ADDRESS 12							
PUSH	1	1	0	1	- SRC REG			0	0	0	0	0	0	0	0	0	0	0	0	
POP	1	1	1	0				0	0	0	0	0	0	0	0	0	0	0	0	
BE	1	1	1	1		CDC4	DEC		CDC3 DEC				ADDRESS 7							
BNE	1	1	1	1	SRC1 REG				SRC2 REG				ADDRESS 7							
																				Z