

## MC68302 TQFP PIN ASSIGNMENT

(C) 1997 Motorola

<u>PIN</u>	<u>NAME</u>	<u>PIN</u>	<u>NAME</u>
1	GND_	37	GND
2	TOUT2/PB6	38	VCC
3	TIN2/PB5	<b>39</b>	RCLK1/L1CLK
4	TOUT1/PB4	40	TCLK1/L1SYS0/SDS1
5	VCC	41	TXD1/L1TXD
6	TIN1/PB3	42	RTS1/L1RQ/GCIDCL
7	IACK1/PB2	43	RTS3/SPTXD
8	IACK6/PB1	44	CD3/SPCLK
9	ĪACK7/PB0	45	BRG1
10	GND	46	NC3
11	GND	47	BUSW
12	UDS/A0	48	DISCPU
13	LDS/DS	49	FRZ
14	AS_	50	DONE/PA15
15	$R/\overline{W}$	51	DACK/PA14
16	GND	<b>52</b>	DREQ/PA13
17	GND	53	BRG3/PA12
18	XTAL	54	GND
19	EXTAL	55	GND
20	VCC	<b>56</b>	TCLK3/PA11
21	VCC	<b>57</b>	RCLK3/PA10
22	CLK0	58	TXD3/PA9
23	ĪPL0/IRQ1	<b>59</b>	RXD3/PA8
24	ĪPL1/IRQ6	<b>60</b>	VCC
<b>25</b>	ĪPL2/IRQ7	<b>61</b>	SDS2/BRG2/PA7
<b>26</b>	BERR	<b>62</b>	CD2/PA6
<b>27</b>	AVEC/IOUT0	63	RTS2/PA5
28	RESET	64	CTS2/PA4
29	HALT	65	GND
30	BR	66	GND
31	NC1	67	TCLK2/PA3
32	BGACK	68	RCLK2/PA2
33	BG	69	TXD2/PA1
34	BCLR	70	RXD2/PA0
35	DTACK	71	RXD1/L1RXD
36	GND	<b>72</b>	CTS1/L1GR
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<u>PIN</u>	<u>NAME</u>	<u>PIN</u>	<u>NAME</u>
73	CD1/L1SY1	109	NC5
74	CTS3/SPRXD	110	A15
<b>75</b>	D0	111	A14
<b>76</b>	D1	112	A13
<b>77</b>	D2	113	A12
<b>78</b>	D3	114	GND
<b>79</b>	GND	115	A11
80	D4	116	A10
81	D5	117	A9
<b>82</b>	D6	118	A8
83	D7	119	A7
84	VCC	120	A6
85	D8	121	A5
86	D9	122	A4
87	D10	123	GND
88	D11	124	A3
89	GND	125	A2
90	D12	126	A1
91	D13	127	FC0
92	D14	128	VCC
93	D15	129	FC1
94	GND	130	FC2
95	GND	131	CS0/IOUT2
96	VCC	132	CS1
97	VCC	133	GND
98	A23	134	GND
99	A22	135	CS2
100	A21	136	CS3
101	A20	137	RMC/IOUT1
102	GND	138	IAC
103	A19	139	PB11
104	A18	140	PB10
105	A17	141	PB9
106	A16	142	PB8
107	VCC	143	WDOG/PB7
108	NC4	144	GND