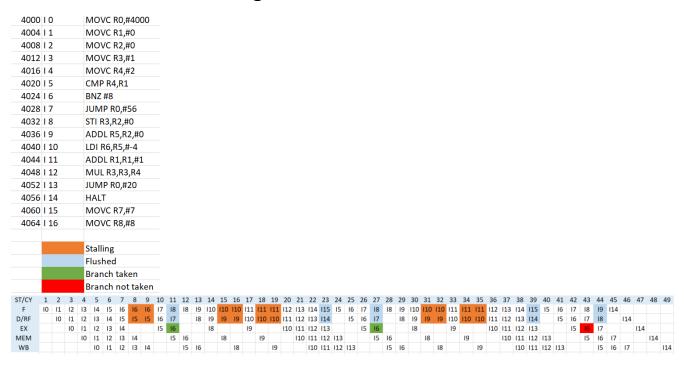
Test case 2:

MOVC R0,#4000 MOVC R1,#0 MOVC R2,#0 MOVC R3,#1 MOVC R4,#2 CMP R4,R1 BNZ #8 JUMP R0,#56 STI R3,R2,#0 ADDL R5, R2, #0 LDI R6,R5,#-4 ADDL R1,R1,#1 MUL R3,R3,R4 JUMP R0,#20 HALT MOVC R7,#7 **MOVC R8,#8**

Expected register and memory value:

Solution without forwarding:



Solution with forwarding:

