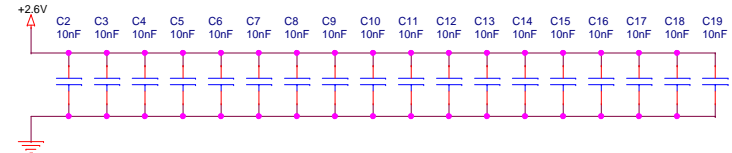
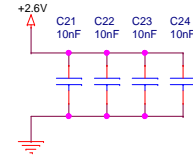


SEE MOTOROLA APPLICATION NOTE
AN2127/D FOR CAPACITOR LAYOUT!

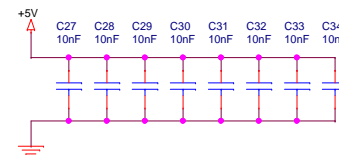
8 capacitors for VDD and 10 capacitors for NVDDL/QVDDL. Place under the CPU.



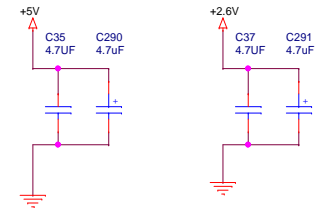
Connect to +2.6V plane around the CPU



8 capacitors for VDDH bypass. Connect to +5V plane around the CPU



Connect to planes near the CPU



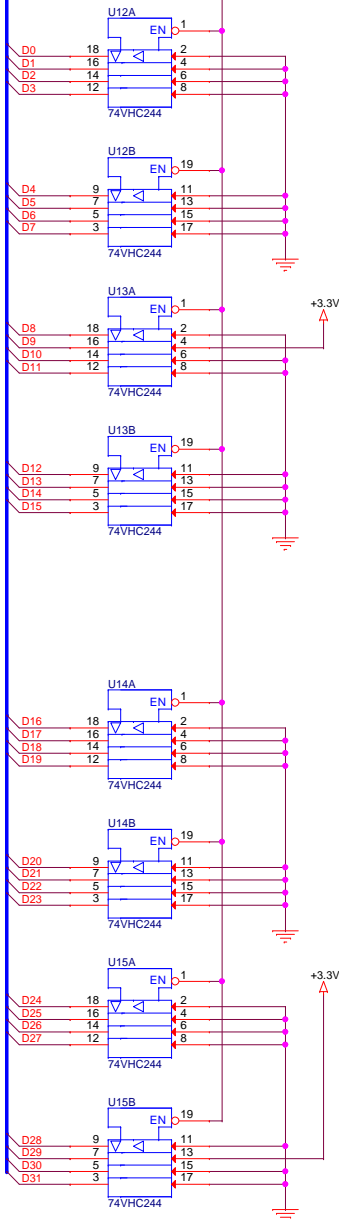
Place resistors R30, R32 and R35 near the CPU U7

K	No change in this sheet	04.06.19	kompraka	keisraj	Designed by	Antti Toivonen	Checked by	Diana Xiao Hu	Language	En
J	No change in this sheet	28.6.18	achand03	keisraj	Dept	Konette	Date	12.5.2003	Product code	LCE
H	No change in this sheet	12.1.15	M.S							
G	No change in this sheet	21.3.14	M.S							
Issue Change description		Date	Des.	by	Appr.	LCECPU561				
Processor power and clocks		Sales ref no.								
Drawing no.		773391		Issue		K		Page		3 of 22



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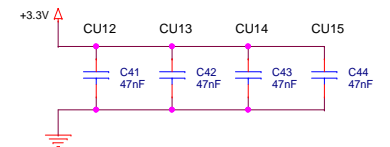
[1] /HRESET3V >>
[1,5,6,7,8] D[0..31] >>



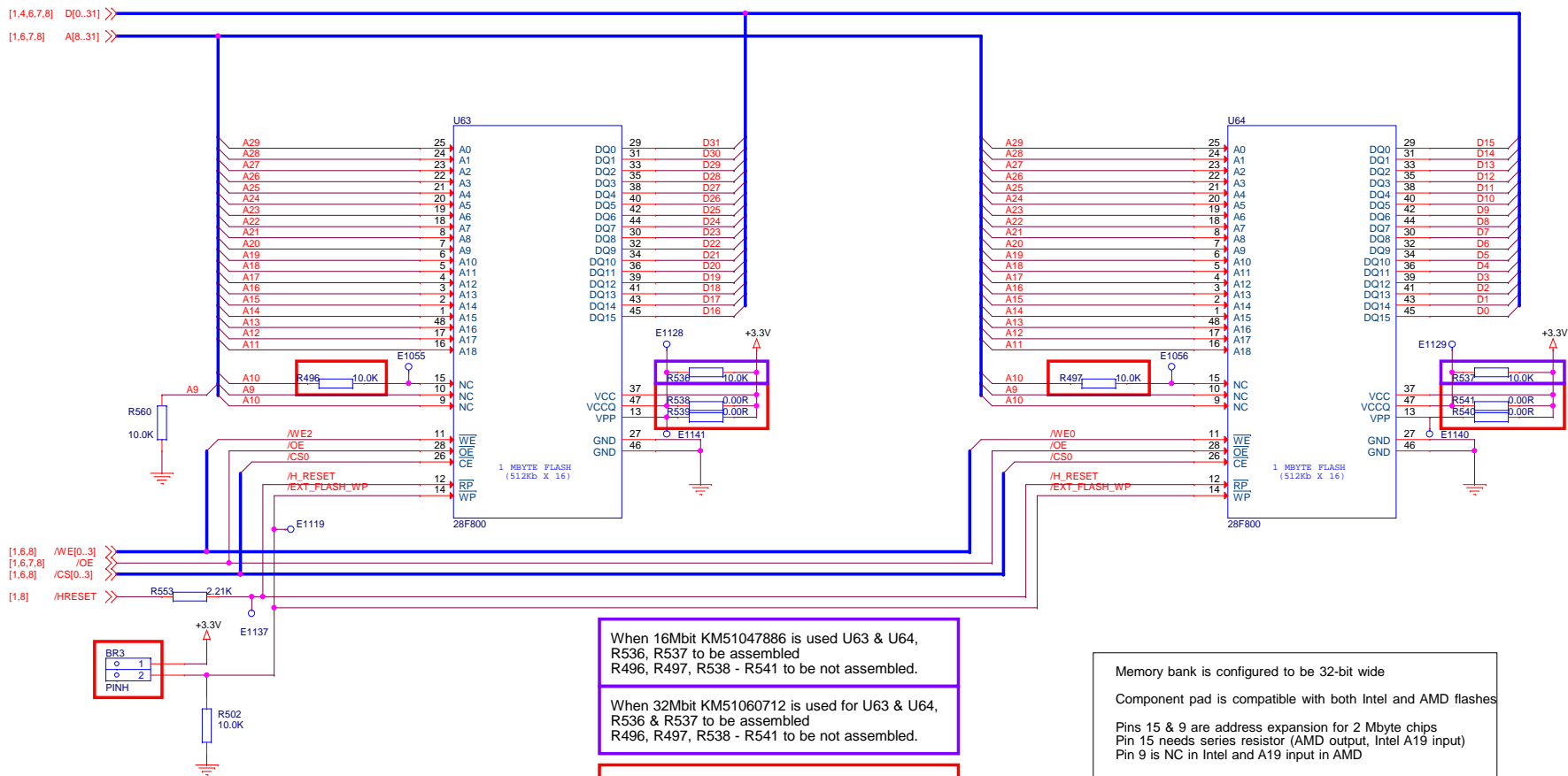
NOTE: buffer U12-15 operating voltage is +3.3V

Hard Reset Configuration word settings (default is all zeros)

0 EARB, 0=internal arbitration
1 IP
2 BDRV (bus drive strength), 0=full drive
3 BDIS (boot disable) 0=boot enabled
4:5 BPS (boot port size) 00=32 bit
6:8 reserved
9:10 DBGCC (debug pin configuration)
11 reserved
12 ATWC
13:14 EBDFF (bus clock divider), 00=full speed
15 reserved
16 PRPM (peripheral mode enable), 0=normal operation
17:18 SC (single chip select), 00=extended chip, 32 bit
19 ETRE (exception table relocation), 0=off
20 FLEN (internal flash enable), 0=flash disabled
21 EN_COMP (enable compression), 0=disabled
22 EXC_COMP
23 reserved
24:25 OERC (other exceptions relocation control), 0=offset 0
26:27 reserved
28:30 ISB (internal memory base address), 010=0x800000
31 DME (dual mapping enable), 0=disabled



K	No change in this sheet	04.06.19	kompraka	keisraj	Designed by Antti Toivonen	Checked by Diana Xiao Hu	Language En
J	No change in this sheet	28.6.18	chand03	keisraj	Dept Konette	Date 12.5.2003	Product code LCE
H	No change in this sheet	12.1.15	M.S		Reset configuration word		
G	No Changes to this sheet	21.3.14	M.S		LCECPU561		
Issue/Change description		Date	Des. by	Appr. by	Reset configuration word		
Sales ref no.		Drawing no.		Issue		Page	
		773391		K		4 of 22	



When 16Mbit KM51047886 is used U63 & U64,
R536, R537 to be assembled
R496, R497, R538 - R541 to be not assembled.

When 32Mbit KM51060712 is used for U63 & U64,
R536 & R537 to be assembled
R496, R497, R538 - R541 to be not assembled.

When U63 & U64 is assembled with KM275523
R496, R497, R538 - R541 to be assembled.
R536 & R537 to be not assembled

Memory bank is configured to be 32-bit wide

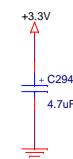
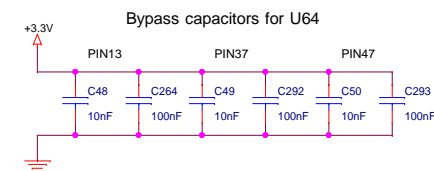
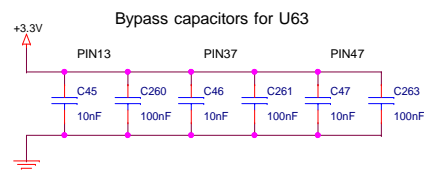
Component pad is compatible with both Intel and AMD flashes

Pins 15 & 9 are address expansion for 2 Mbyte chips
Pin 15 needs series resistor (AMD output, Intel A19 input)
Pin 9 is NC in Intel and A19 input in AMD

Pin 10 is address expansion for 4 Mbyte chips (AMD and Intel)

Connect to plane for power stabilization

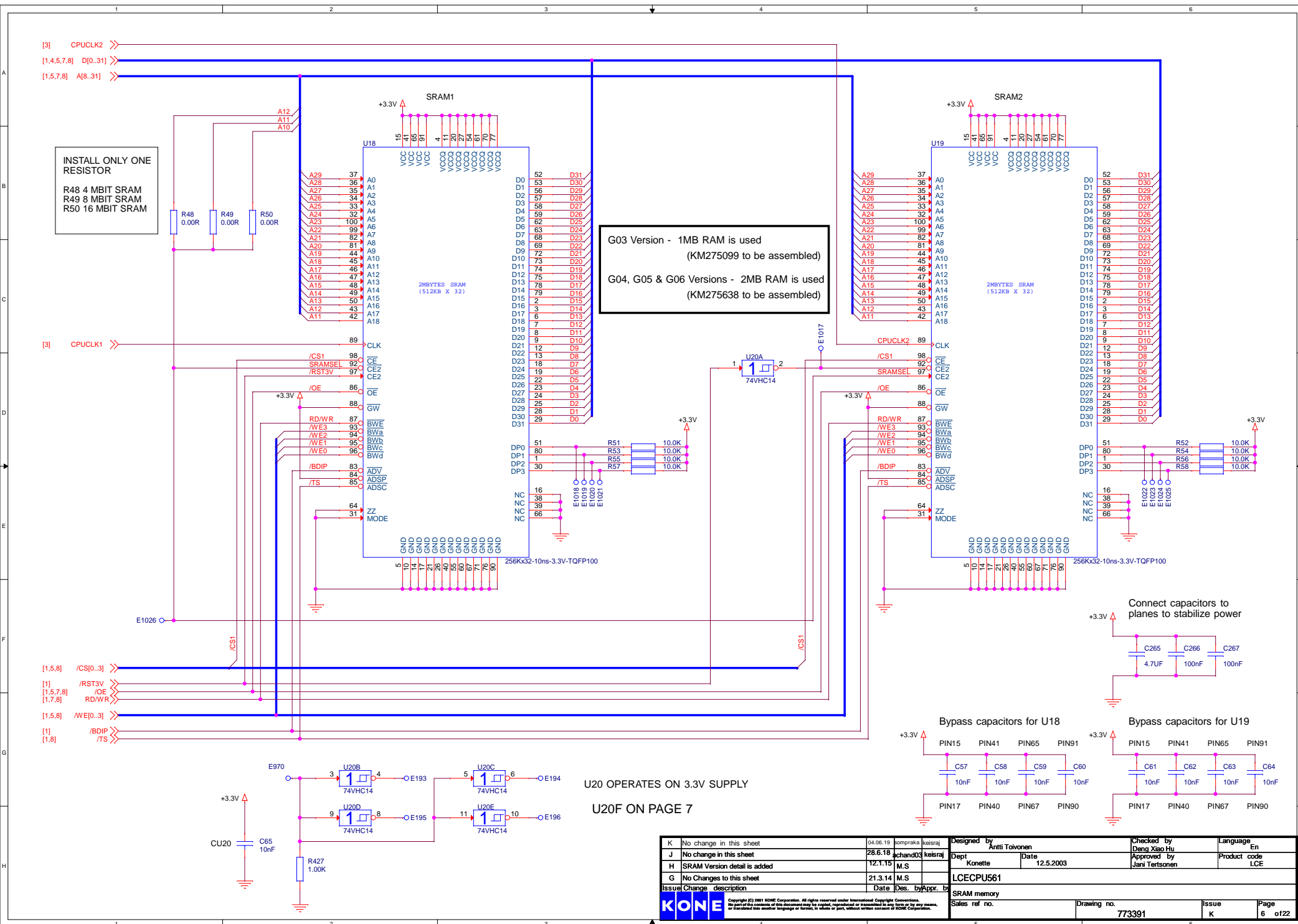
G03 Version : 1MB flash (KM275523) U63 & U64 (Total - 2MB) &
(Full Version) 1MB RAM (KM275099)
G04, G05 Version : 2MB flash (KM51047886) U63 & U64 (Total - 4MB) &
(Reduced Version) 2MB RAM (KM275638)
G06 Version : 2MB flash (KM51047886) U63 & U64 (Total - 4MB) &
(Full Version) 2MB RAM (KM275638)

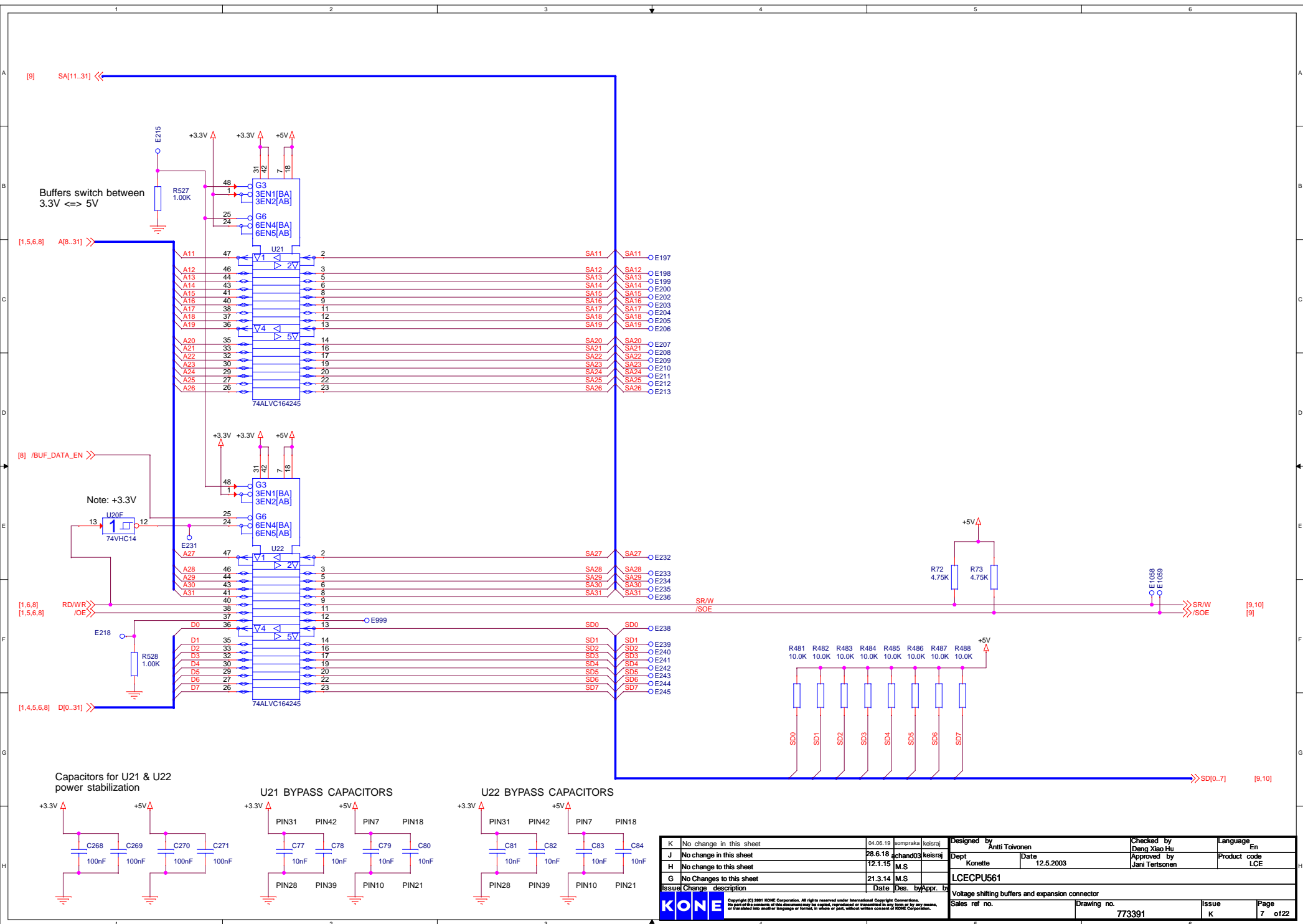


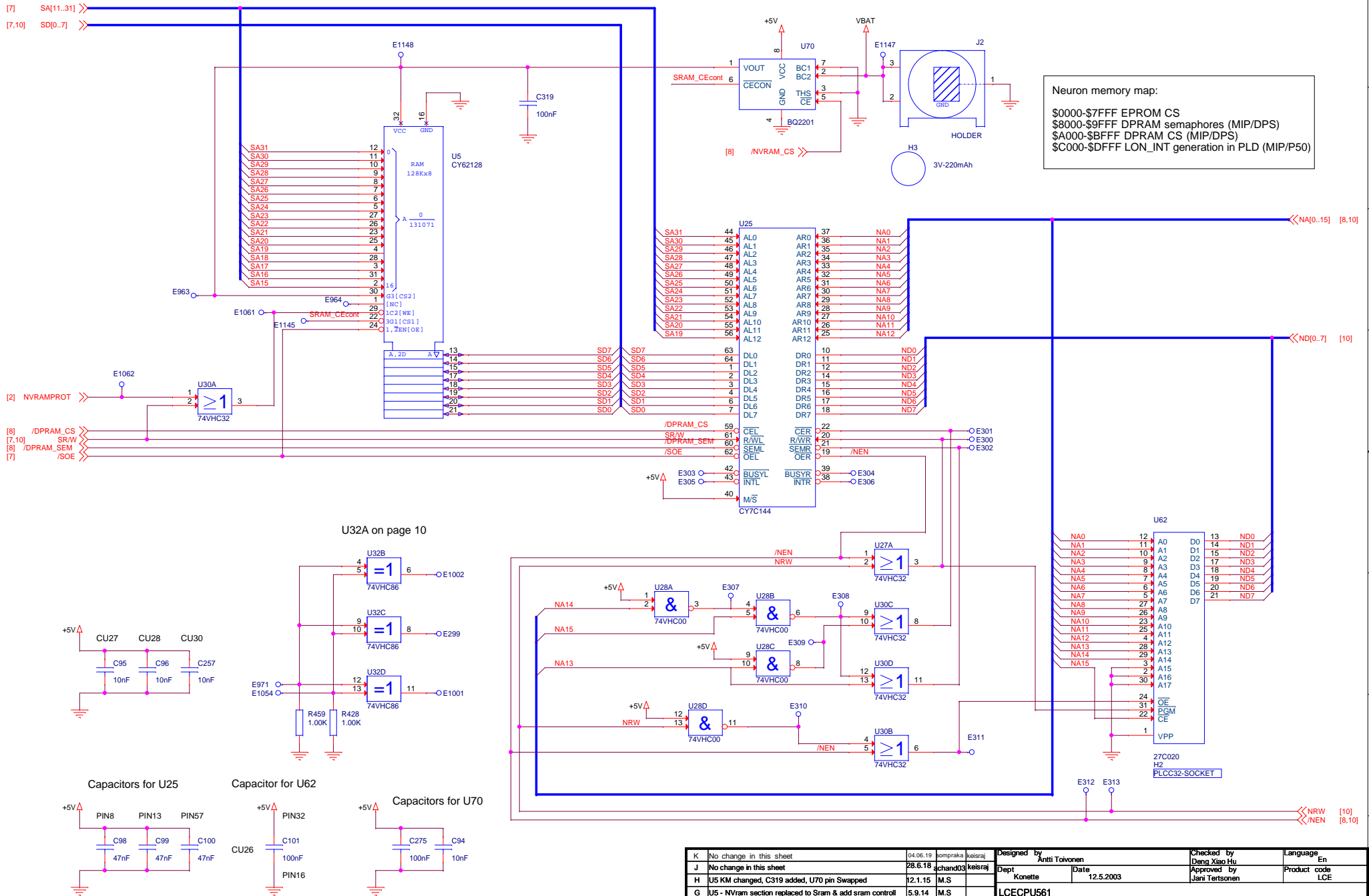
K	No change in this sheet	04.06.19	kompraka	keisraj	Designed by	Antti Toivonen	Checked by	Dana Xiao Hu	Language	En
J	Refer CR S. No. 89	28.6.18	chand03	keisraj	Dept	Konette	Date	12.5.2003	Product code	LCE
H	Flash & Ram Version detail is added	12.1.15	M.S							
G	R536-R541 & R553 resistor added in flash section	21.3.14	M.S							
Issue Change description Date Des. by Appr. b										
Flash memory										
Sales ref no.										
Drawing no. 773391										
Issue 5 of 22										



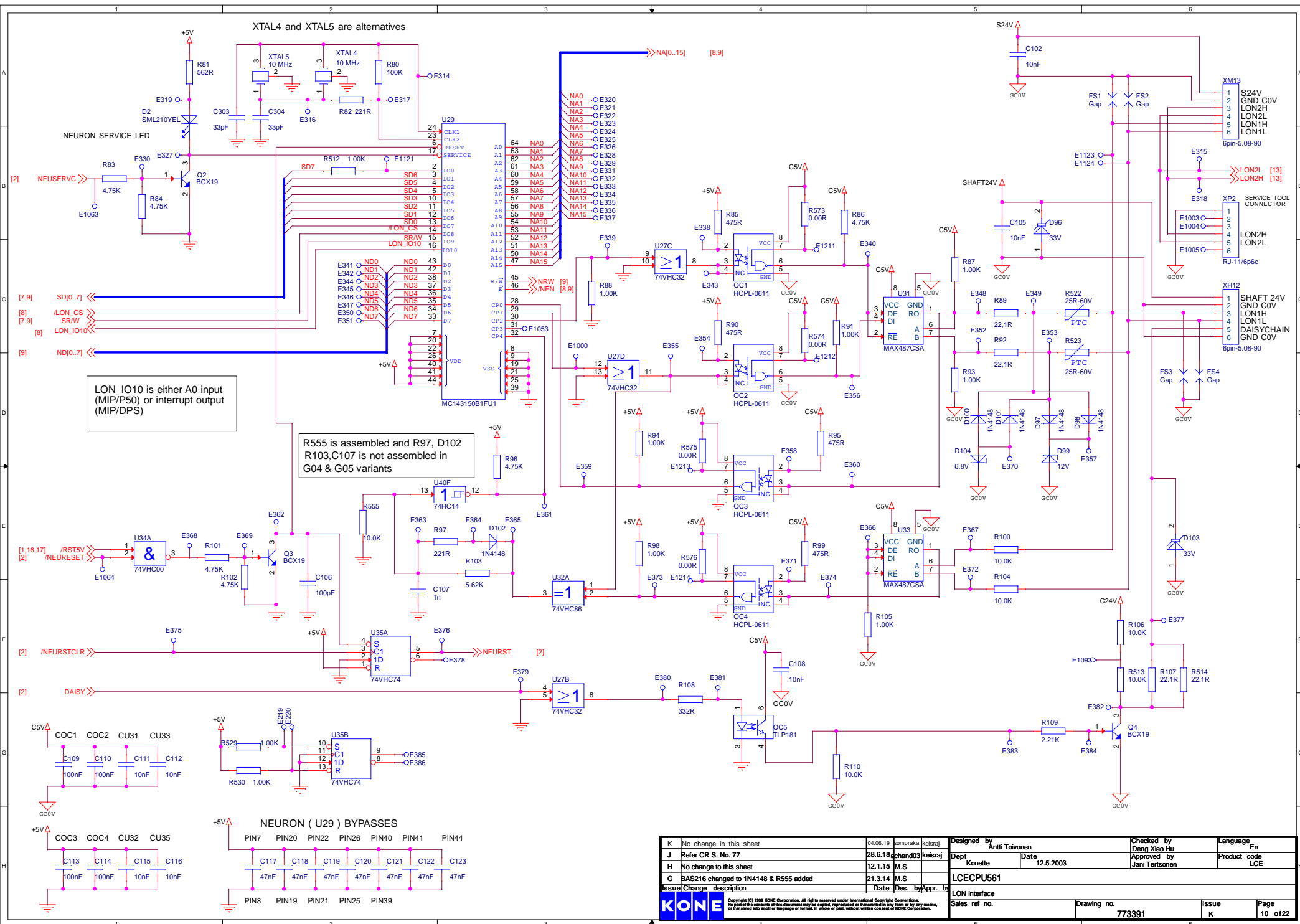
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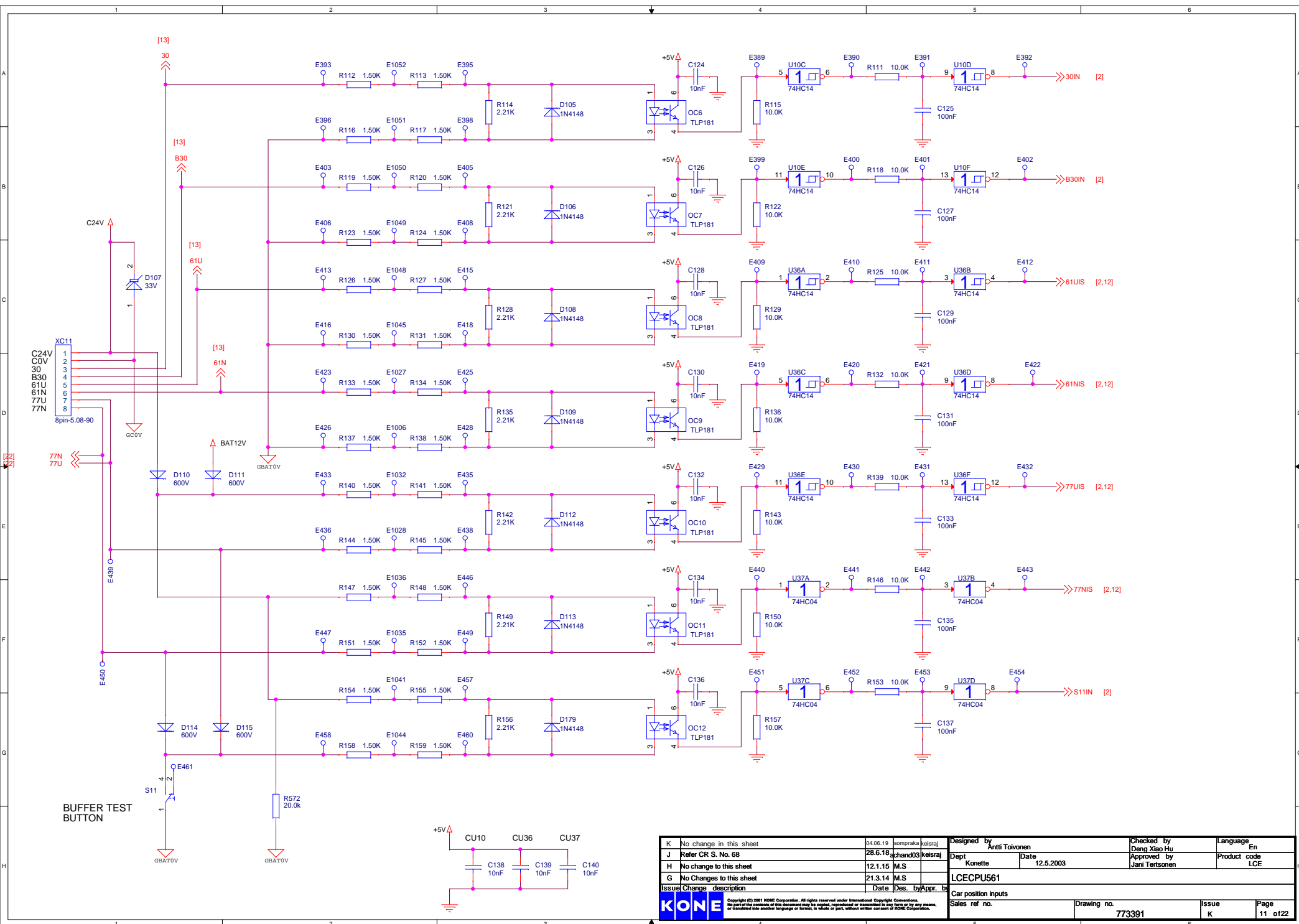


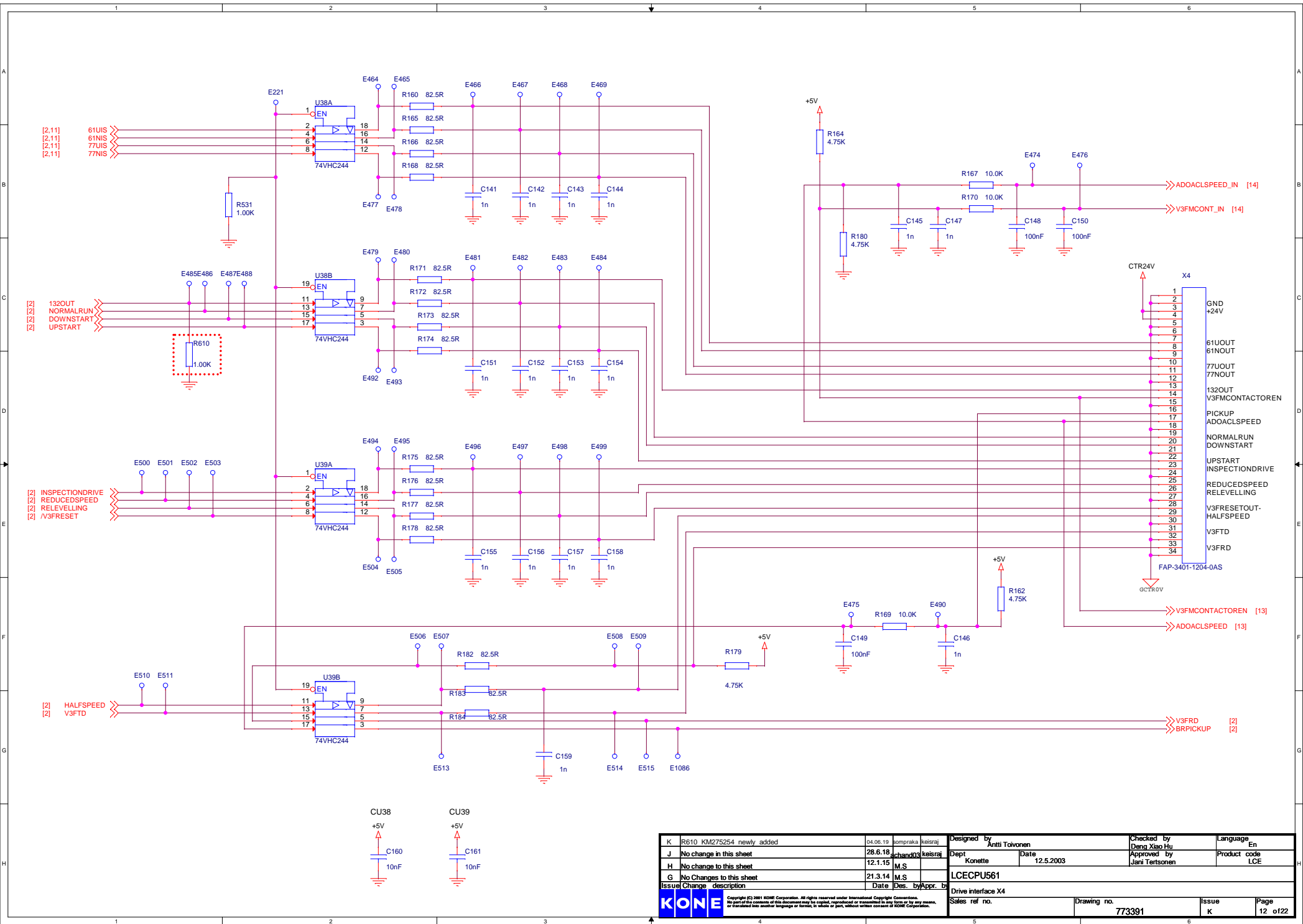


K	No change in this sheet	04.06.19	kompraka	keisraja	Designed by	Antti Toivonen	Checked by	Diana Xiao Hu	Language	En
J	No change in this sheet	28.6.18	achand03	keisraja	Dept	Konette	Date	12.5.2003	Approved by	Jani Tertonen
H	U5 KM changed, C319 added, U70 pin Swapped	12.1.15	M.S						Product code	LCE
G	U5 - NVRam section replaced to Sram & add sram controll	5.9.14	M.S		LCECPU561					
Issue/Change description		Date	Des.	by	Appr. by					
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		Sales ref no.		Drawing no.		773391		Issue		Page
								K		9 of 22

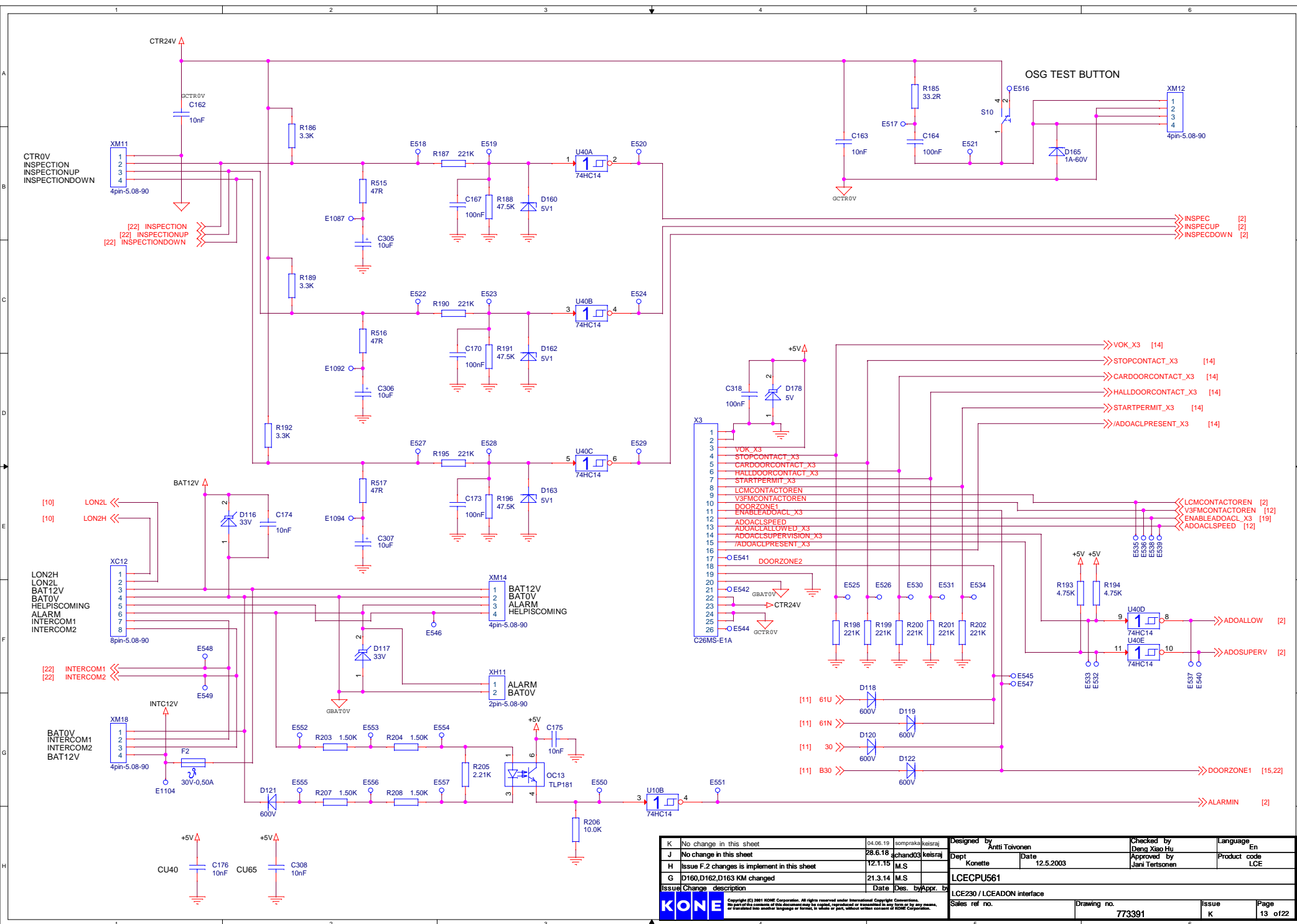


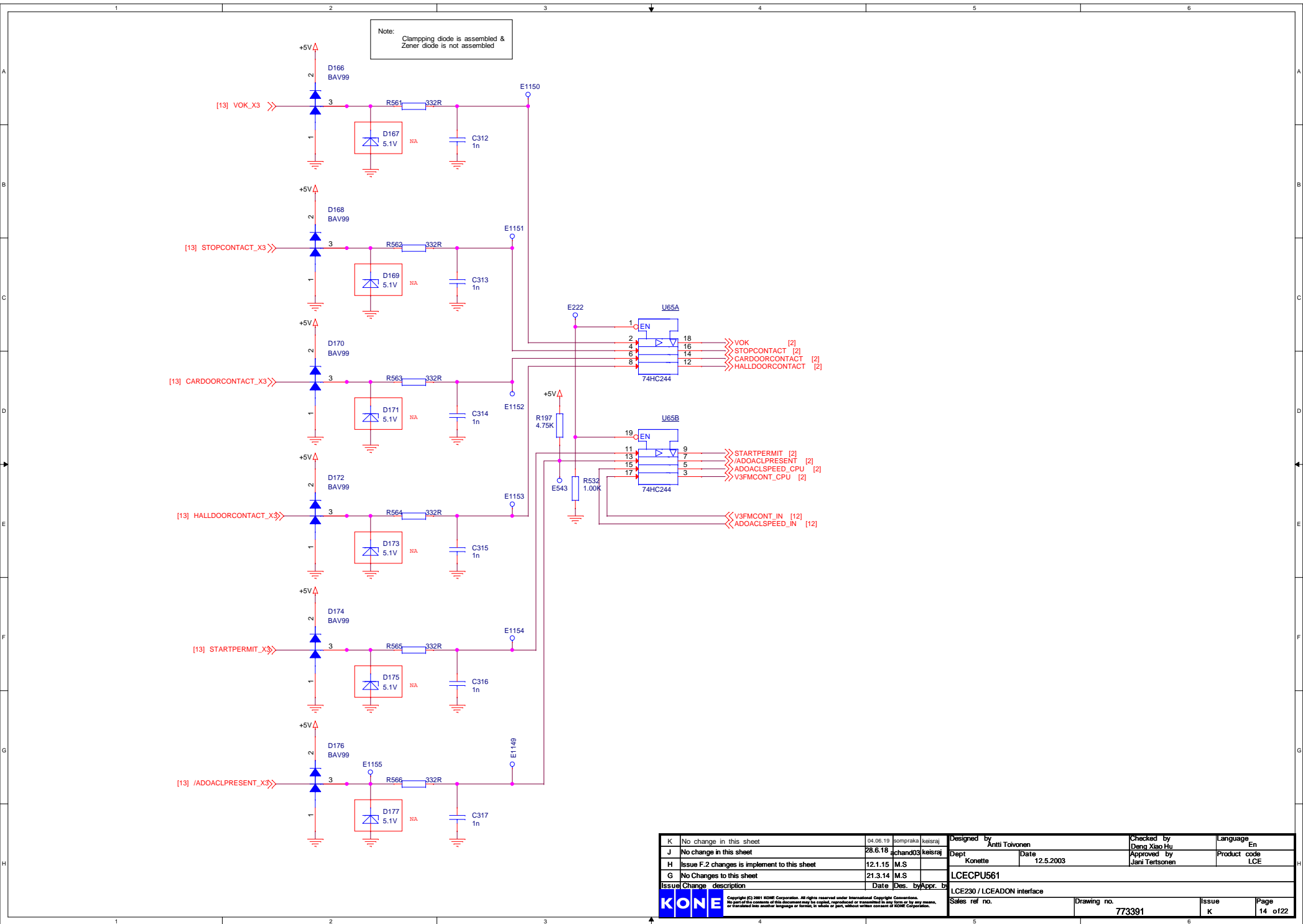
K	No change in this sheet	04.06.18	Jamprikka Kaisraj	Designed by	Antti Toivonen	Checked by	Dang Xiao Hu	Language	En
J	Refer CR S. No. 77	28.6.18	Chandrashekar Kaisraj	Dept	Konette	Date	12.5.2003	Approved by	Jani Tertsunen
H	No change in this sheet	12.1.15	M.S					Product code	LCE
G	BAS216 changed to 1N4148 & R555 added	21.1.15	M.S						
Issue/Change description				Date	Des. by	Appr. by			
				LCECPU561					
				LON interface					
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Drawing no.				773391					
Issue				K					
Page				10 of 22					

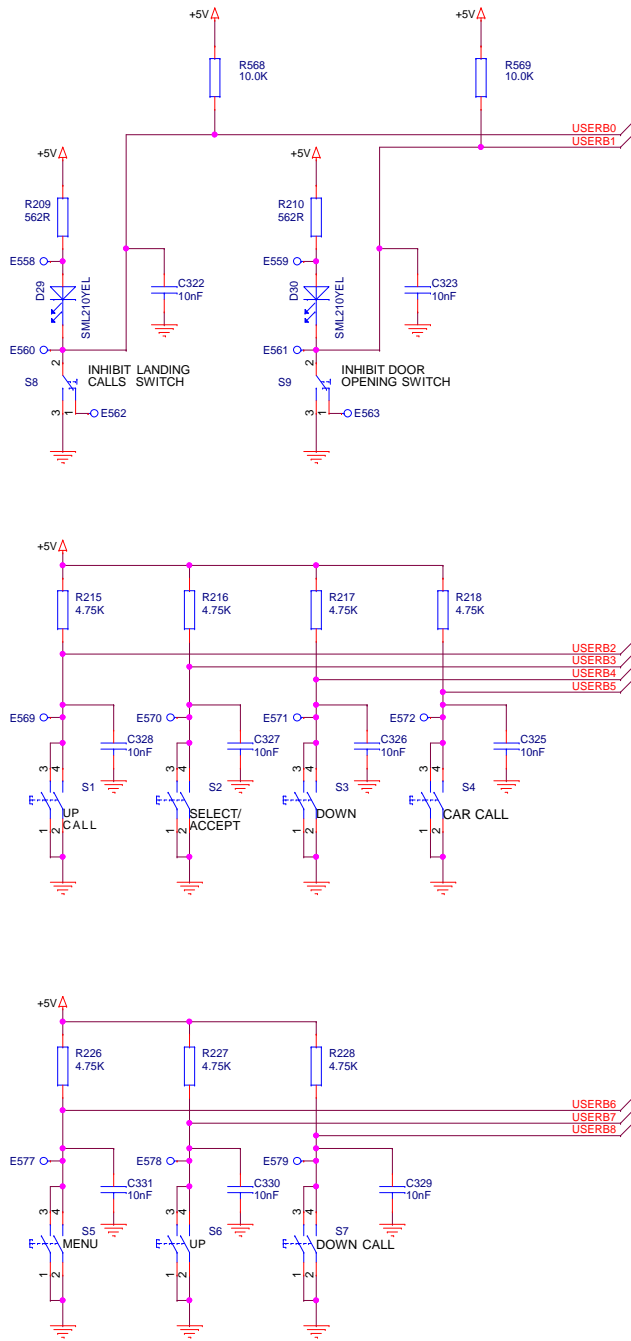




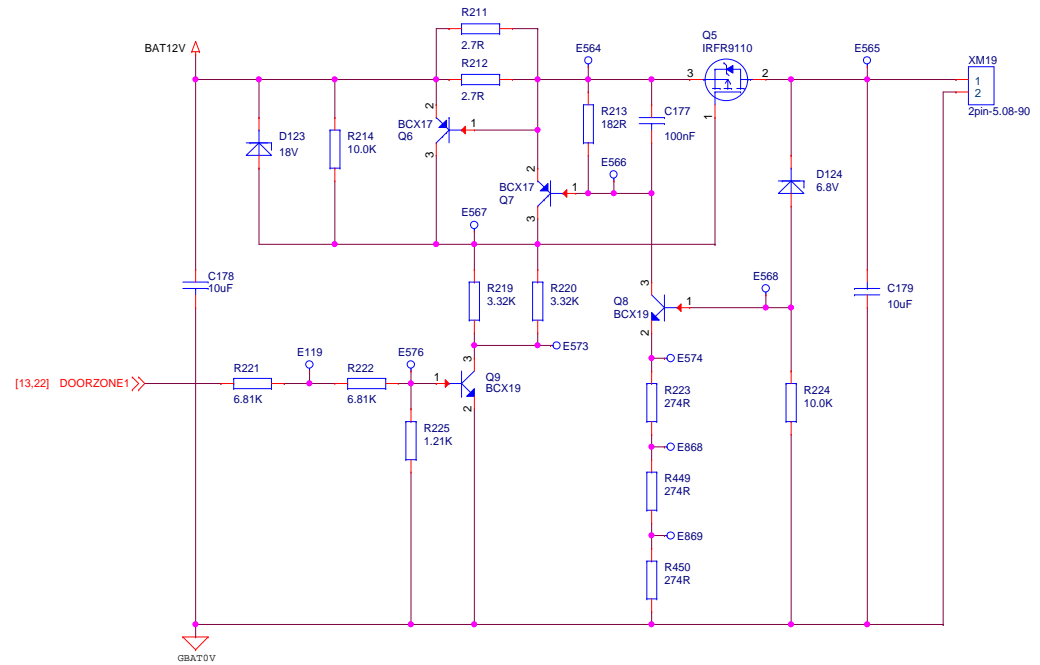
K	R610 KM275254 newly added	04.06.19	homprika	keisraji	Designed by	Antti Toivonen	Checked by	Diana Xiao Hu	Language	En
J	No change in this sheet	28.6.18	chandro	keisraji	Dept	Konette	Date	12.5.2003	Approved by	Jani Tervitsen
H	No change to this sheet	12.1.15	M.S		Product code LCE					
G	No Changes to this sheet	21.3.14	M.S		LCECPU561					
Issue/Change description		Date	Des.	by	Appr. by					
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		Sales ref no.		Drawing no.		773391		Issue	K	Page 12 of 22







DOOR ZONE INDICATOR REGULATOR



K	No change in this sheet	04.06.19	Isompraka	keisraj	Designed by	Antti Toivonen	Checked by	Diana Xiao Hu	Language	En
J	Refer CR No.58,59. C178,C179 replaced with 10uF/50V	28.6.18	chand03	keisraj	Dept	Konette	Date	12.5.2003	Approved by	Jani Tertsonen
H	No change to this sheet	12.1.15	M.S						Product code	LCE
G	D123 diode KM number changed	21.3.14	M.S							
Issue Change description		Date	Des.	by	Appr. by	User buttons and door zone indicator				
Sales ref no.		Drawing no.		Issue		Page				
		773391		K		15		of 22		



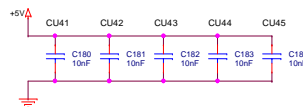
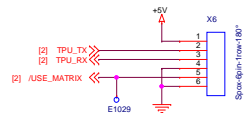
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U46B ON PAGE 16

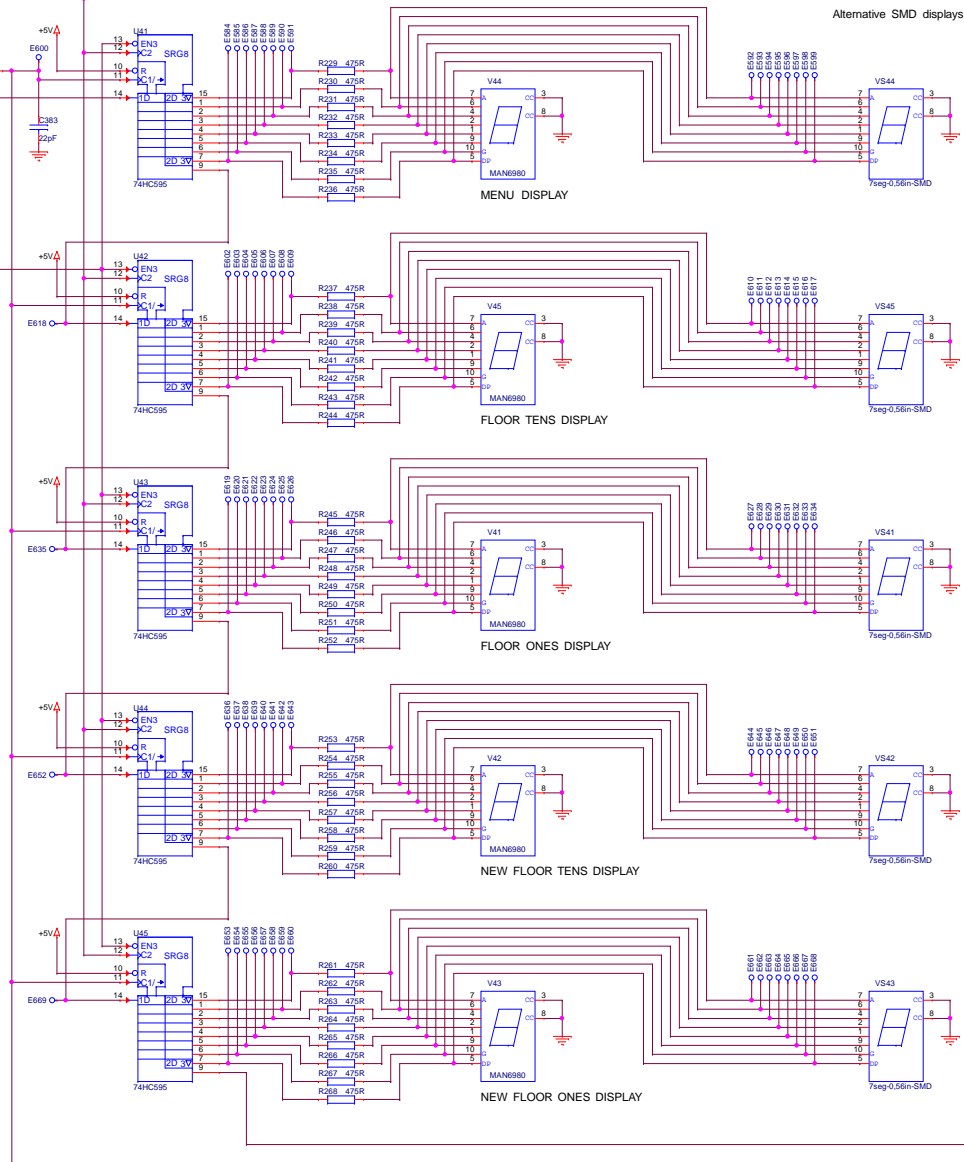
[2.22] SPL_PCS[0..3]
[2.22] SPL_CLK
[2.22] SPL_OUT

[1,10,17] _RSTBY
[2,17] DISPLAYEN

Extra connector for LCD display upgrade

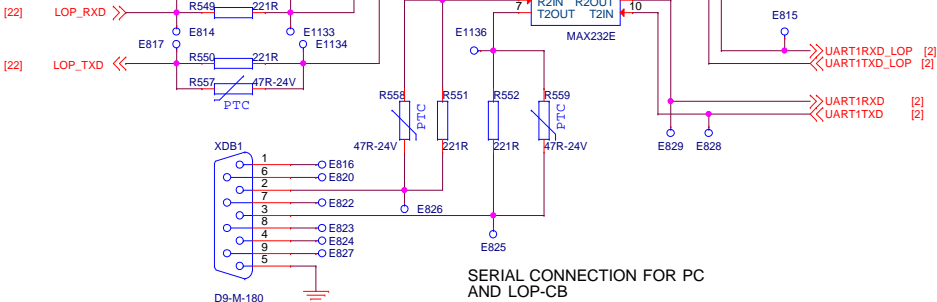


Alternative SMD displays

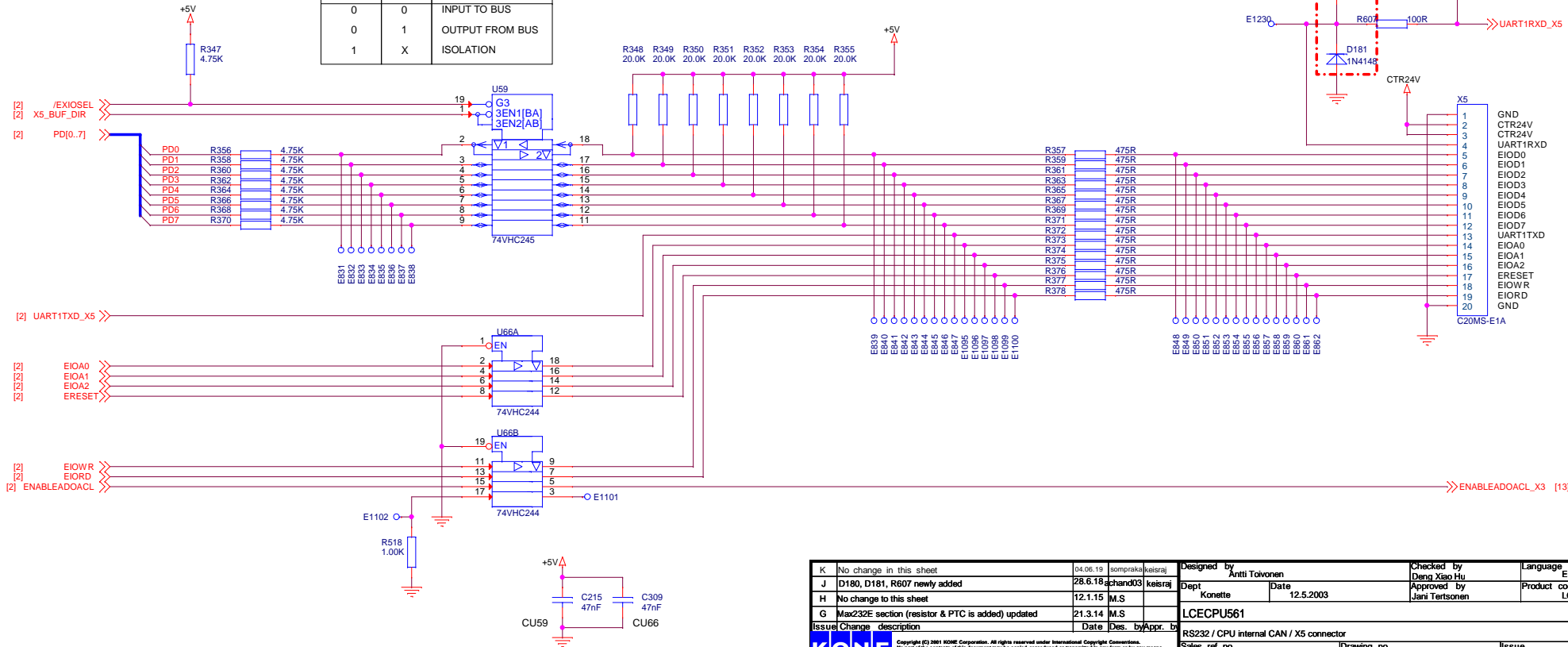


K	222F (M4275307) added to U46-3 with respect to Ground	04.06.10	Impromba	Autoren	Designed by	Jani Toivonen	Checked by	Jani Toivonen	Language	En
J	Refer CR S. No. 62	08.06.10	Impromba	Autoren	Drawn	Konette	Approved by	Jani Toivonen	Product code	LCE
H	No change to this sheet	12.1.10	W.S.		Date	12.5.2003				
G	No changes to this sheet	21.3.14	W.S.		Date	21.3.14				
Issue	Change description	Date	Iss.	Autopr.	Issue	Change description	Date	Iss.	Autopr.	
KONE					7-segment displays					
					Sales ref. no.					
					Drawing no.					
					773391					
					Issue					
					K					
					Page					
					18 of 22					

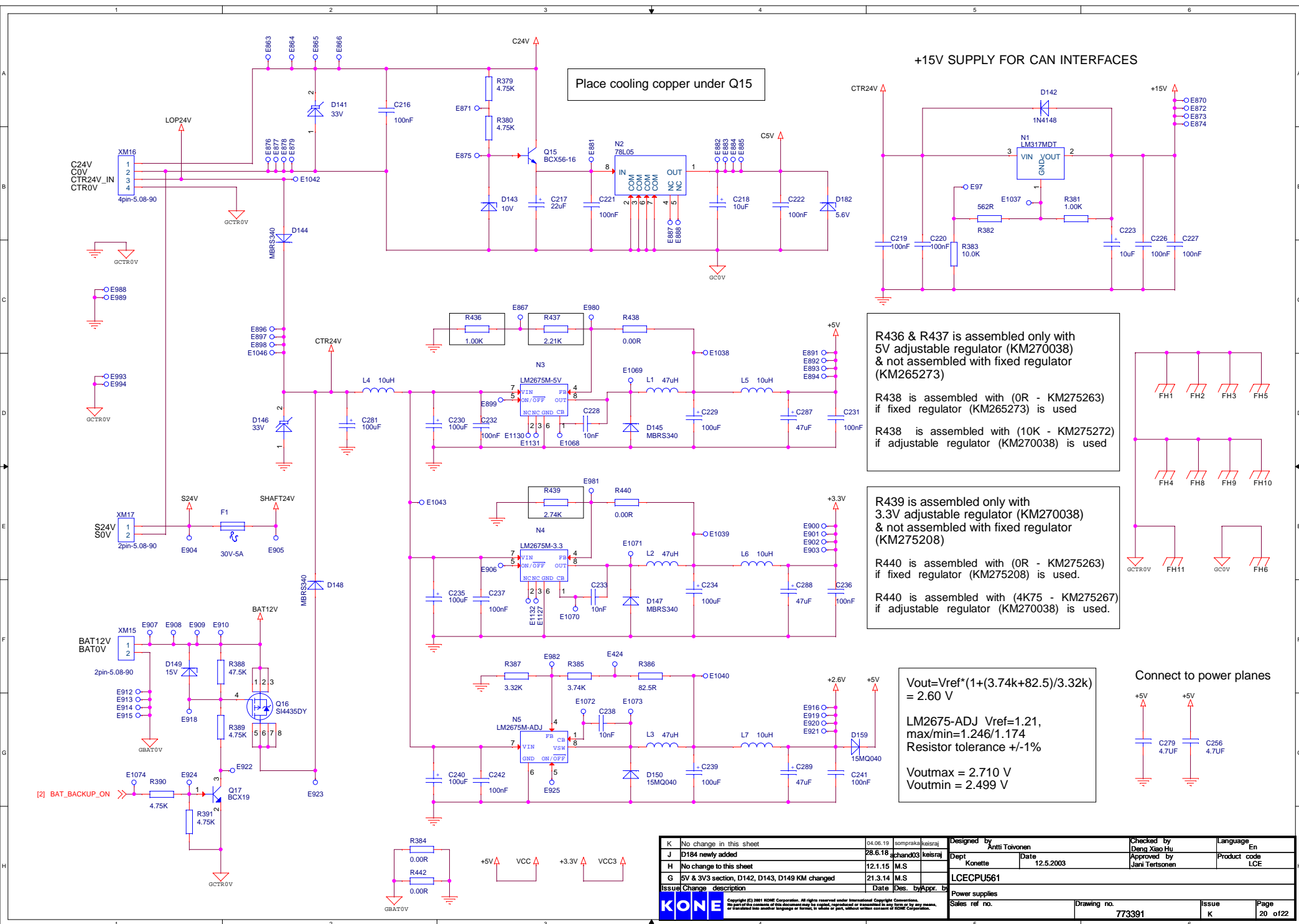
R556 - R559 (PTC Thermistor) is not assembled

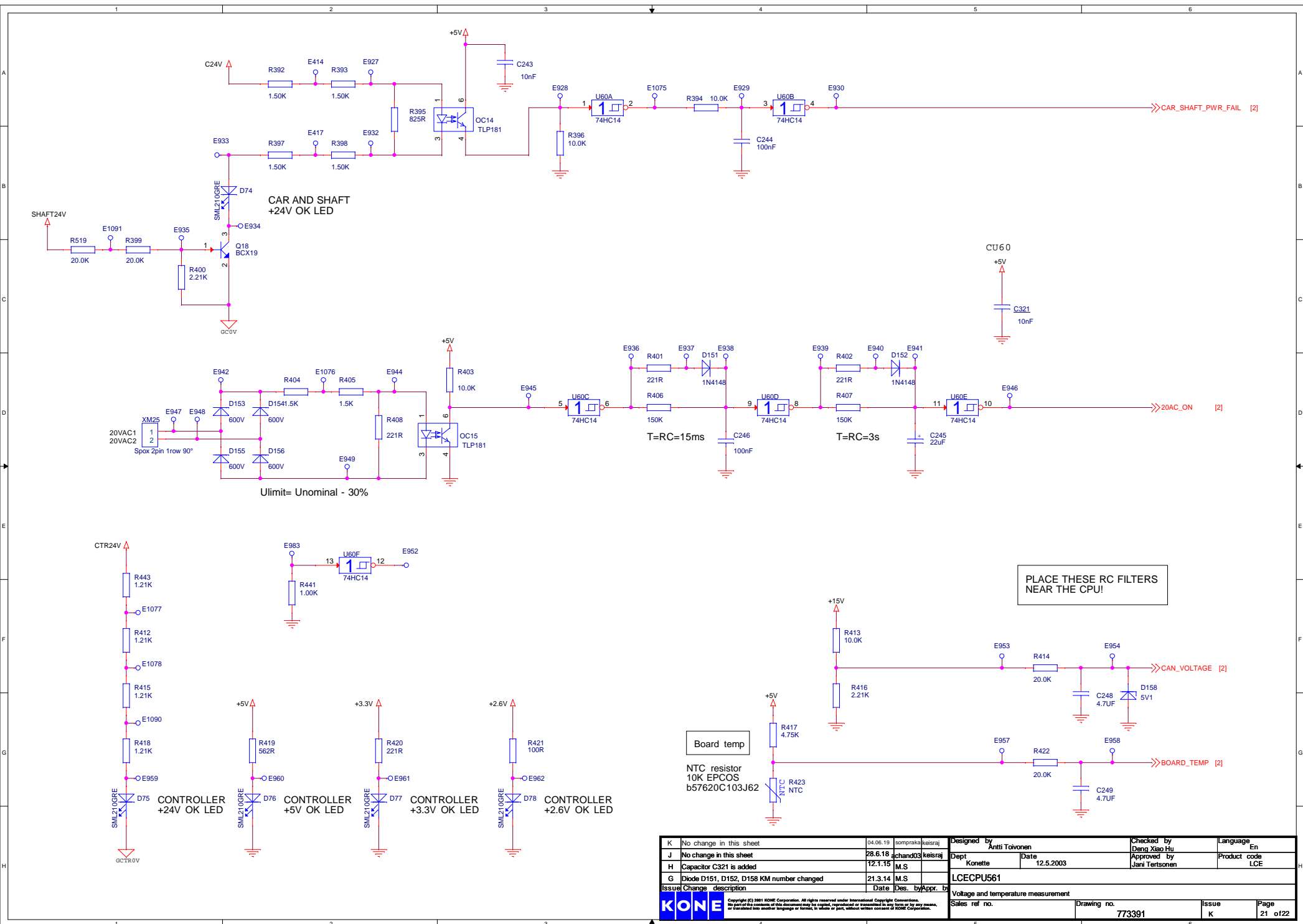


/EXIOSEL	EIORD	ACTION
0	0	INPUT TO BUS
0	1	OUTPUT FROM BUS
1	X	ISOLATION

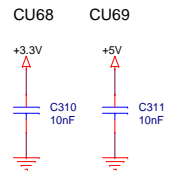
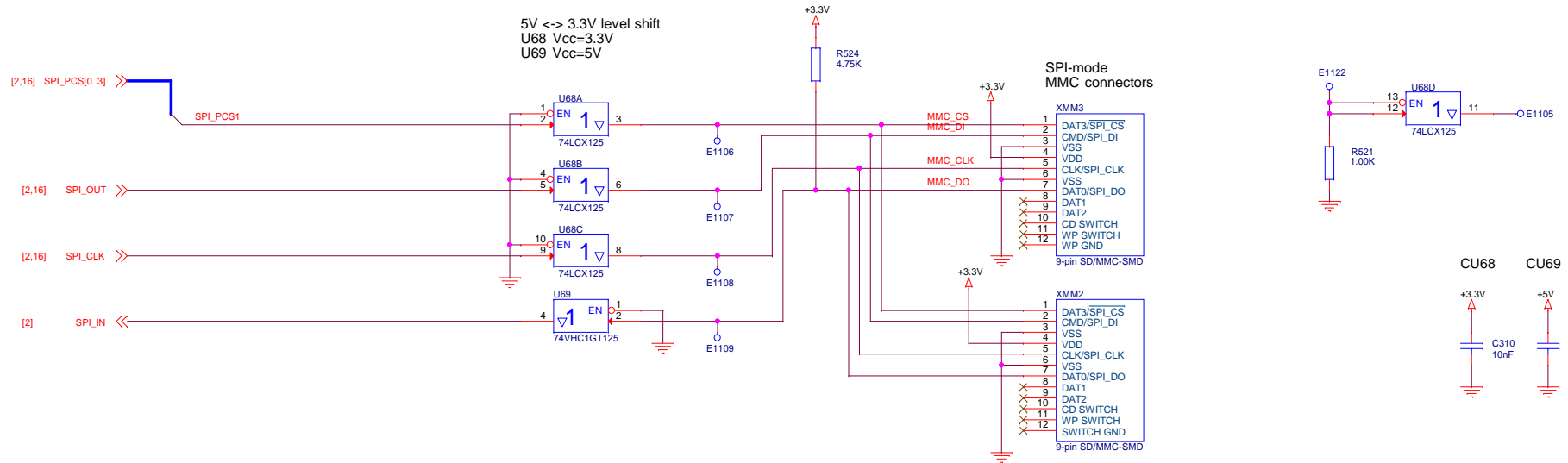
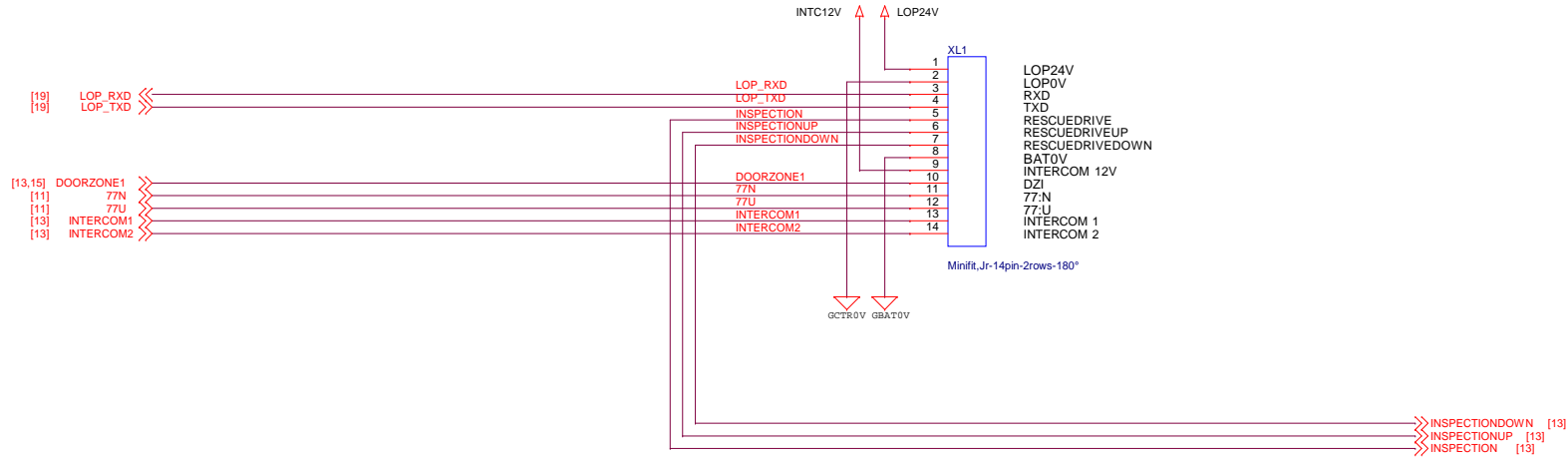


K	No change in this sheet	04.06.19	sompriksa/keisra	Designed by	Antti Toivonen	Checked by	Dena Xiao Hu	Language	En
J	D180, D181, R607 newly added	28.6.18	chando3/keisra	Dept	Konette	Date	12.5.2003	Product code	LCE
H	No change to this sheet	12.1.15	M.S						
G	Max232E section (resistor & PTC is added) updated	21.3.14	M.S						
Issue Change description		Date	Des. by	Appr. by	LCECPU561				
					RS232 / CPU internal CAN / X5 connector				
					Sales ref no.				
					Drawing no.				
					773391				
					Issue				
					K				
					Page				
					19 of 22				





Optional connector for LOP-CB control panel



K	No change in this sheet	04.06.19	Isompraka	keisraj	Designed by	Antti Toivonen	Checked by	Diana Xiao Hu	Language	En
J	No change in this sheet	28.6.18	ahand03	keisraj	Dept	Konette	Date	12.5.2003	Approved by	Jani Tertsonen
H	No change in this sheet	12.1.15	M.S						Product code	LCE
G	Xmm2 & Xmm3 ref designator name interchanged	21.3.14	M.S							
Issue Change description Date Des. by Appr. b										
Removable flash memory and No Cabinet XL1 connector										
Sales ref no. Drawing no. 773391 Issue 22 of 22										



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