myCPU 16

Final Release 16/05/2025

BUS Module	**Layout/Single Connector**
11	5/3
RESET IO	**BUS Manager**
11	9
CSM	**CLOCK**
10	23
I.DECODER	**PC**
9	15
FLAGS/SEQ	**I.REGISTER**
14	11
REG C	**REG A**
8	10
MAR	**ALU**
5	19
SRAM	**REG B**
15	10
DISPLAY 4 DIG	**OUTPUT REG**
16	6
PROTOBOARDS	**DISPLAY LED 8b/16b**
4,4,2,7	2/5