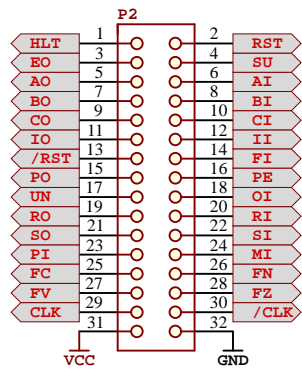
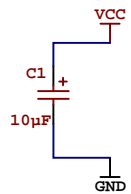
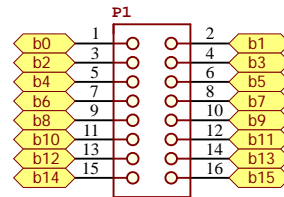




### Control BUS Connector



### Data BUS Connector



**Project:** myCPU Protoboard (LEFT)

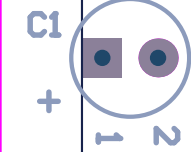
**Revision:** 4

**Date:** 19/01/2022

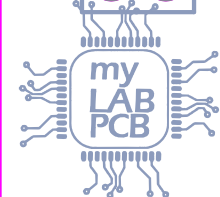
**Author:** Rafa Hernández

90.00

4.00



|     |   |   |     |
|-----|---|---|-----|
| HLT | ■ | ● | RS  |
| EO  | ● | ● | SU  |
| A0  | ● | ● | AI  |
| B0  | ● | ● | BI  |
| C0  | ● | ● | CI  |
| I0  | ● | ● | II  |
| /RS | ● | ● | FI  |
| P0  | ● | ● | PE  |
| UN  | ● | ● | OI  |
| R0  | ● | ● | RI  |
| S0  | ● | ● | SI  |
| PI  | ● | ● | MI  |
| FC  | ● | ● | FN  |
| FV  | ● | ● | FZ  |
| CK  | ● | ● | /CK |
| +5v | ● | ● | GND |



GND  
Vcc

1 2



21.11

60.00

4.00

ProtoBoard Rev1

Vcc  
GND

100.00



