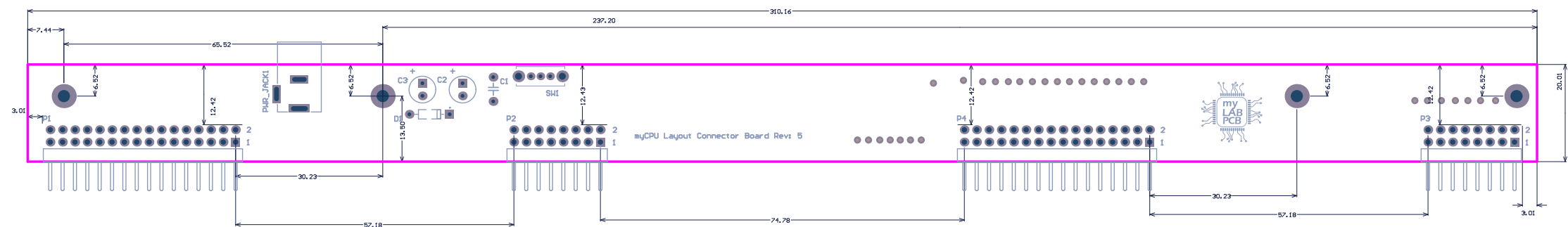


**Project:** myCPU Layout Connector module

**Revision:** 5

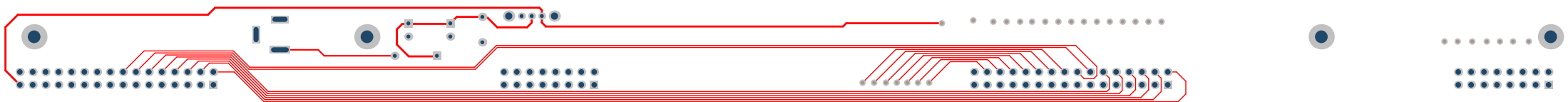
**Date:** 06-Apr-25

**Author:** Rafa Hernández















# Bill of Materials

## myCPU Layout Connector

Description	Value	Q
Ceramic capacitor	100nF	1
Electrolytic capacitor 16v/50v	330µF	2
1N5817 or 1N4007	1N5817	1
Pin Header, THT, pitch 2.54mm, Dual Row, Angled	32p	2
Pin Header, THT, pitch 2.54mm, Dual Row, Angled	16p	2
DC Power Jack, 2.5 A, 2mm Center Pin, 3 Position	2mm 3p	1
Mini slide switch SP2T 2mm ANGLED	SP2T	1



# Assembly List

## myCPU Layout Connector

Designator	Description	Value
C1	Ceramic capacitor	100nF
C2	Electrolytic capacitor 16v/50v	330µF
C3	Electrolytic capacitor 16v/50v	330µF
D1	1N5817 or 1N4007	1N5817
P1	Pin Header, THT, pitch 2.54mm, Dual Row, Angled	32p
P2	Pin Header, THT, pitch 2.54mm, Dual Row, Angled	16p
P3	Pin Header, THT, pitch 2.54mm, Dual Row, Angled	16p
P4	Pin Header, THT, pitch 2.54mm, Dual Row, Angled	32p
PWR_JACK1	DC Power Jack, 2.5 A, 2mm Center Pin, 3 Position	2mm 3p
SW1	Mini slide switch SP2T 2mm ANGLED	SP2T