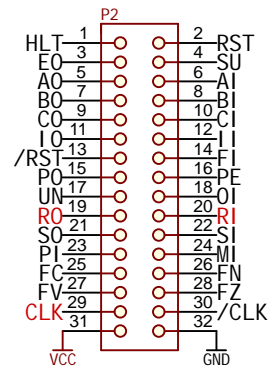
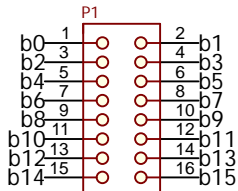




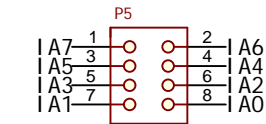
## Control BUS Connector



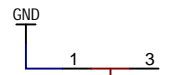
## Data BUS Connector



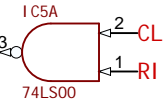
## Address INPUT connector



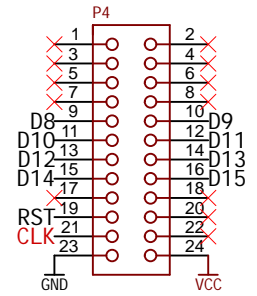
## PROG Mode WR



## RUN Mode WR

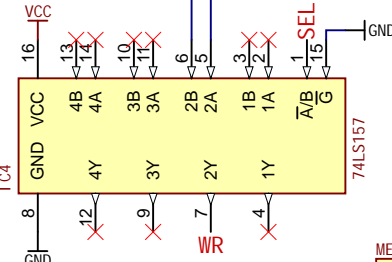
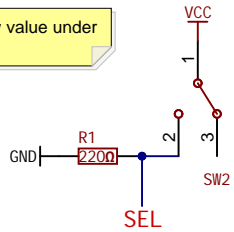


## Output Connector

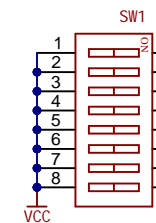
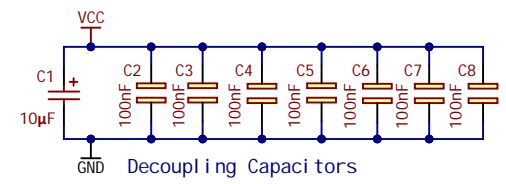


## Program Mode Switch

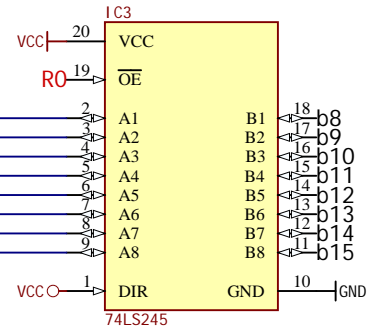
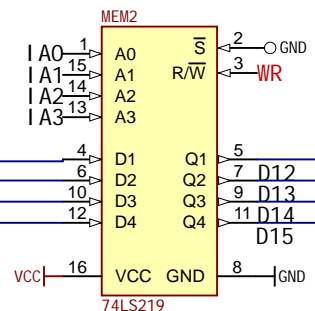
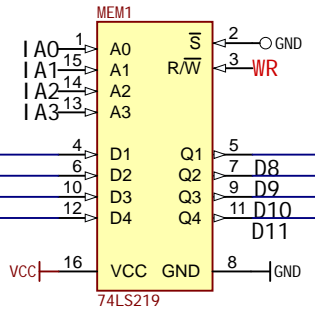
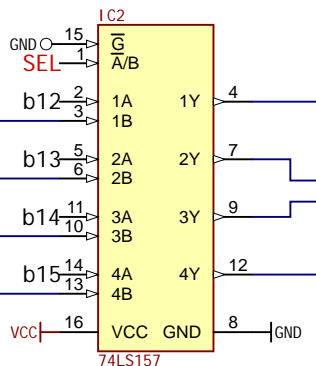
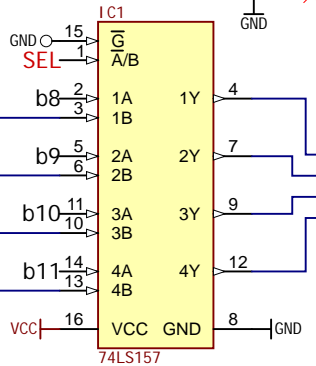
R1 should be a low value under 250  $\Omega$ .



Using only the less significative word of the address passed by MAR module: A0-A3. Supporting only 4 bit address.



Pull-Down Resistors



DIR set from A>B by connect PIN 1 to VCC

Title		
RAM Module (Ver: 219)		
Size	Number	Revision
A4		11
Date:	12/21/2021	Sheet of
File:	E:\OneDrive - UNED\Ram219.SchDoc	Drawn By:

