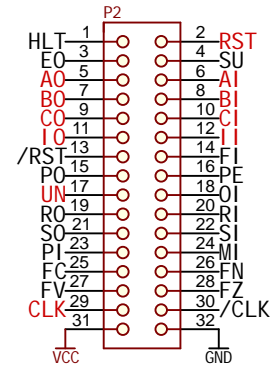
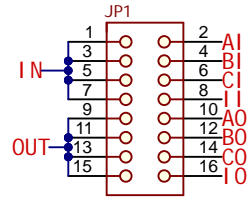


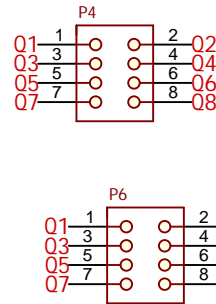
Control BUS Connector



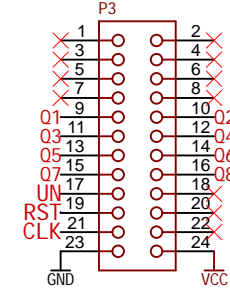
REGISTER SETUP



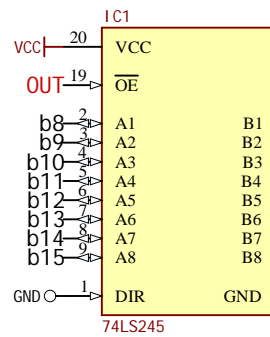
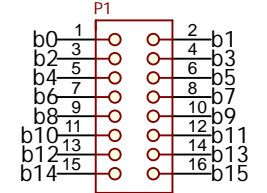
Direct Data Output



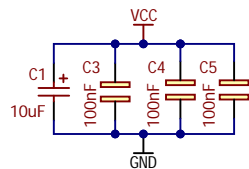
OUTPUT Connector



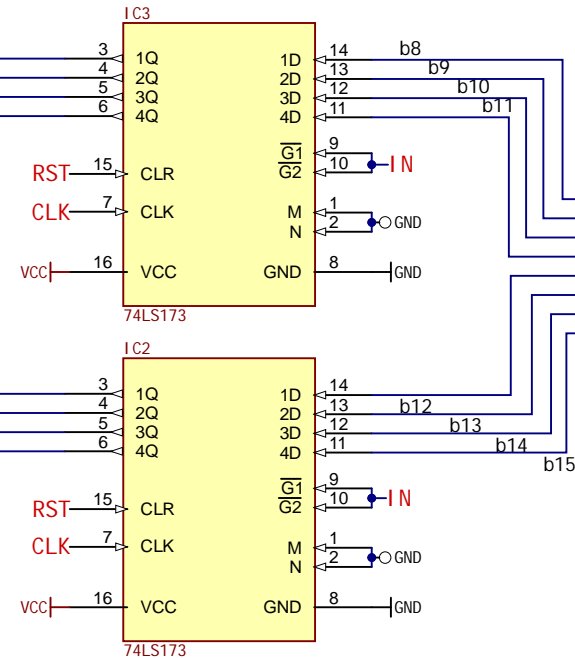
Data BUS Connector



▲ DIR to GND, set data flow direction from B to A



Decoupling Capacitors



▲ G1, G2 input control. Set both to LOW to load all bit on inputs in flip-flops

▲ M, N output control. Set all to LOW allow driving all outputs to 245.

Title		
Register 8 bits module (LEFT)		
Size	Number	Revision
A4		3
Date:	12/21/2021	Sheet of
File:	E:\OneDrive - UNED\...Register_8b_Led_R2_Sch.Doc	4 of 25

