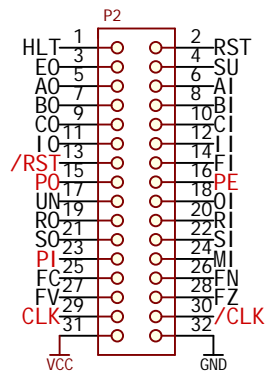
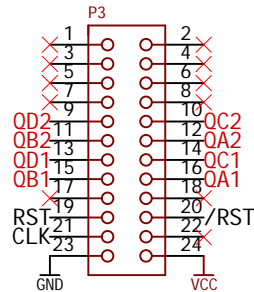


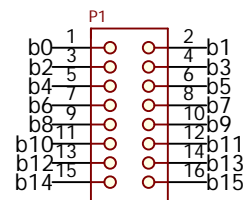
Control BUS Connector



OUTPUT Connector



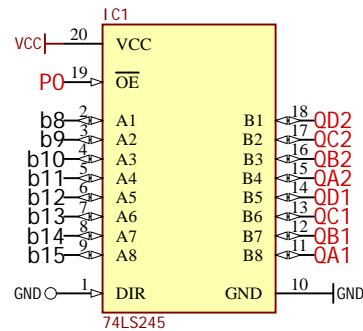
Data BUS Connector



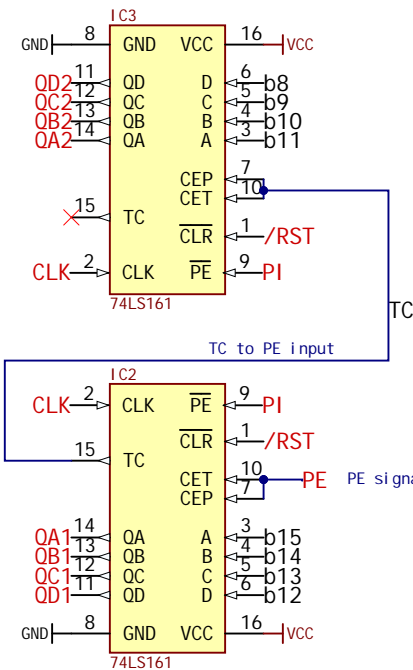
8 Bits counter. Mode 4 Bits can be used taking only the less significative 4 bits word of the counter.

Counts operation goes from QA to QD, A is less significative bit.

Cascade goes from IC2 to IC3 through pin TC (Terminal Count Output) to pin CET (10) forcing a count step.

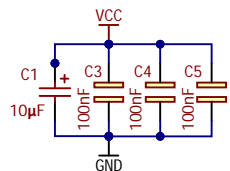


Pin DIR to GND means data flow from B >> A when a low PO signal comes.



PI is the control signal to load the current value on the DataBUS to the counters.

PE signal is not related to /PE pin



Decoupling Capacitors

Title		
SAP-1 Program Counter module		
Size	Number	Revision
A4		9
Date:	12/21/2021	Sheet of
File:	E:\OneDrive - UNED\...Program Counter	SchDoc

