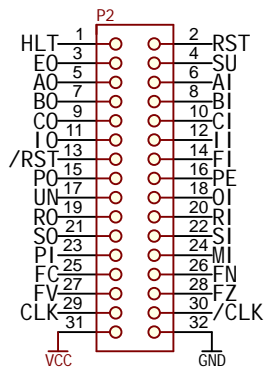
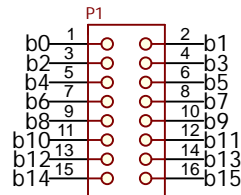


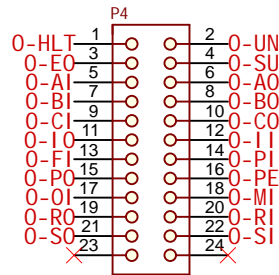
Control BUS Connector Top



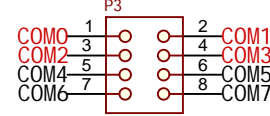
Data BUS Connector Top



Control Logic Decoder Connector

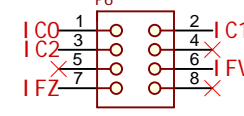


Instruction Register Connector

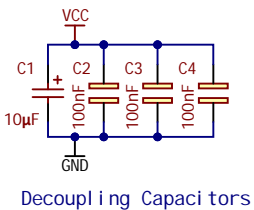
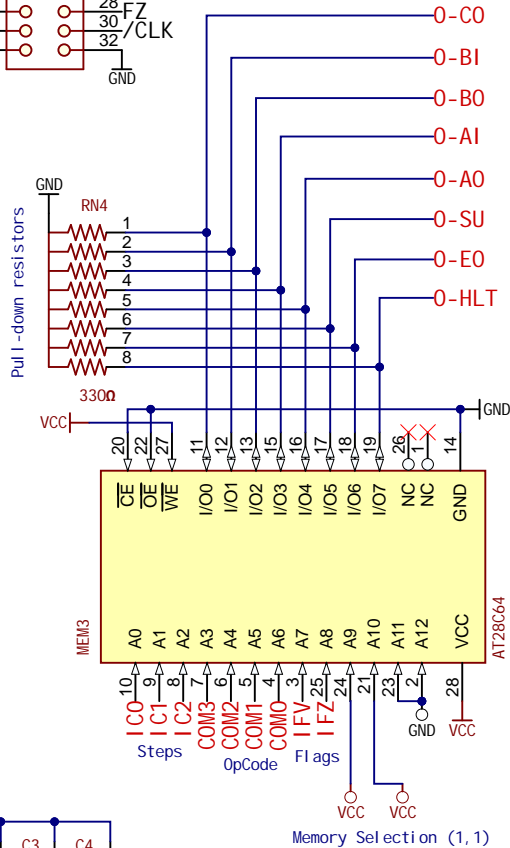


▲ Deocder use the most significative word stored in the instruction register which correspond with the command OPCODE.

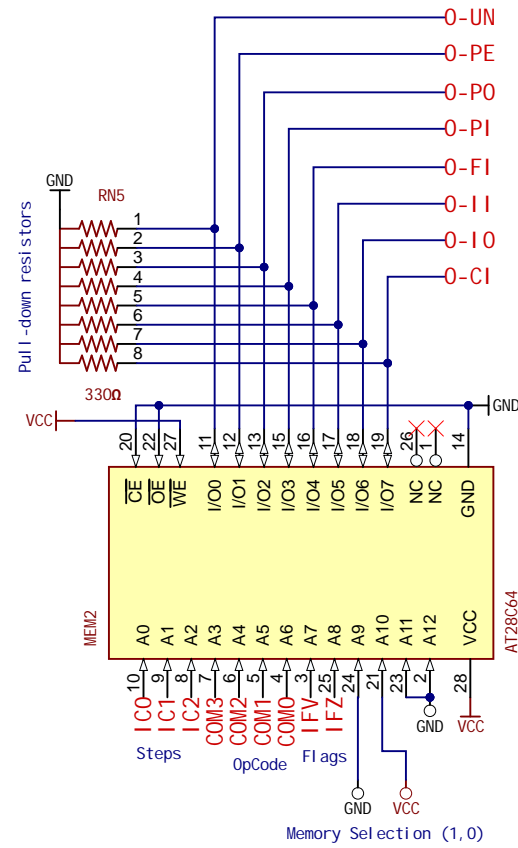
Flags register + Steps counter Connector



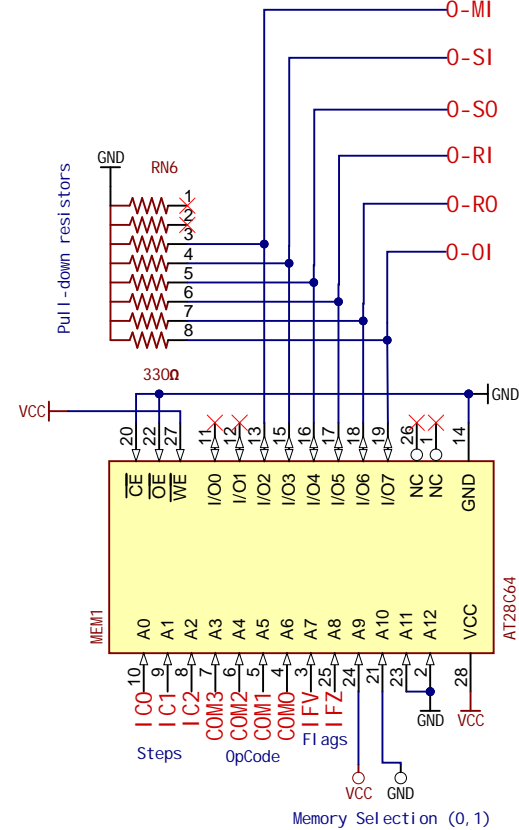
▲ Flags signals used are IFV, IFZ which are stored in the Flag Register not the current values of FV and FZ which come through the control BUS.



Decoupling Capacitors



▲ Memory selector set which byte part of the entire microinstruction are stored in the memory: MEM1 holds the byte 0-7, MEM2 holds the byte 8-15 and MEM3 holds byte 16-23



Title		
SAP -1 Logic Control Decoder (AT28C64)		
Size	Number	Revision
A4		6
Date:	12/21/2021	Sheet of
File:	E:\OneDrive-UNED\...LogicControl	AD280645 SchDoc

