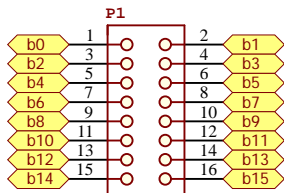
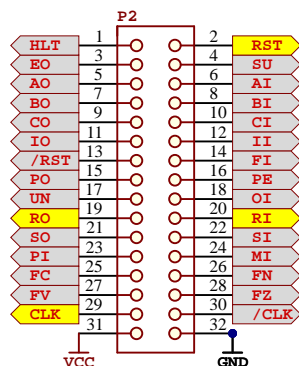
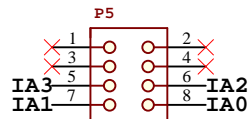


Control BUS Connector Data BUS Connector



Address INPUT connector

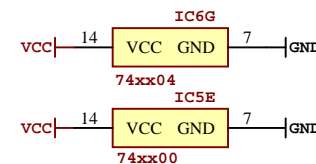
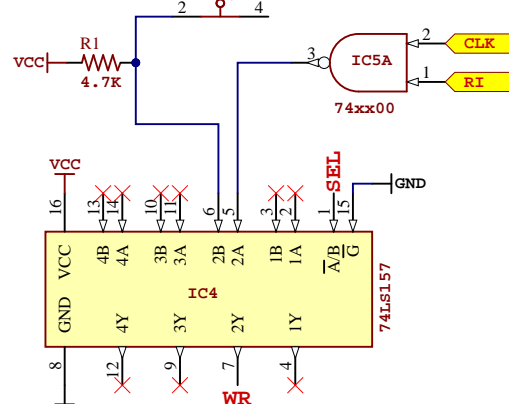


PROG Mode WR

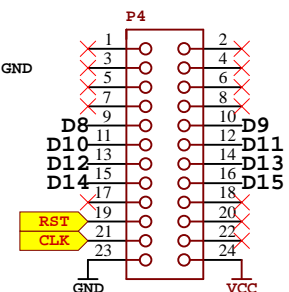


Active RI Signal is HIGH from CSM. Has to combine with clock signal to get a WR (LOW) signal for a synchronized write to memory.

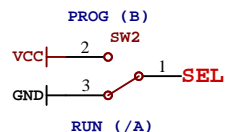
RUN Mode WR



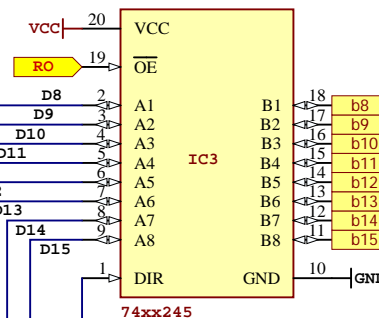
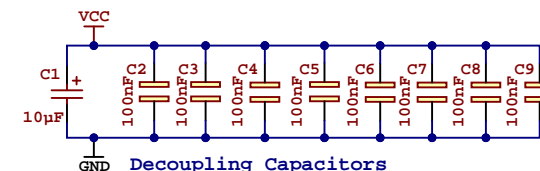
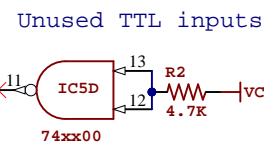
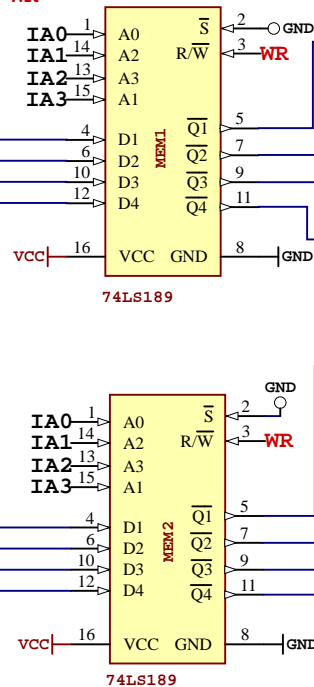
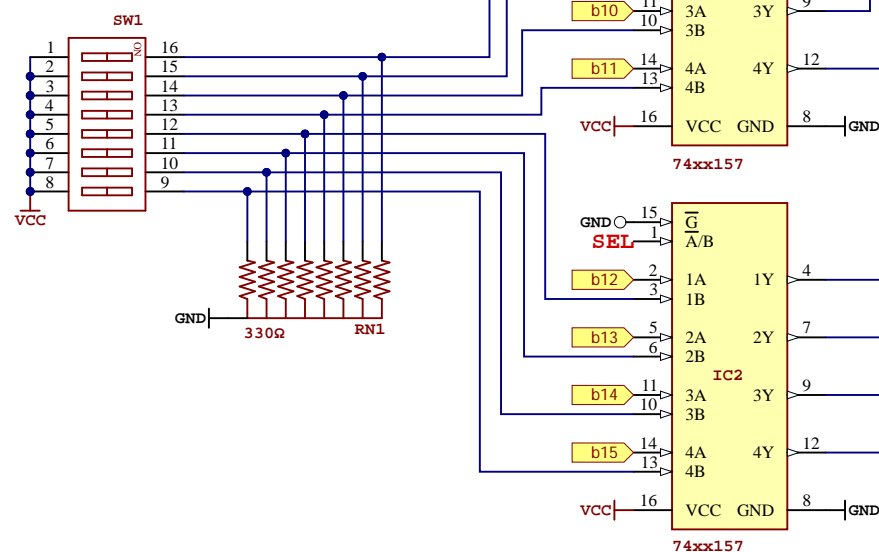
Output Connector



Programming Mode Switch



Using only the less significative word of the address from MAR module: A0-A3. Supporting only 4 bit



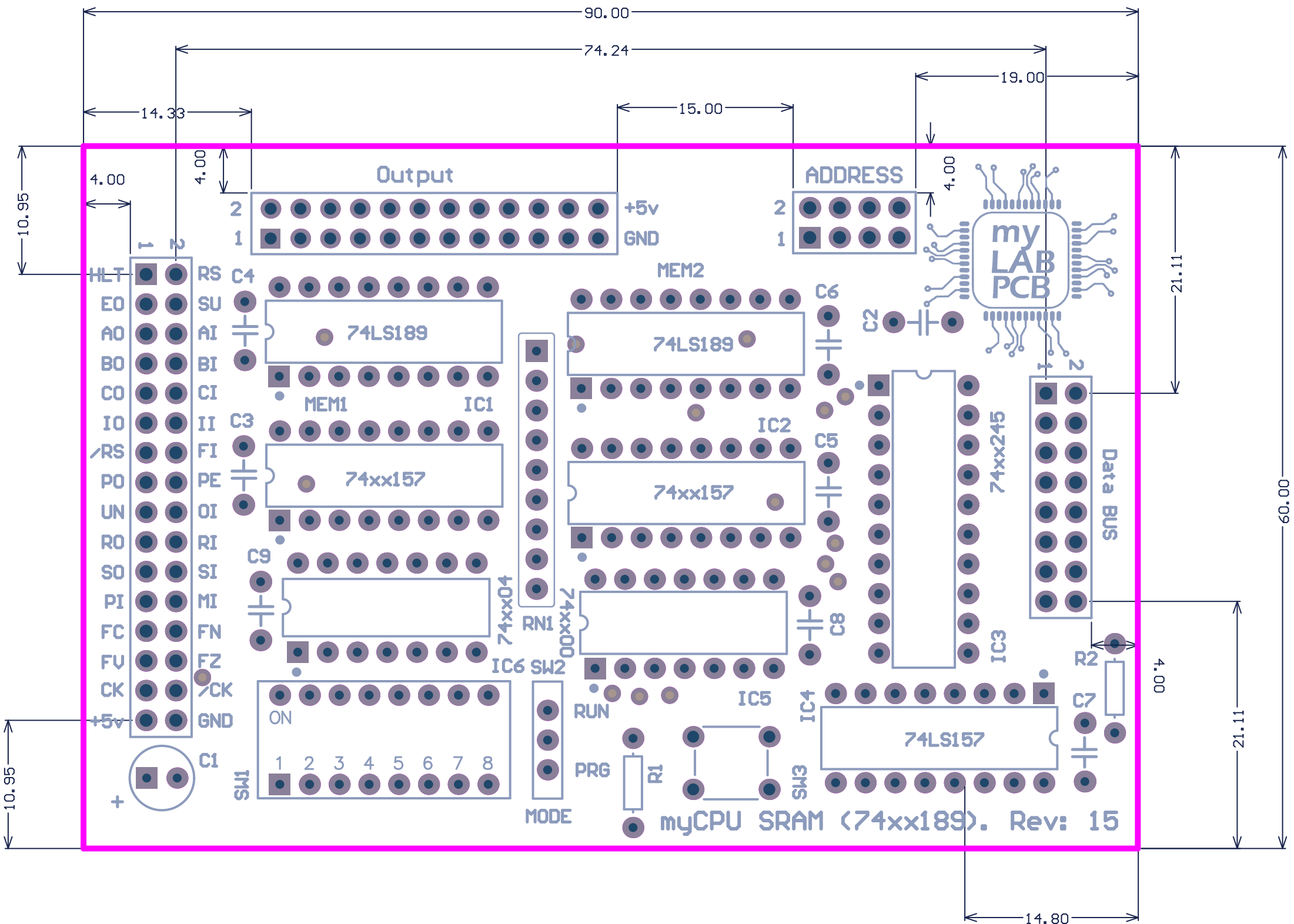
Project: myCPU SRAM module (Ver: 189)

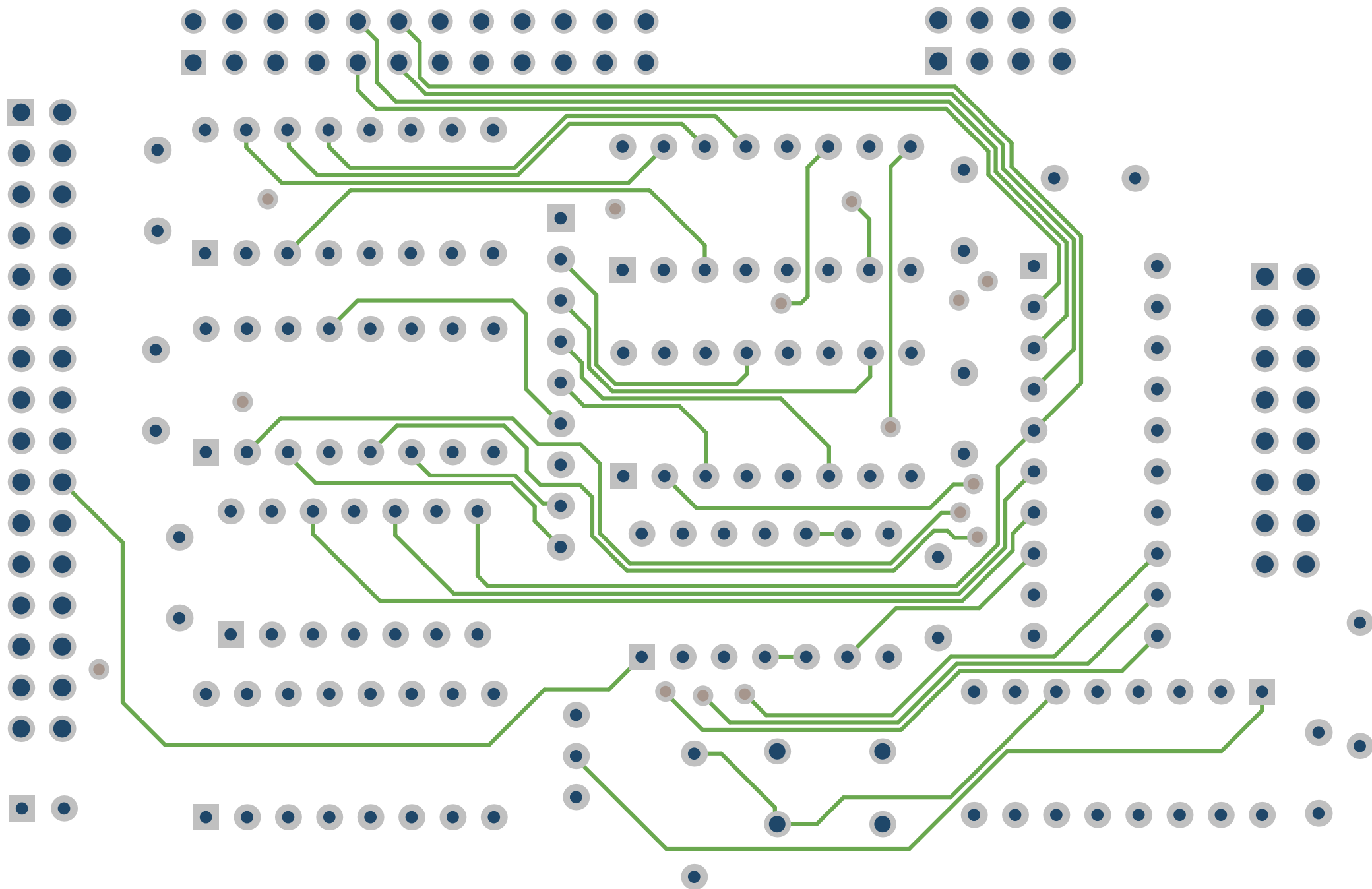
Revision: 15

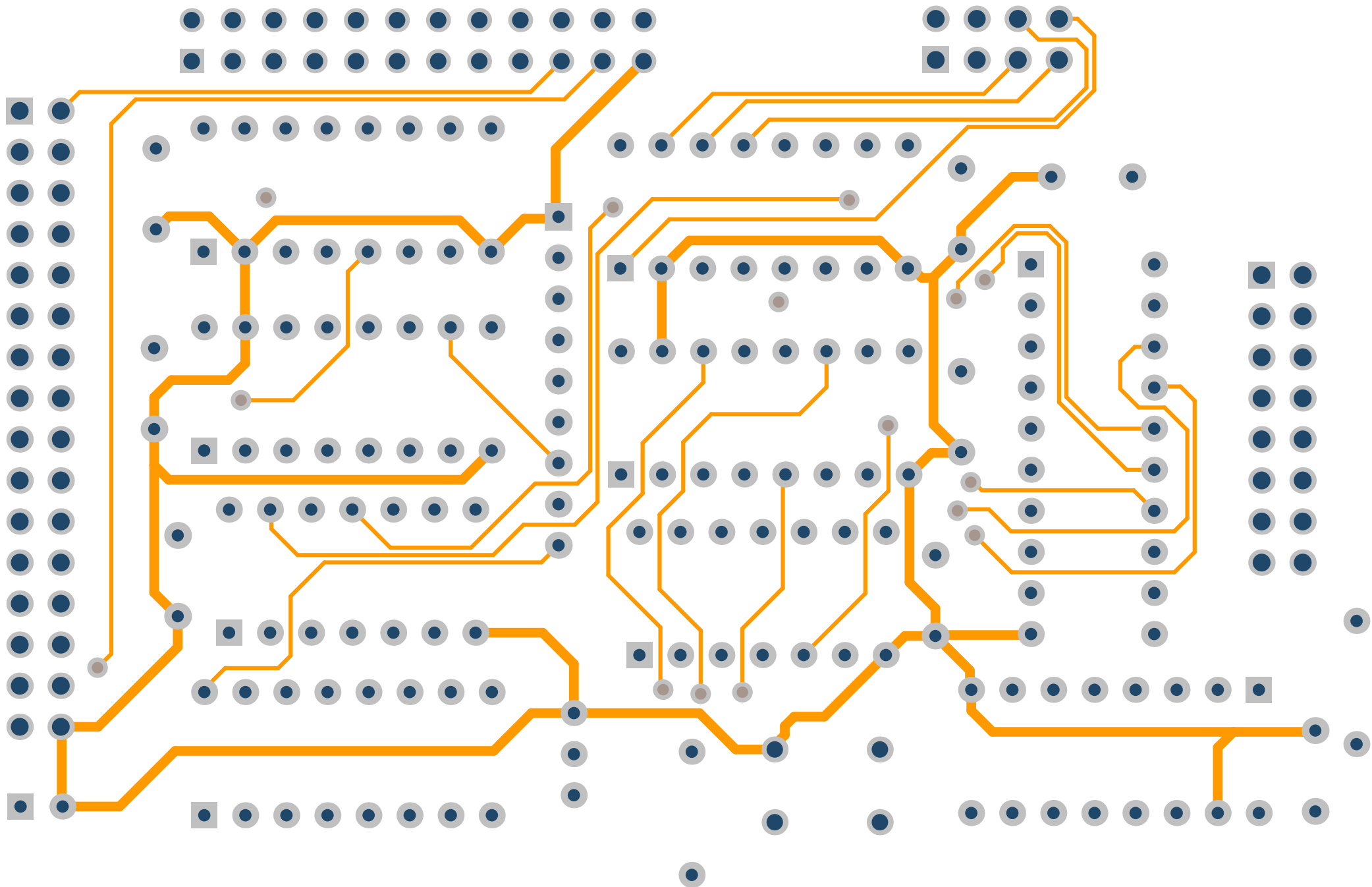
Date: 26-Jan-24

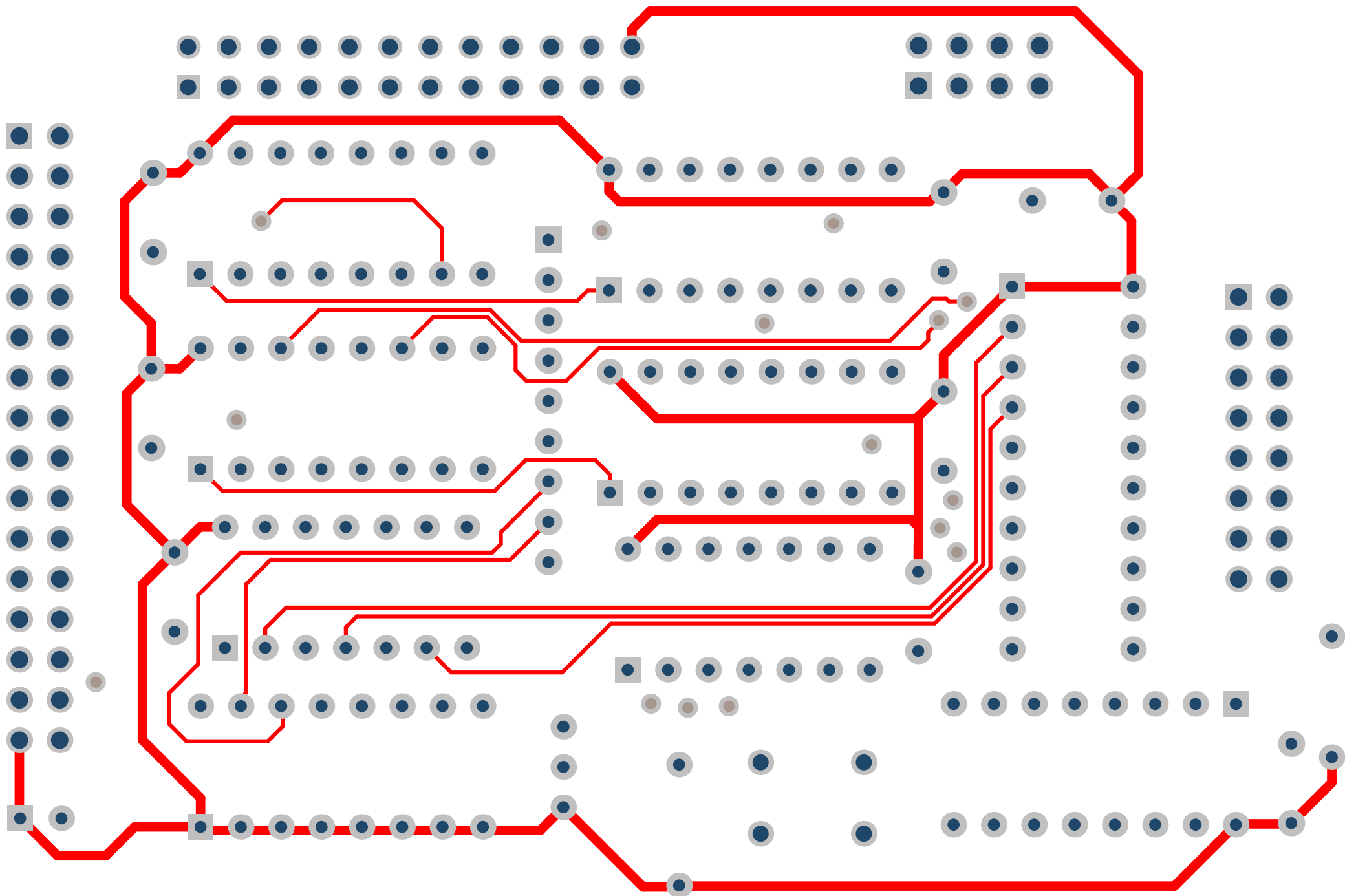
Author: Rafa Hernández

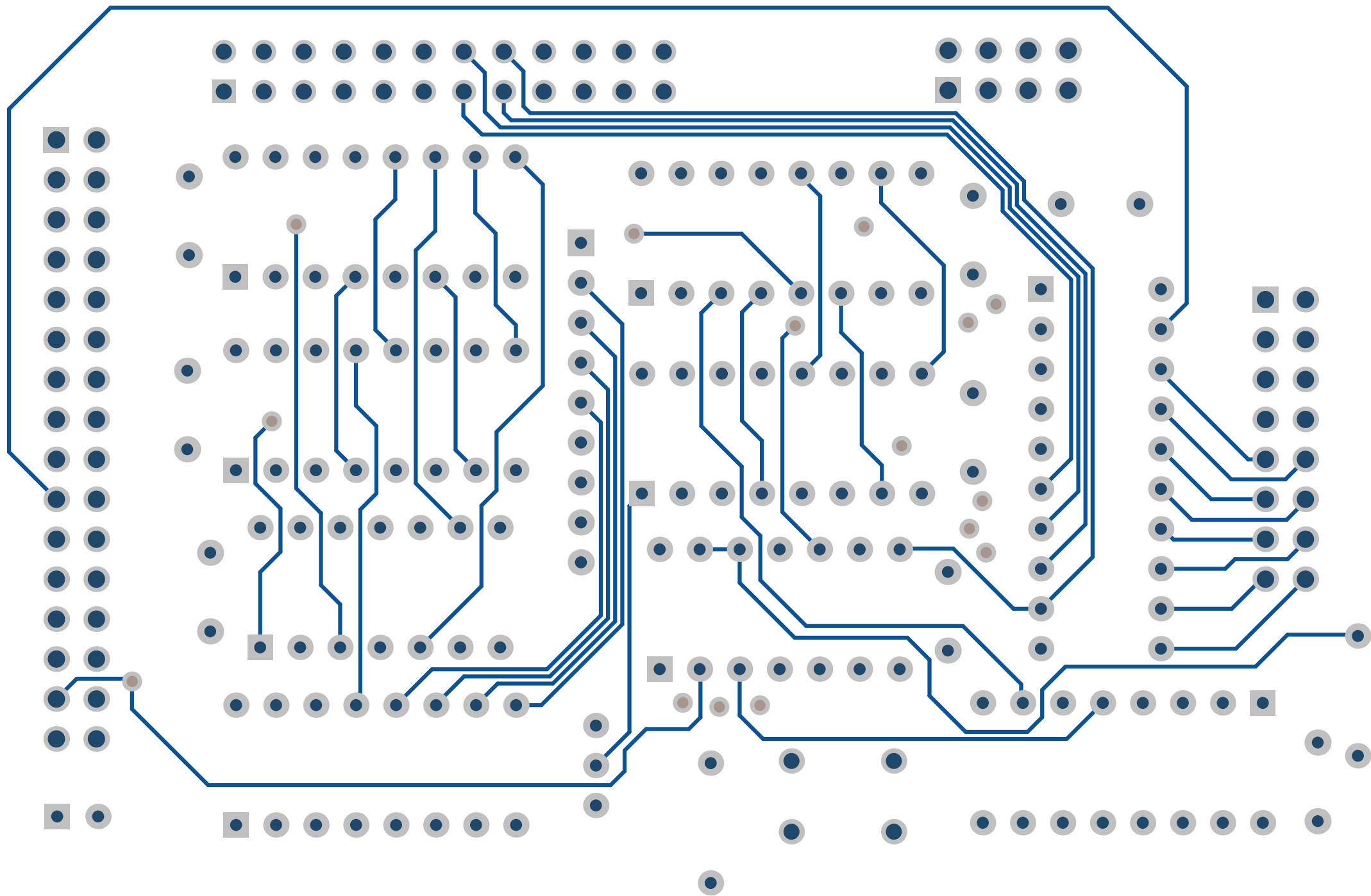


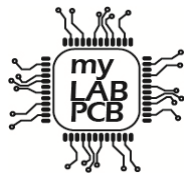












Bill of Materials

myCPU SRAM module (Ver: 189)

Description	Value	Q
Electrolytic capacitor 16v/50v	10µF	1
Ceramic capacitor	100nF	8
4-Bits 2-Line to 1 Line data selector	74xx157	2
Non inverting bus transceiver	74xx245	1
4-Bits 2-Line to 1 Line data selector	74LS157	1
Quad 2-input NAND gates	74xx00	1
Hex inverters	74xx04	1
SRAM 16x1 inverted outputs	74LS189	2
Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 16p	16p	1
Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 32p		1
Socket Header, THT, pitch 2.54mm, Dual Row, Vertical, 24p	24p	1
Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 8p	8p	1
Resistor Axial	4.7K	2
Resistor array 8 elements,9 pins	330Ω	1
DIP switch 8 positions	8 pos	1
Mini slide switch 2 pos, 3 pins		1
Tactile button 6 mm	6mm	1



Assembly List

myCPU SRAM module (Ver: 189)

Designator	Description	Value
C1	Electrolytic capacitor 16v/50v	10 μ F
C2	Ceramic capacitor	100nF
C3	Ceramic capacitor	100nF
C4	Ceramic capacitor	100nF
C5	Ceramic capacitor	100nF
C6	Ceramic capacitor	100nF
C7	Ceramic capacitor	100nF
C8	Ceramic capacitor	100nF
C9	Ceramic capacitor	100nF
IC1	4-Bits 2-Line to 1 Line data selector	74xx157
IC2	4-Bits 2-Line to 1 Line data selector	74xx157
IC3	Non inverting bus transceiver	74xx245
IC4	4-Bits 2-Line to 1 Line data selector	74LS157
IC5	Quad 2-input NAND gates	74xx00
IC6	Hex inverters	74xx04
MEM1	SRAM 16x1 inverted outputs	74LS189
MEM2	SRAM 16x1 inverted outputs	74LS189
P1	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 16p	16p
P2	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 32p	
P4	Socket Header, THT, pitch 2.54mm, Dual Row, Vertical, 24p	24p
P5	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 8p	8p
R1	Resistor Axial	4.7K
R2	Resistor Axial	4.7K
RN1	Resistor array 8 elements,9 pins	330 Ω
SW1	DIP switch 8 positions	8 pos
SW2	Mini slide switch 2 pos, 3 pins	
SW3	Tactile button 6 mm	6mm