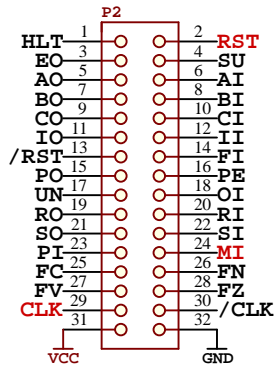
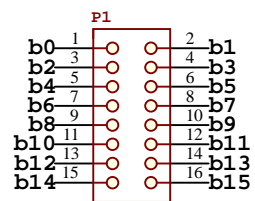


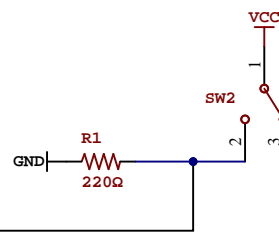
Control BUS Connector



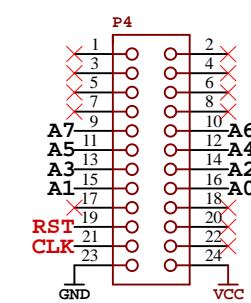
Data BUS Connector



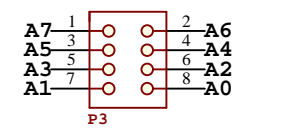
Programming Mode Switch



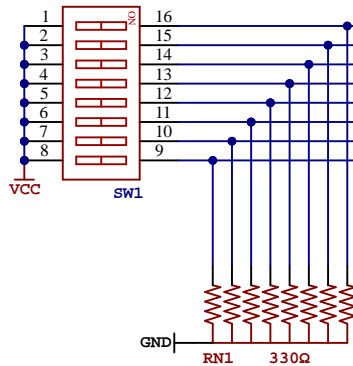
Output Connector



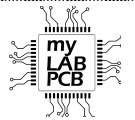
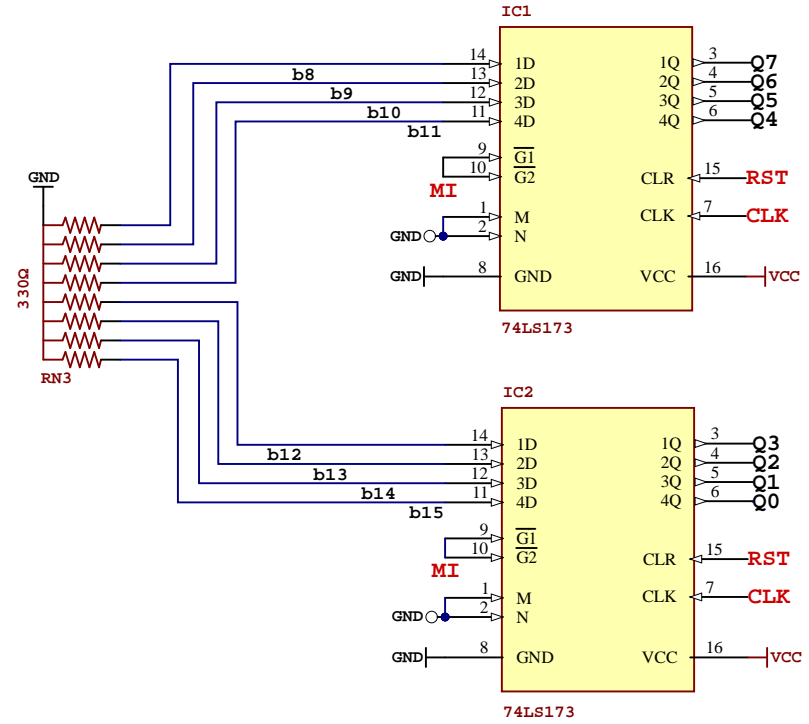
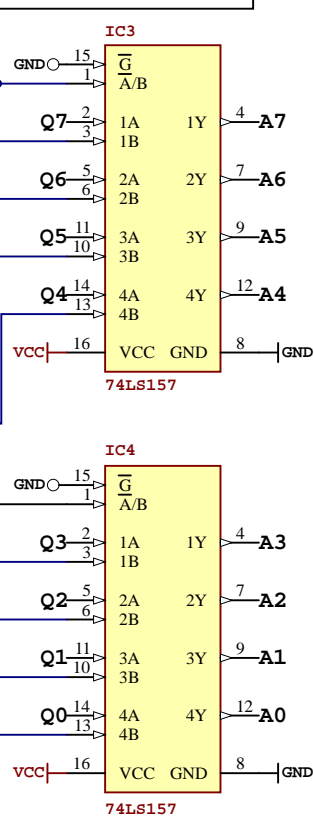
Address OUTPUT Connector



Set pin 15 to GND of 74LS157 to enable input



Decoupling Capacitors

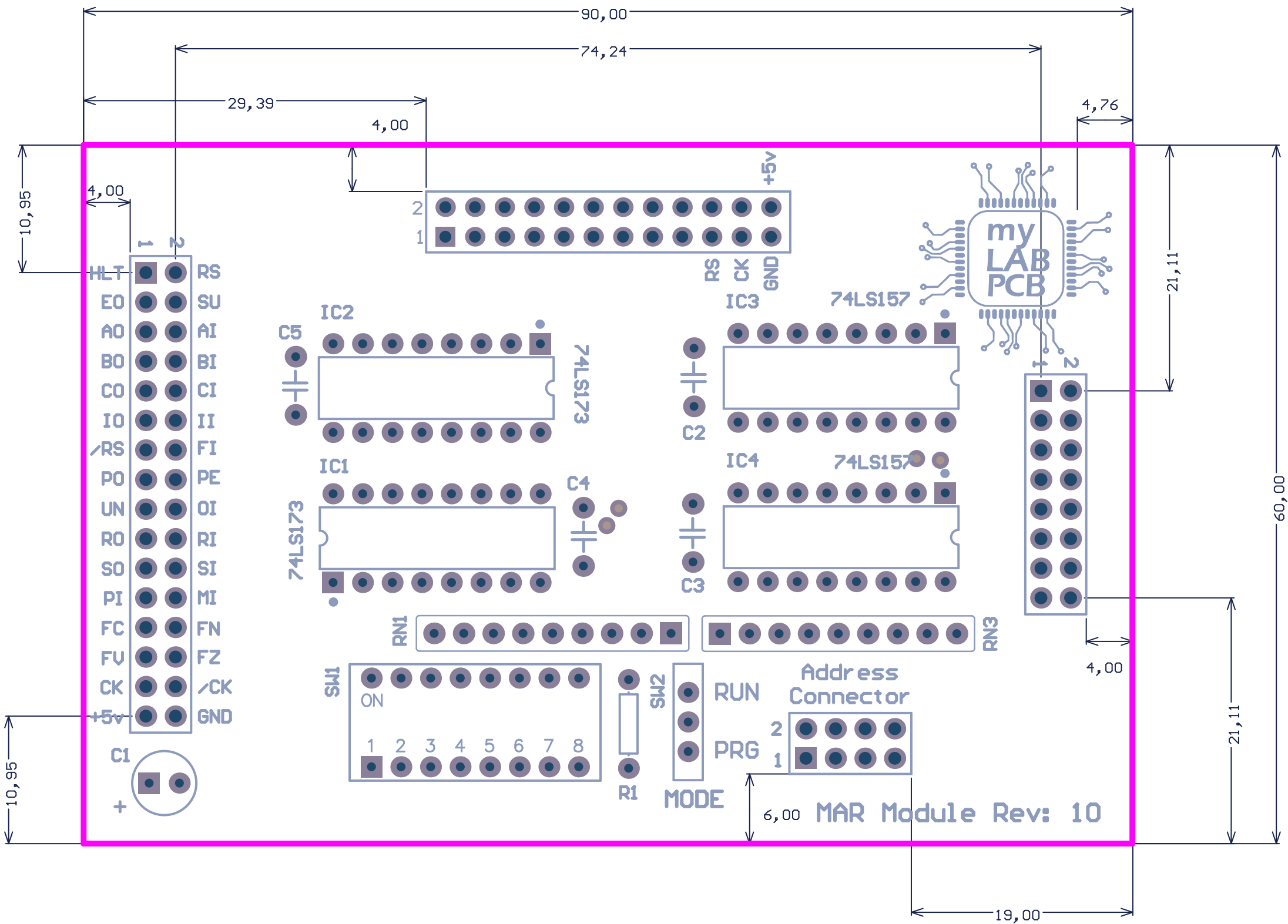


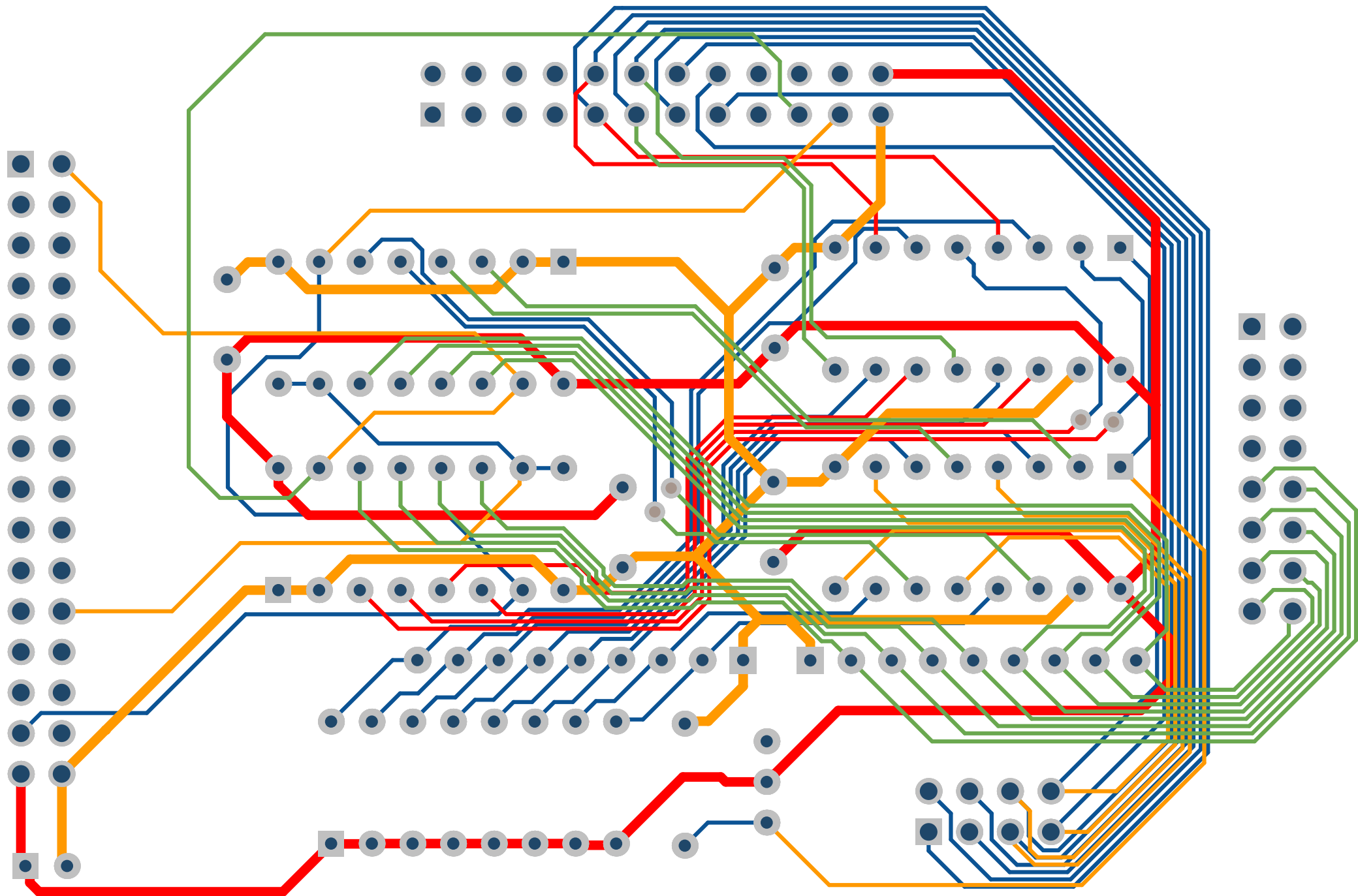
Project: myCPU MAR 8 bits Module

Revision: 10

Date: 09/10/2021

Author: Rafa Hernández







Bill of Materials

Designator	Description	Value	Q
C1	Electrolytic capacitor 16v/50v	10 μ F	1
C2, C3, C4, C5	Ceramic or tantalum capacitor	100nF	4
IC1, IC2	4-bit D-Type Register with 3 state outputs	74LS173	2
IC3	2-Line to 1 Line data selector 4-Bits	74LS157	1
IC4	4-Bits 2-Line to 1 Line data selector	74LS157	1
P1	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 16p		1
P2	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 32p		1
P3	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 8p		1
P4	Socket Header, THT, pitch 2.54mm, Dual Row, Vertical, 24p		1
R1	Resistor axial	330 Ω	1
RN1, RN3	Resistor array 8 elements,9 pins	330 Ω	2
SW1	DIP switch 8 positions		1
SW2	Mini slide switch 2 pos, 3 pins		1



Assembly List

Desig.	Description	Value
C1	Electrolytic capacitor 16v/50v	10 μ F
C2	Ceramic or tantalum capacitor	100nF
C3	Ceramic or tantalum capacitor	100nF
C4	Ceramic or tantalum capacitor	100nF
C5	Ceramic or tantalum capacitor	100nF
IC1	4-bit D-Type Register with 3 state outputs	74LS173
IC2	4-bit D-Type Register with 3 state outputs	74LS173
IC3	2-Line to 1 Line data selector 4-Bits	74LS157
IC4	4-Bits 2-Line to 1 Line data selector	74LS157
P1	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 16p	
P2	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 32p	
P3	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 8p	
P4	Socket Header, THT, pitch 2.54mm, Dual Row, Vertical, 24p	
R1	Resistor axial	330 Ω
RN1	Resistor array 8 elements,9 pins	330 Ω
RN3	Resistor array 8 elements,9 pins	330 Ω
SW1	DIP switch 8 positions	
SW2	Mini slide switch 2 pos, 3 pins	