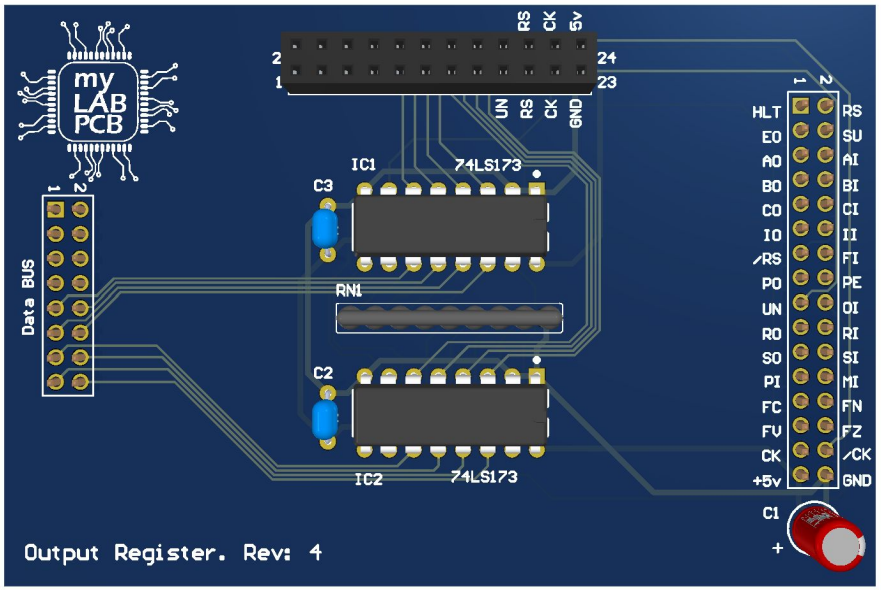
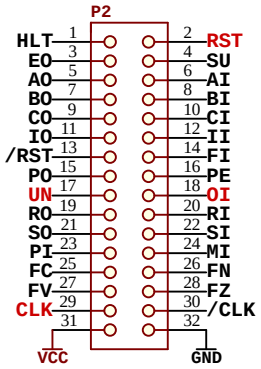
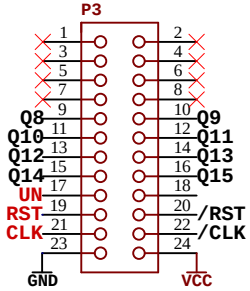

my
LAB
PCB
Data BUS
1
2
RS
CK
5v
24
23
UN
RS
CK
GND
IC1
74LS173
C3
RN1
C2
IC2
74LS173
HLT
EO
AO
BO
CO
IO
/RS
PO
UN
RO
SO
PI
FC
FV
CK
+5v
RS
SU
AI
BI
CI
II
FI
PE
OI
RI
SI
MI
FN
FZ
/CK
GND
C1
+
Output Register. Rev: 4

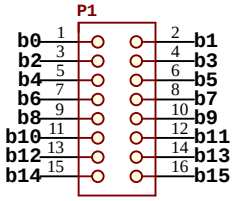
Control BUS Connector



OUTPUT Connector

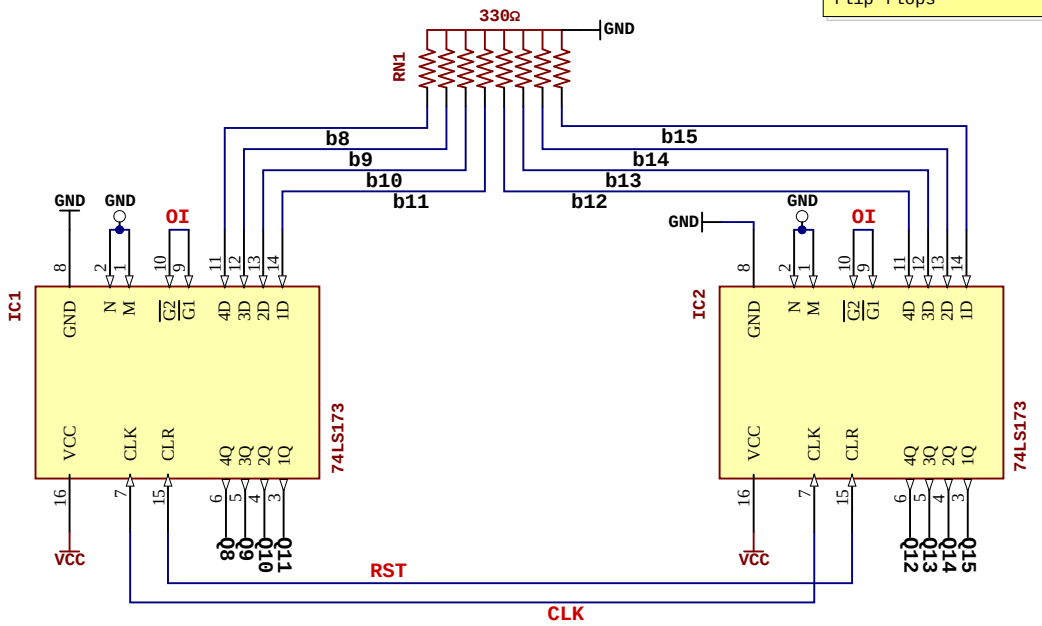


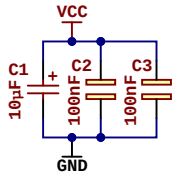
Data BUS Connector



G1, G2 input control.
Set both to LOW to load all bit on inputs into flip-flops

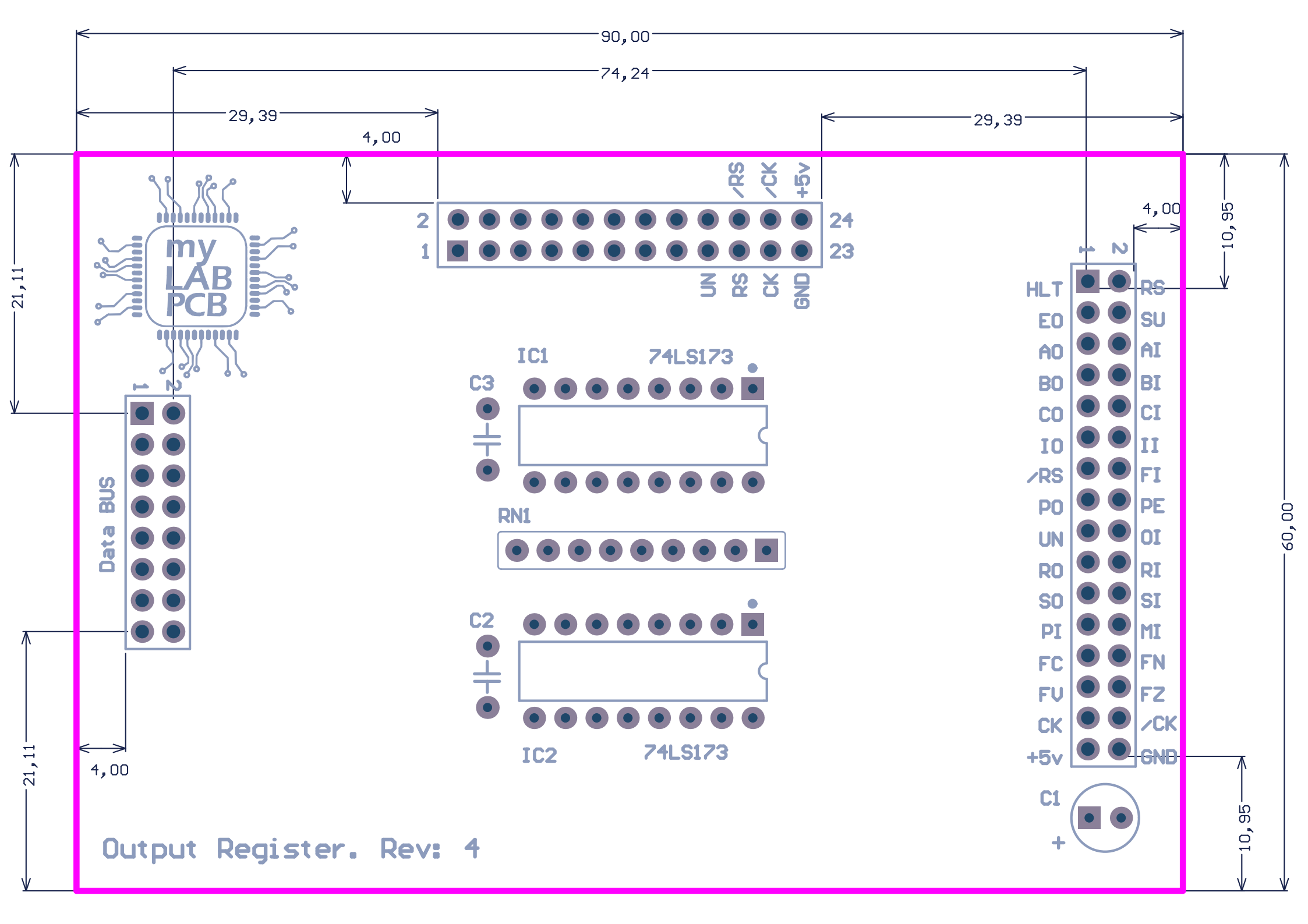
Pull-Down resistors

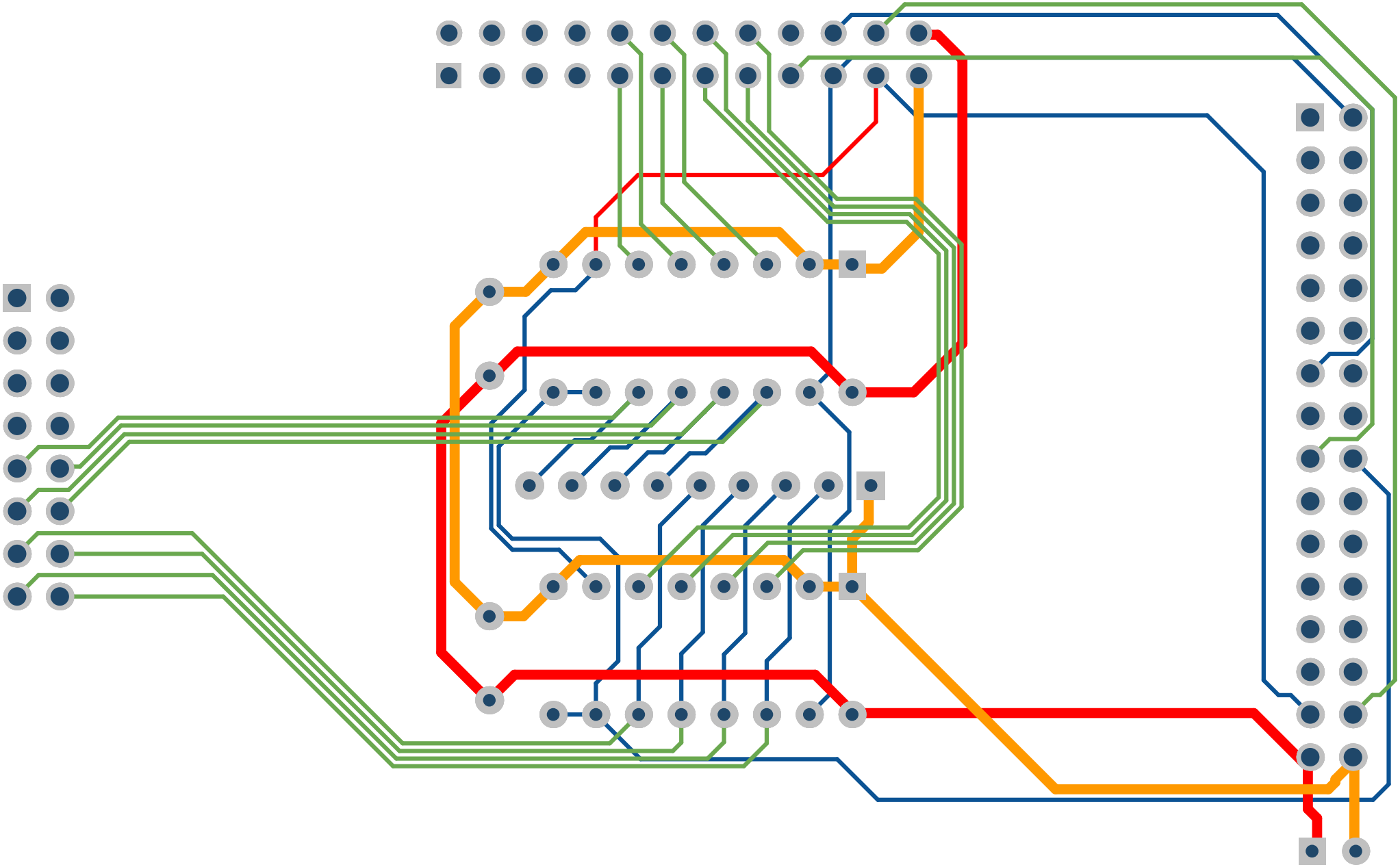




Decoupling Capacitors

Project: myCPU Output Register 8 bit	
Revision: 4	**Date:** 07/10/2021
Author: Rafa Hernández	


90,00
74,24
29,39
4,00
29,39
21,11
/RS
/CK
+5v
2
1
24
23
UN
RS
CK
GND
my
LAB
PCB
4,00
10,95
60,00
IC1
74LS173
C3
RN1
C2
IC2
74LS173
Data BUS
HLT
EO
AO
BO
CO
IO
/RS
PO
UN
RO
SO
PI
FC
FV
CK
+5v
RS
SU
AI
BI
CI
II
FI
PE
OI
RI
SI
MI
FN
FZ
/CK
GND
C1
+
4,00
21,11
10,95
Output Register. Rev: 4



Bill of Materials

Designator	Description	Value	Q
C1	Electrolytic capacitor 16v/50v	10µF	1
C2, C3	Ceramic or tantalum capacitor	100nF	2
IC1, IC2	4-bit D-Type Register with 3 state outputs	74LS173	2
P1	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 16p	16p	1
P2	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 32p	32p	1
P3	Socket Header, THT, pitch 2.54mm, Dual Row, Vertical, 24p	24p	1
RN1	Resistor array 8 elements,9 pins	330Ω	1

Assembly List

Desig.	Description	Value
C1	Electrolytic capacitor 16v/50v	10µF
C2	Ceramic or tantalum capacitor	100nF
C3	Ceramic or tantalum capacitor	100nF
IC1	4-bit D-Type Register with 3 state outputs	74LS173
IC2	4-bit D-Type Register with 3 state outputs	74LS173
P1	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 16p	16p
P2	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 32p	32p
P3	Socket Header, THT, pitch 2.54mm, Dual Row, Vertical, 24p	24p
RN1	Resistor array 8 elements,9 pins	330Ω