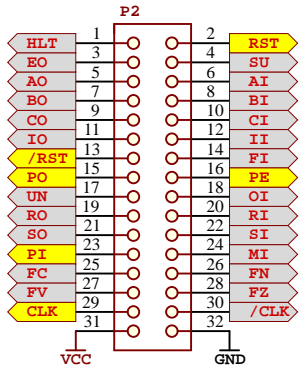
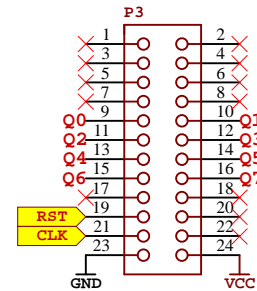




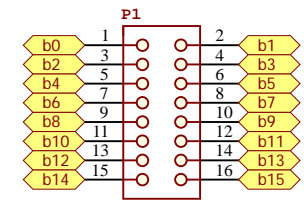
## Control BUS Connector



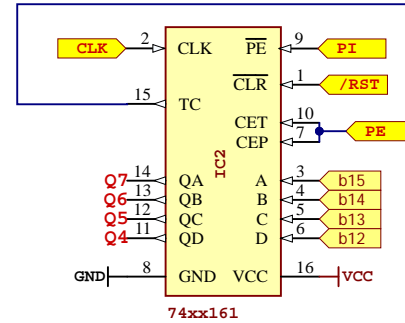
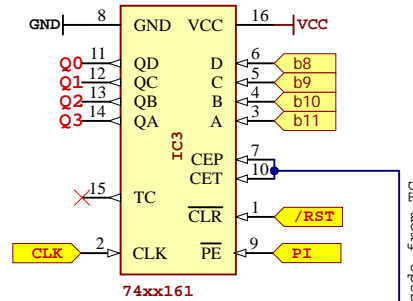
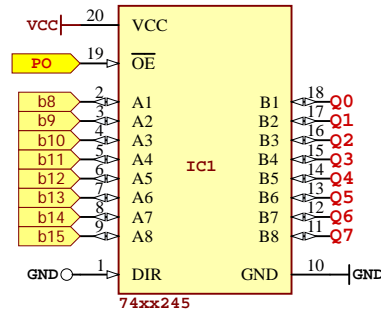
## OUTPUT Connector



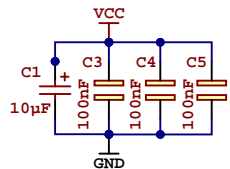
## Data BUS Connector



A Count operation goes from QA to QD, A pin is less significant bit input.

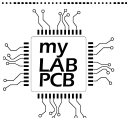


A PE control signal is not related to /PE pin of the IC



## Decoupling Capacitors

"74xx" Indicates the use LS (TTL) or HC(CMOS) ICs as your prefer for your build.

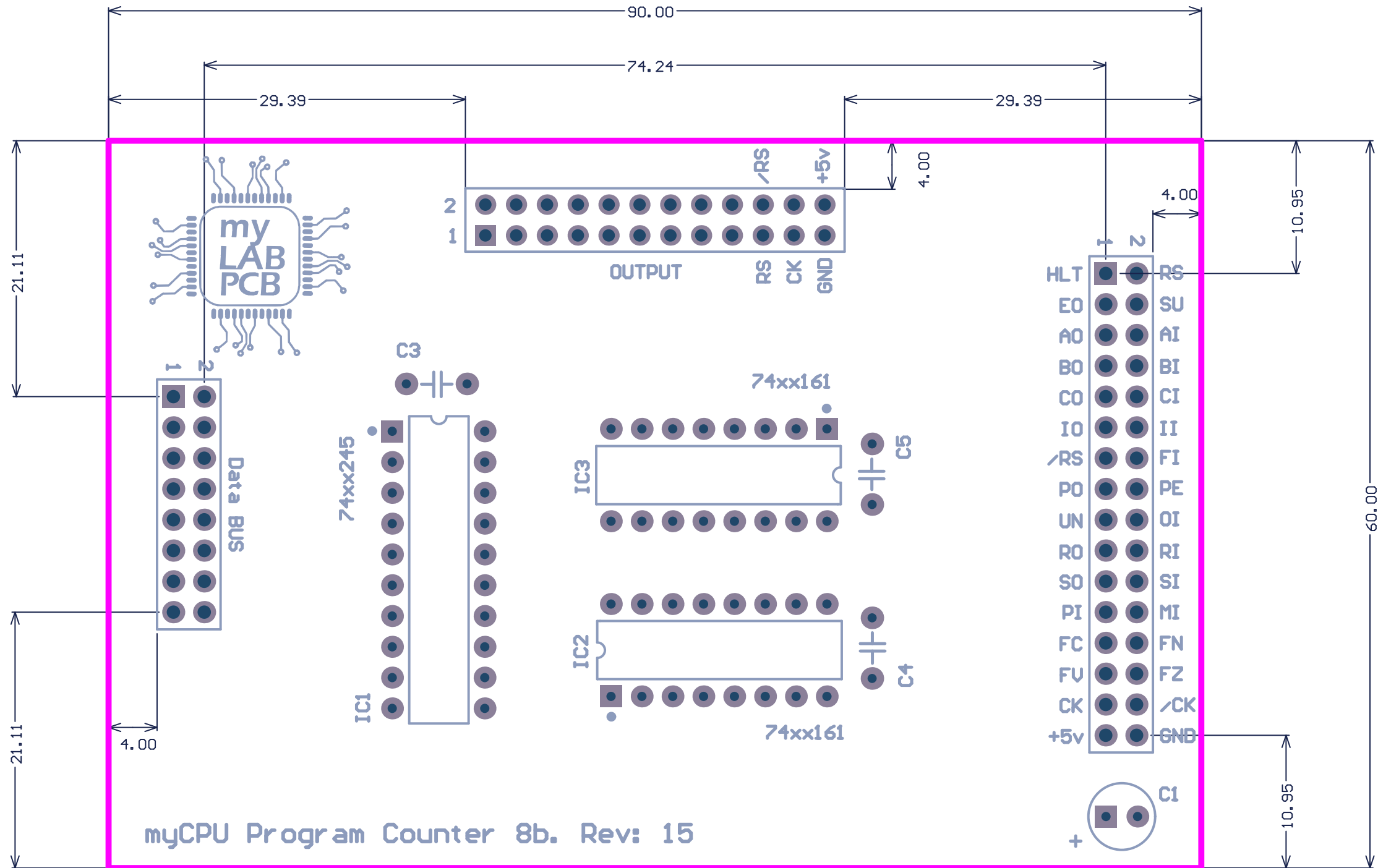


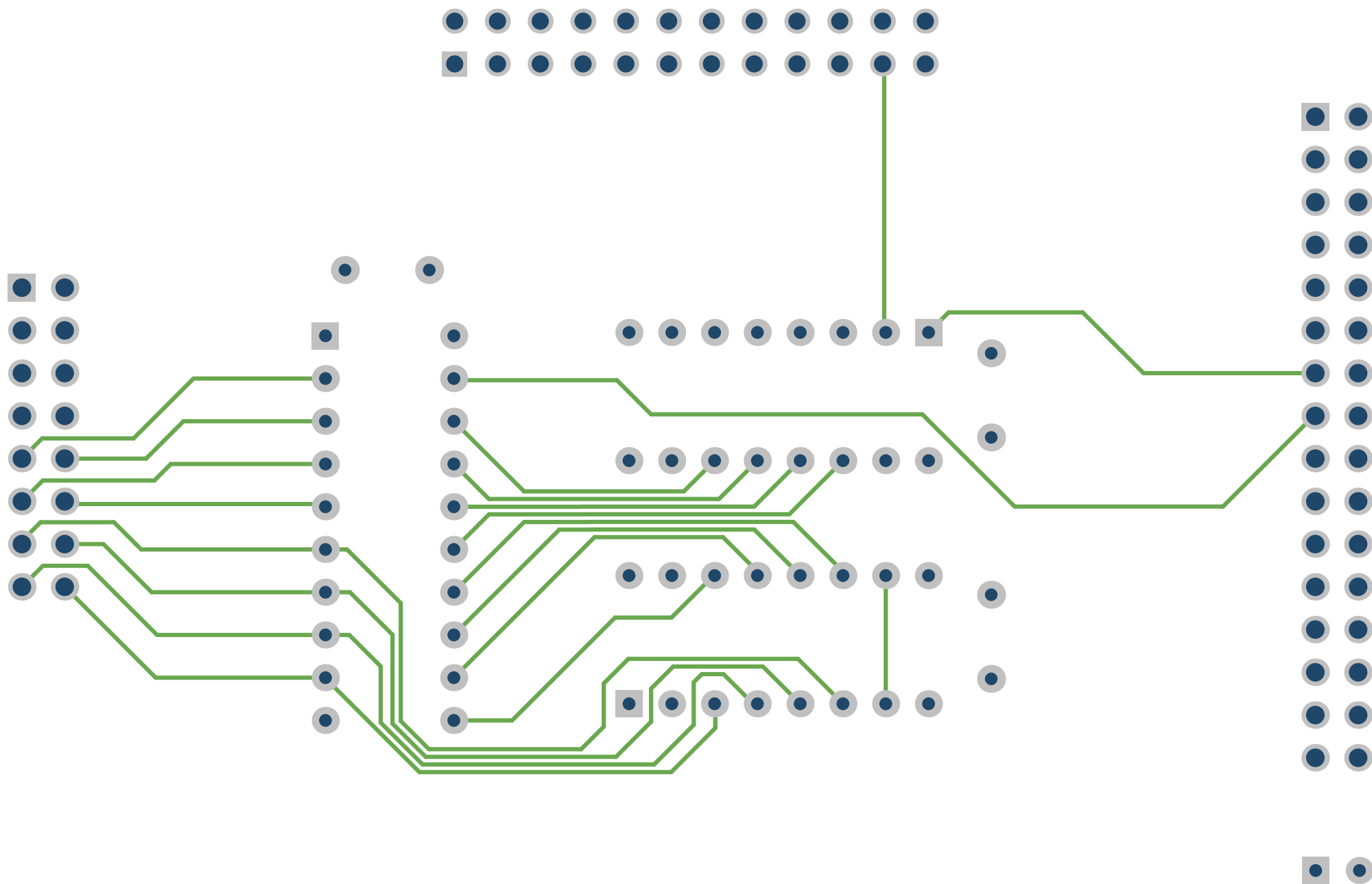
**Project:** myCPU Program Counter 8b

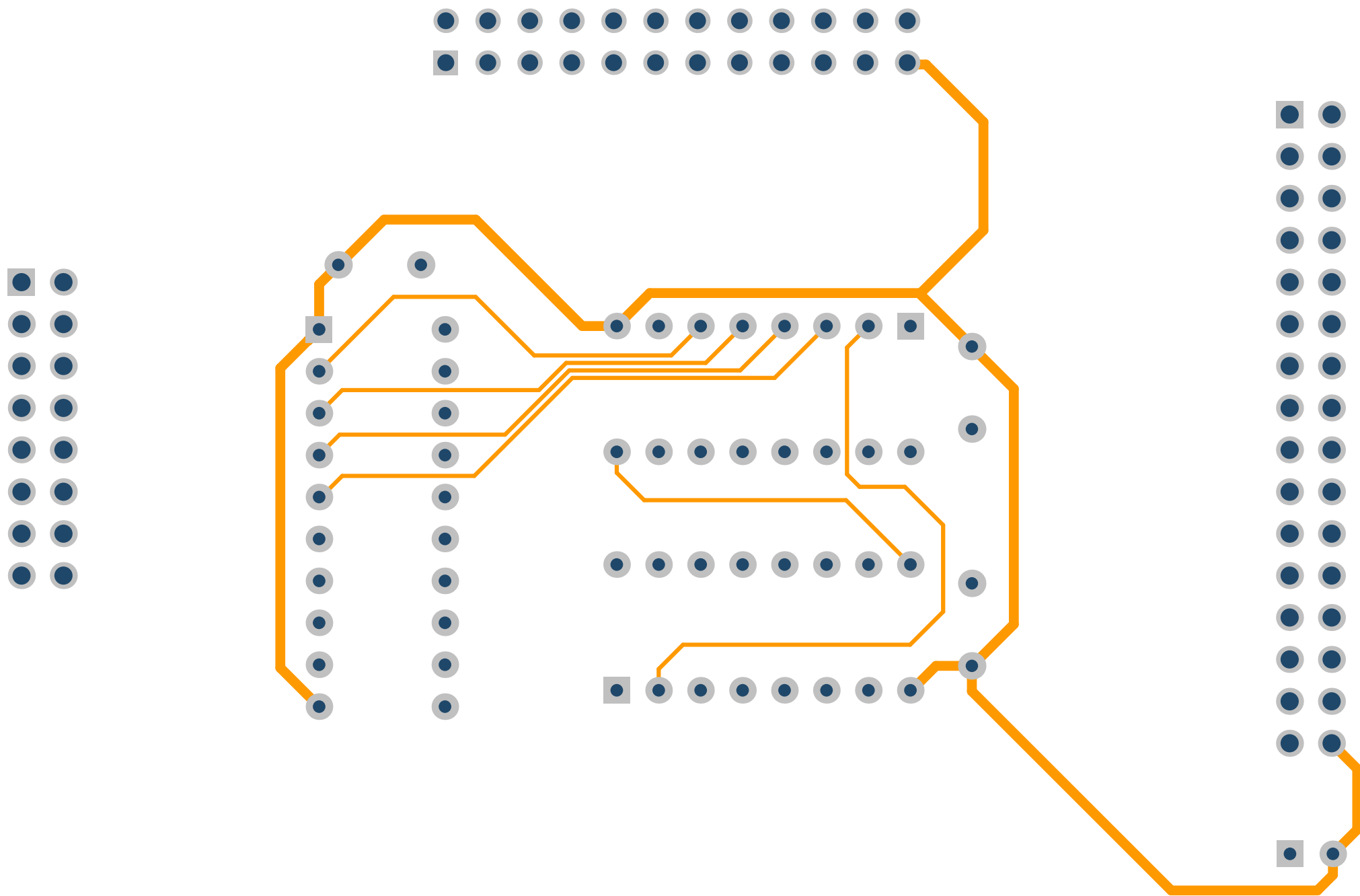
**Revision:** 15

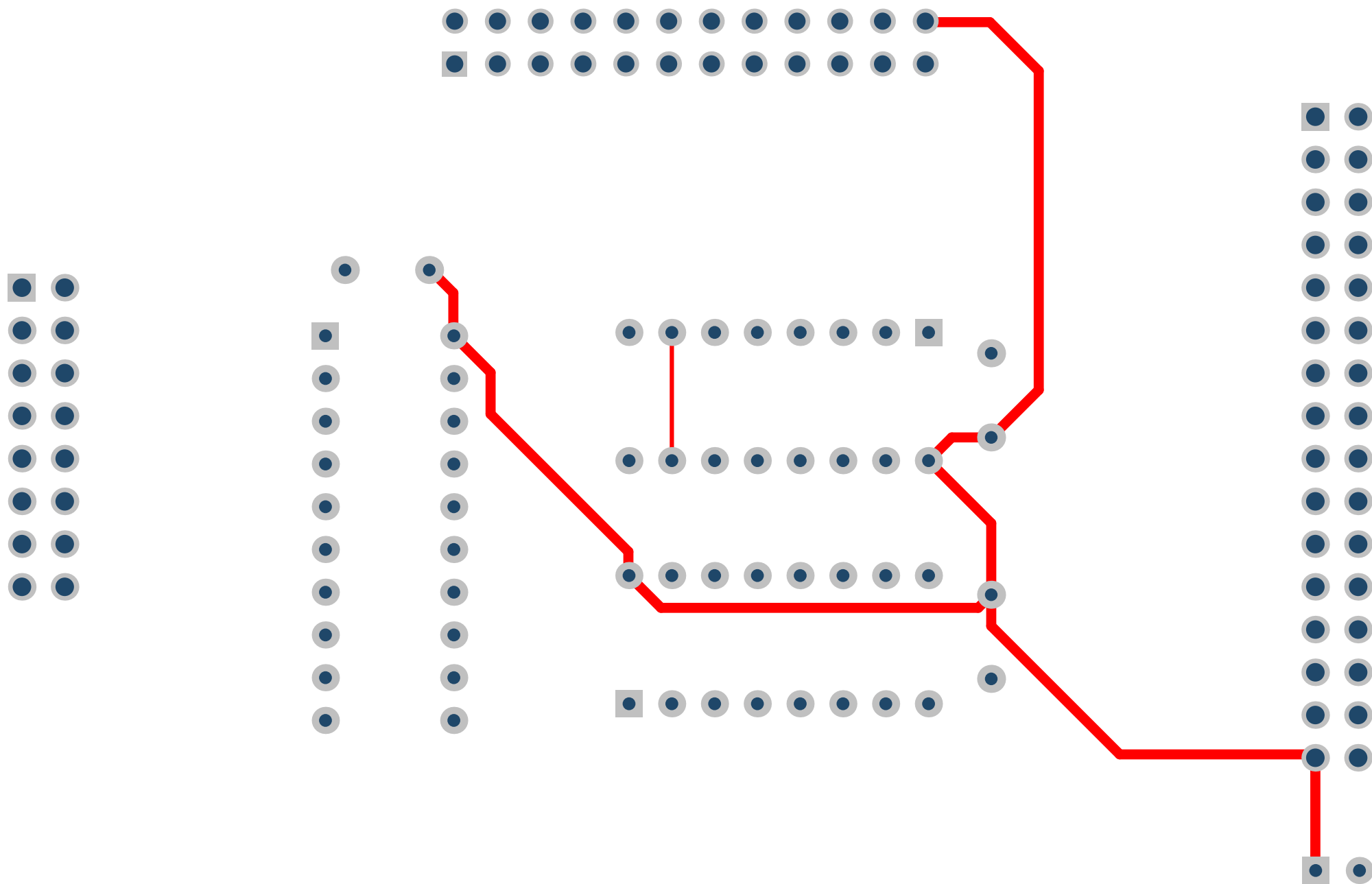
**Date:** 04-Apr-24

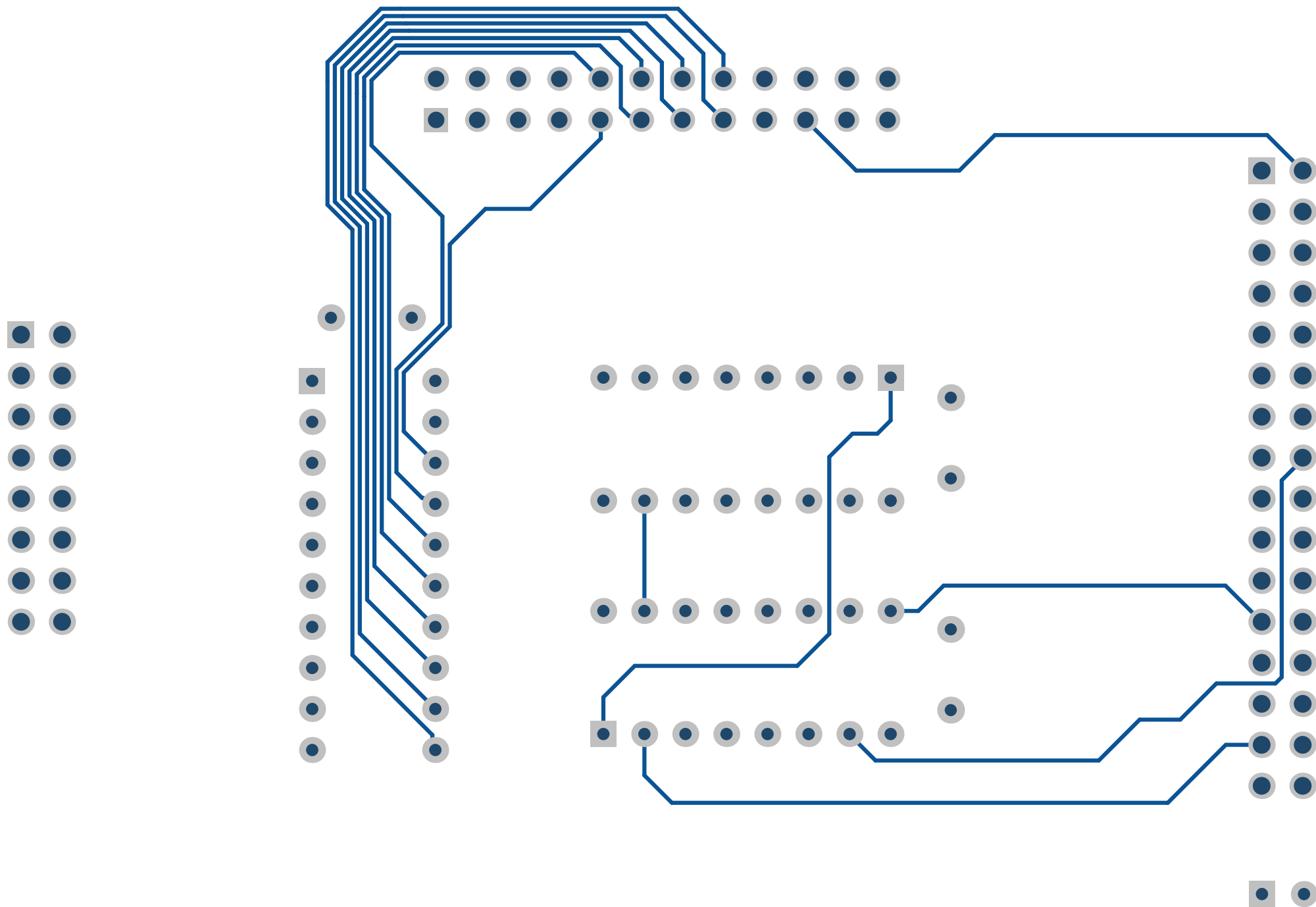
**Author:** Rafa Hernández















# Bill of Materials

## myCPU Program Counter 8b

Description	Value	Q
Electrolytic capacitor 16v/50v	10 $\mu$ F	1
Ceramic or tantalum capacitor	100nF	3
Non inverting bus transceiver	74xx245	1
4-Bit sync counter	74xx161	2
Pin Header, THT, pitch 2.54mm, Dual Row, Vertical	16p	1
Pin Header, THT, pitch 2.54mm, Dual Row, Vertical	32p	1
Socket Header, THT, pitch 2.54mm, Dual Row, Vertical	24p	1



# Assembly List

## myCPU Program Counter 8b

Designator	Description	Value
C1	Electrolytic capacitor 16v/50v	10 $\mu$ F
C3	Ceramic or tantalum capacitor	100nF
C4	Ceramic or tantalum capacitor	100nF
C5	Ceramic or tantalum capacitor	100nF
IC1	Non inverting bus transceiver	74xx245
IC2	4-Bit sync counter	74xx161
IC3	4-Bit sync counter	74xx161
P1	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical	16p
P2	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical	32p
P3	Socket Header, THT, pitch 2.54mm, Dual Row, Vertical	24p