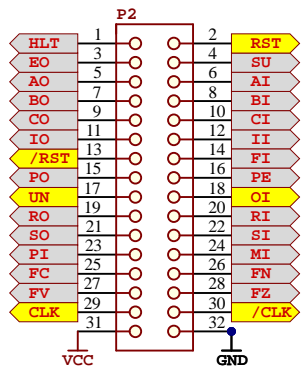
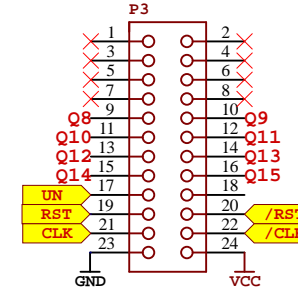




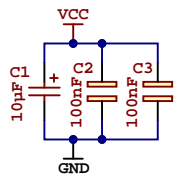
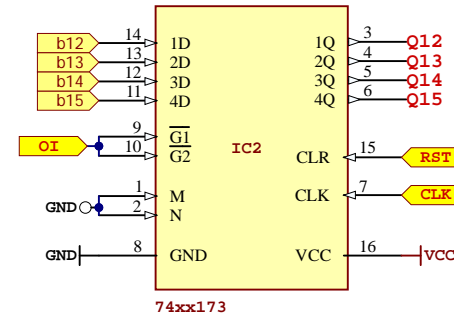
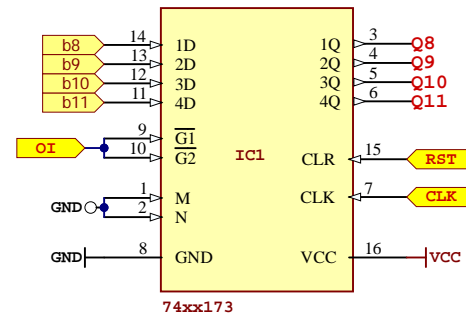
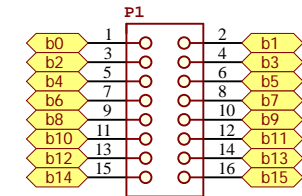
## Control BUS Connector



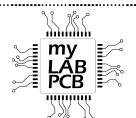
## OUTPUT Connector



## Data BUS Connector



Decoupling Capacitors

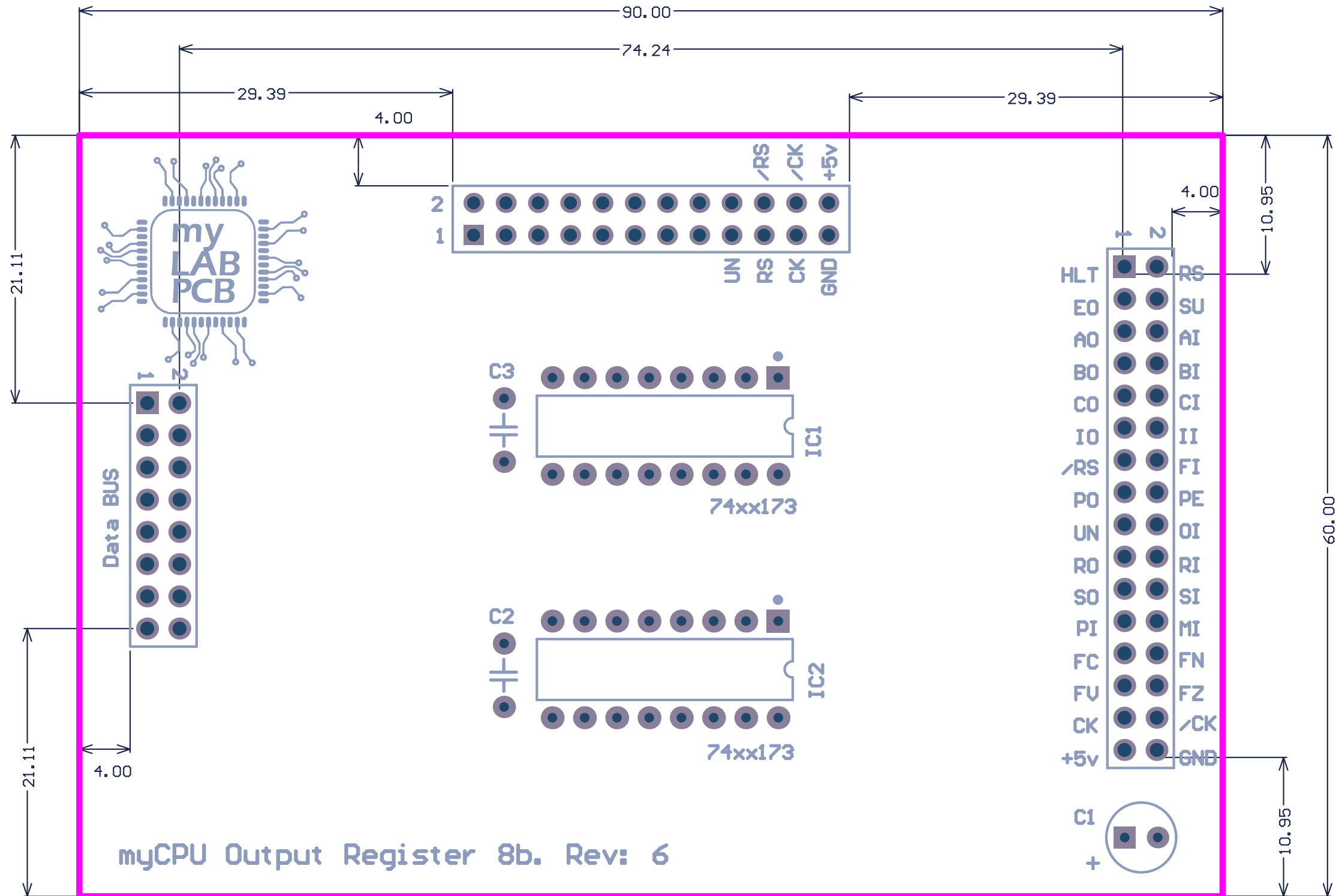


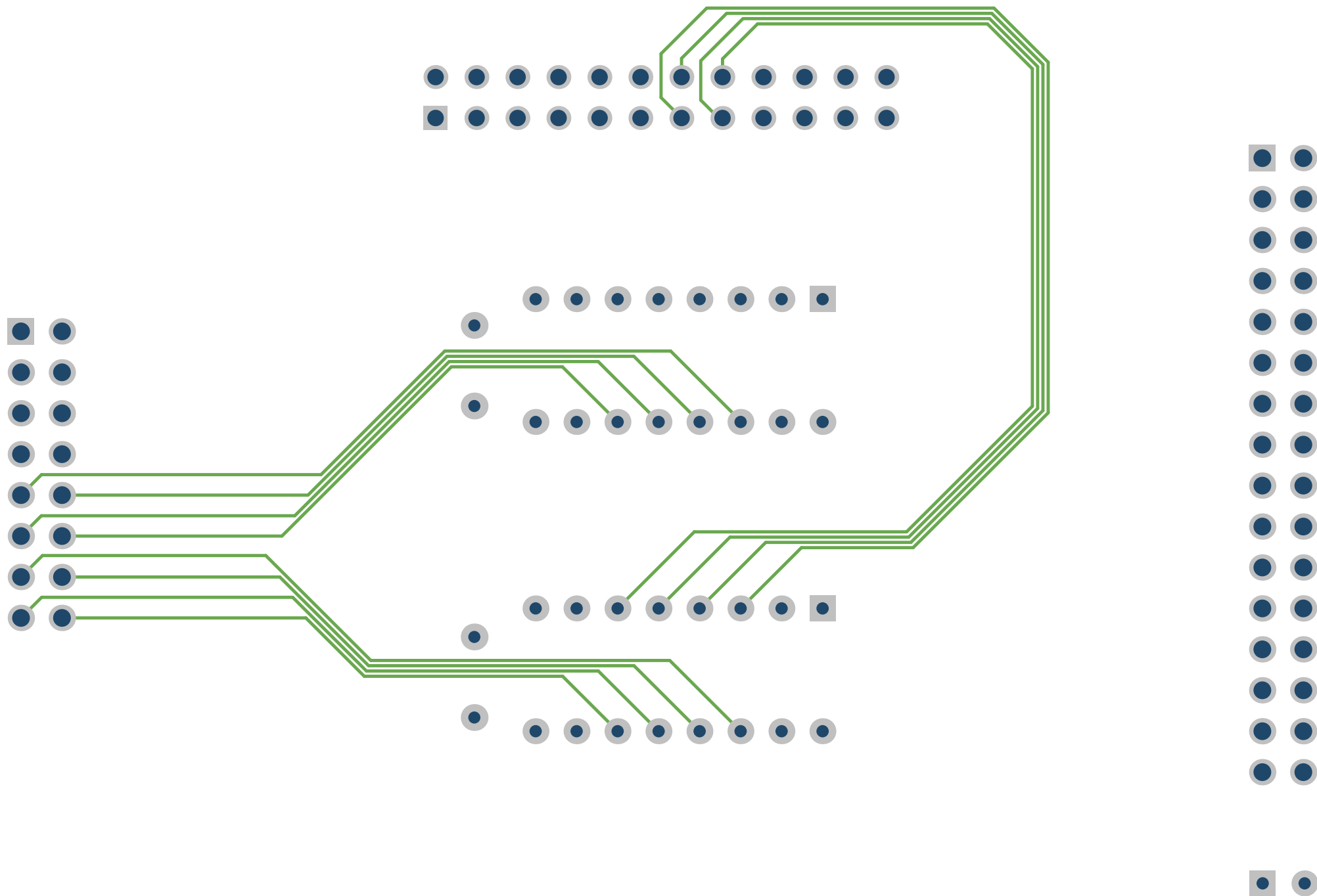
**Project:** myCPU Output Register 8 bit

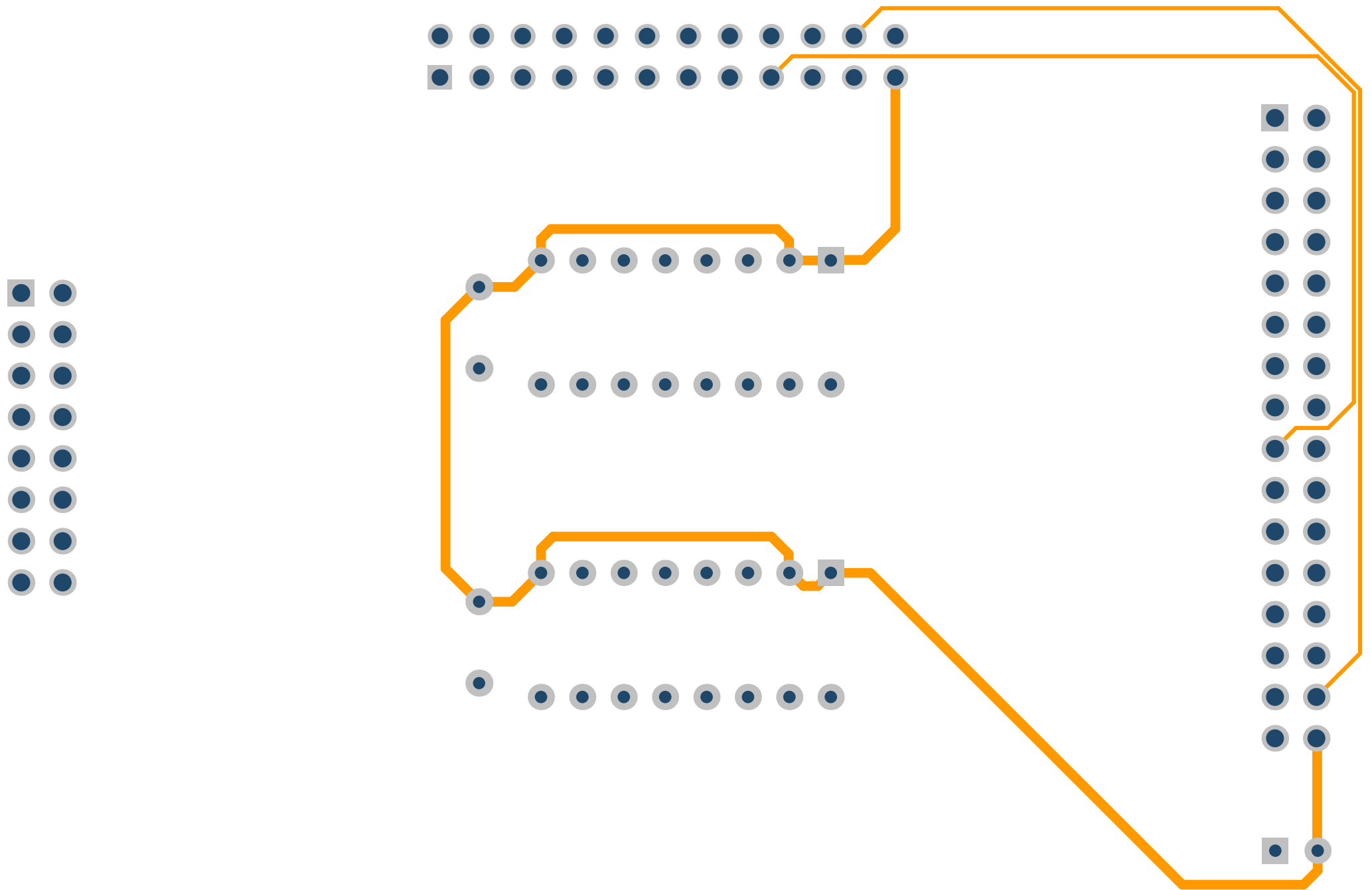
**Revision:** 6

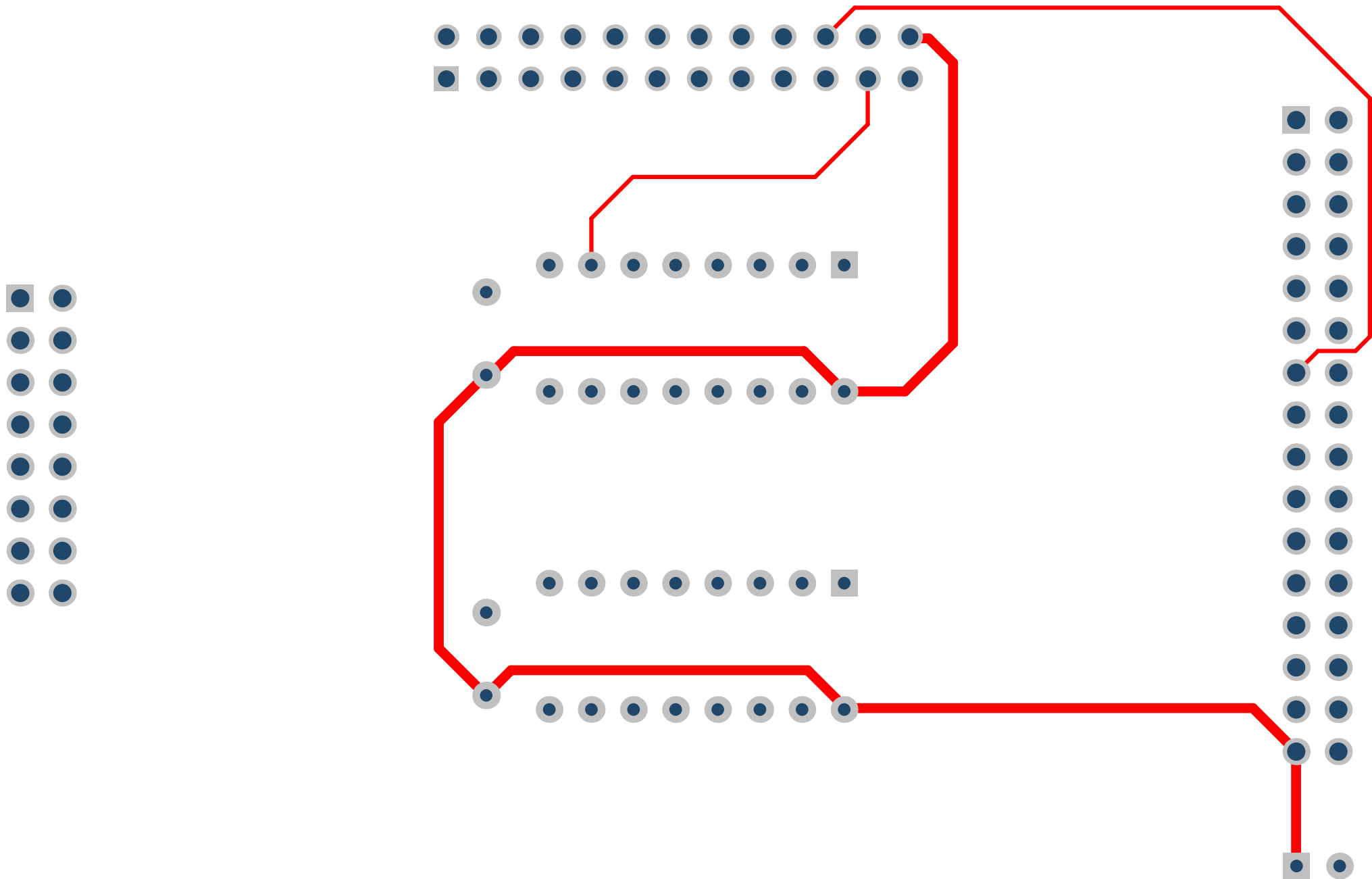
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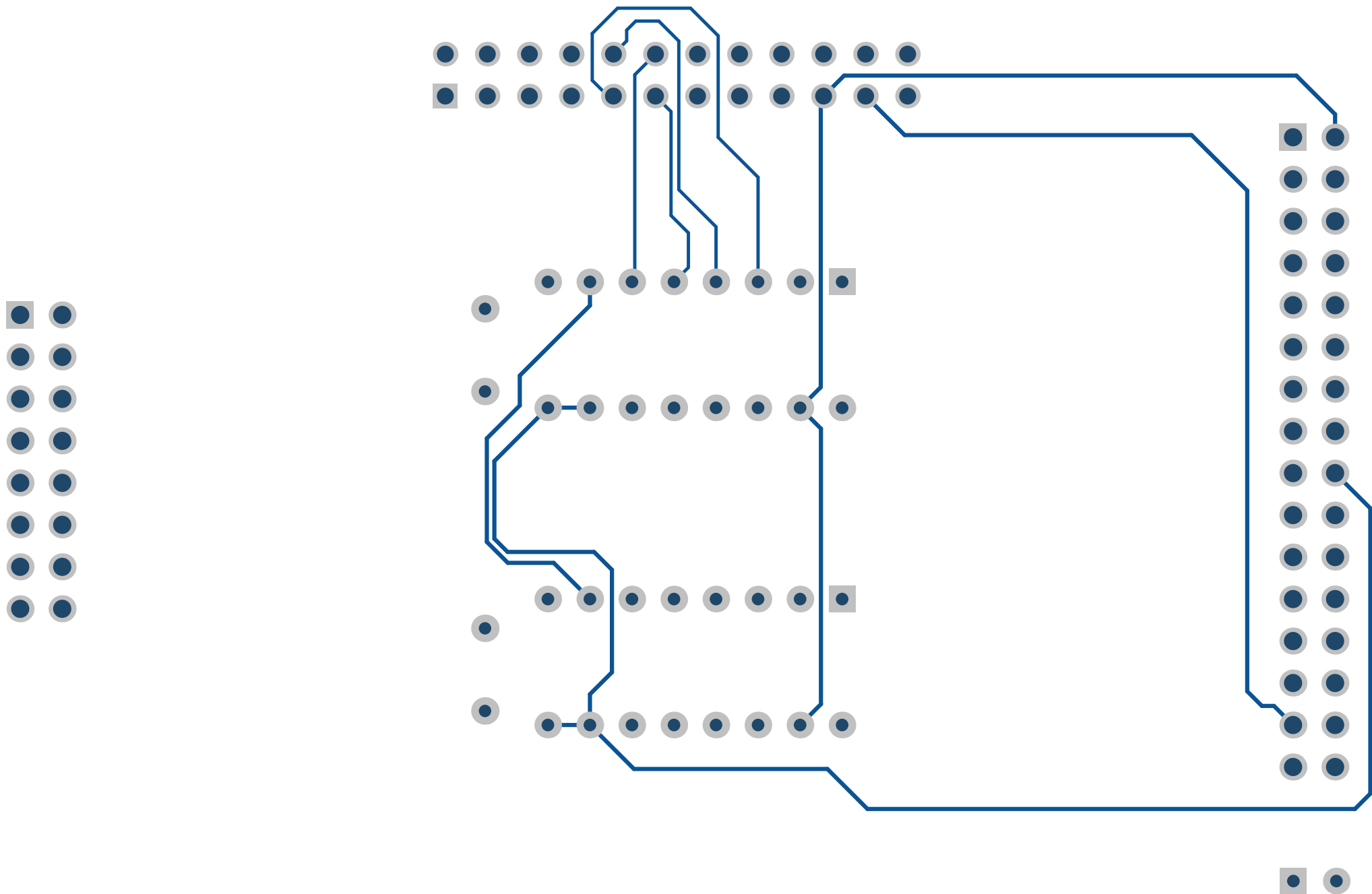
**Author:** Rafa Hernández















# Bill of Materials

## myCPU Output Register 8 bit

Description	Value	Q
Electrolytic capacitor 16v/50v	10μF	1
Ceramic capacitor	100nF	2
4-bit D-Type Register with 3 state outputs	74xx173	2
Pin Header, THT, pitch 2.54mm, Dual Row, Vertical	16p	1
Pin Header, THT, pitch 2.54mm, Dual Row, Vertical	32p	1
Socket Header, THT, pitch 2.54mm, Dual Row, Vertical	24p	1



# Assembly List

## myCPU Output Register 8 bit

Designator	Description	Value
C1	Electrolytic capacitor 16v/50v	10 $\mu$ F
C2	Ceramic capacitor	100nF
C3	Ceramic capacitor	100nF
IC1	4-bit D-Type Register with 3 state outputs	74xx173
IC2	4-bit D-Type Register with 3 state outputs	74xx173
P1	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical	16p
P2	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical	32p
P3	Socket Header, THT, pitch 2.54mm, Dual Row, Vertical	24p