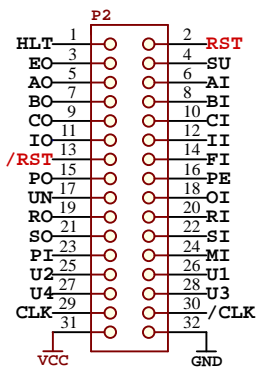
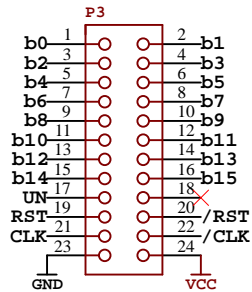


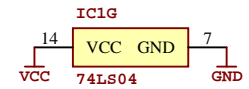
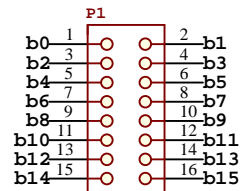
Control BUS Connector



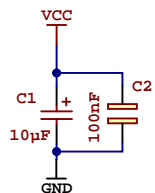
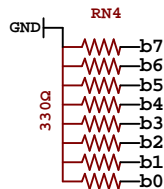
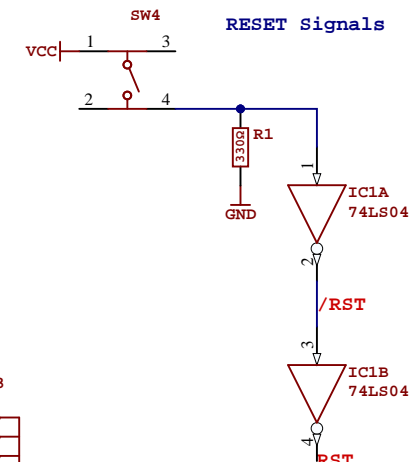
OUTPUT Connector



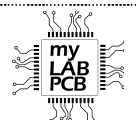
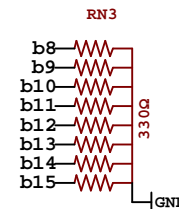
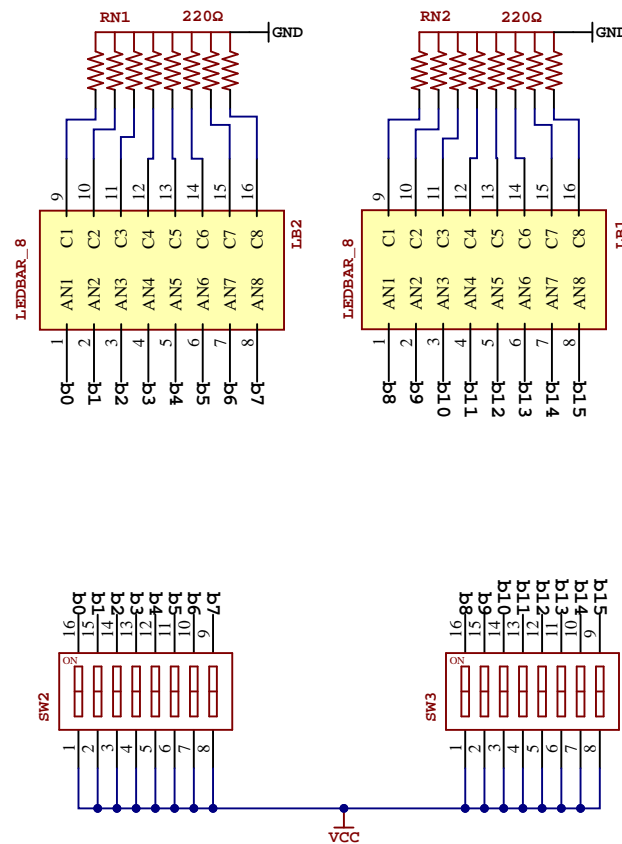
Data BUS Connector



A During Off state /RST is High



Decoupling Capacitors

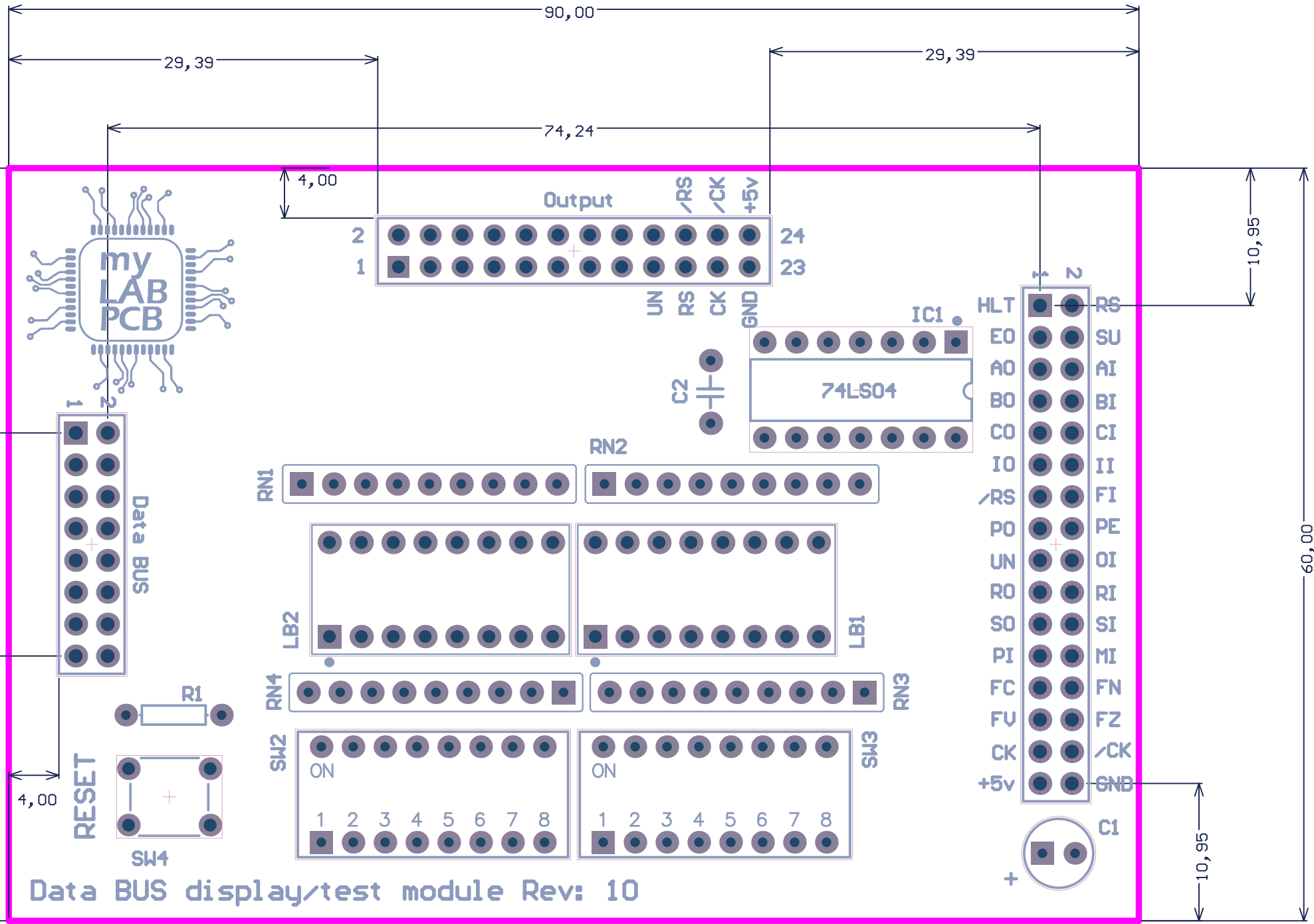


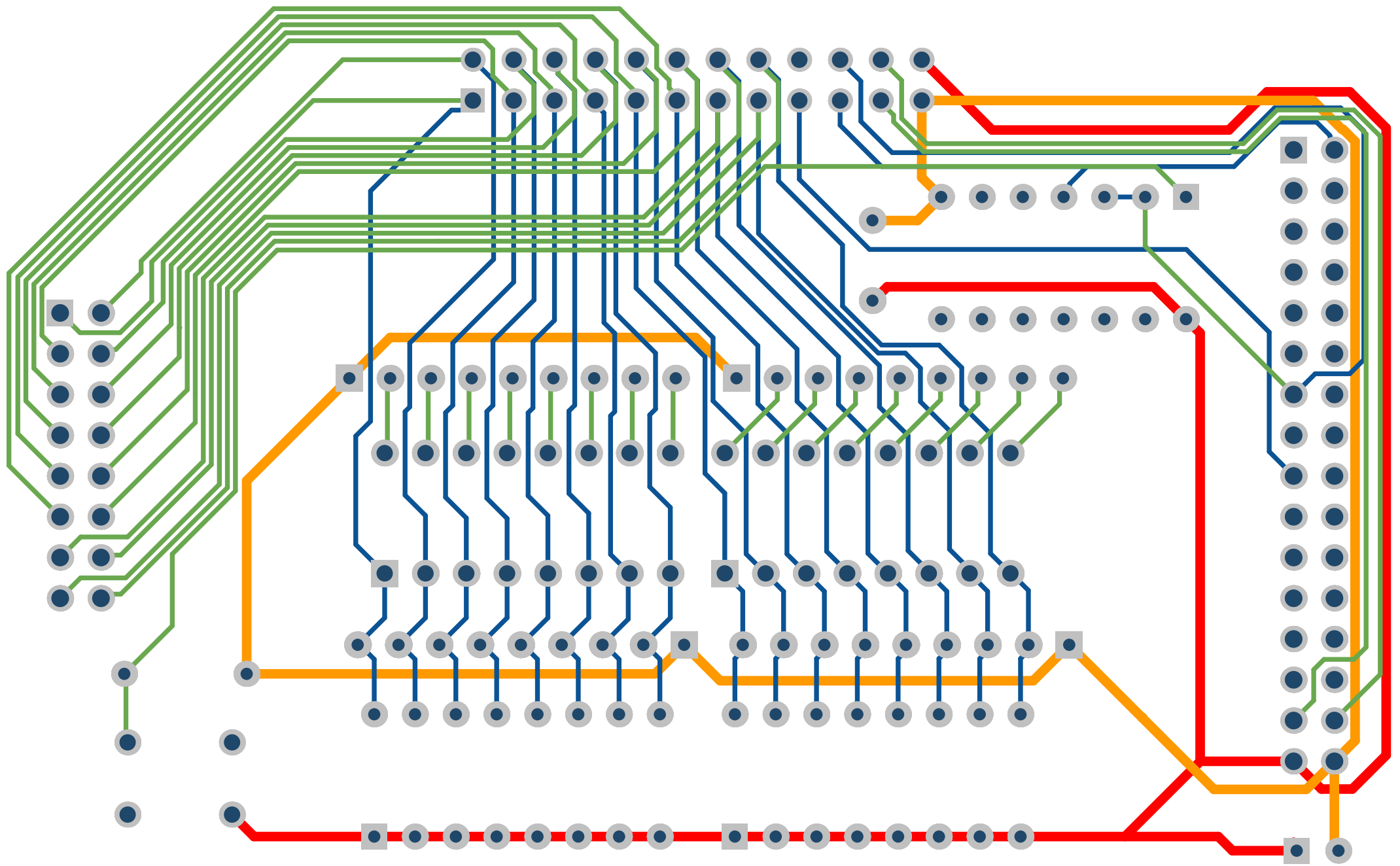
Project: myCPU BUS Test module

Revision: 10

Date: 14/10/2021

Author: Rafa Hernández







Bill of Materials

Designator	Description	Value	Q
C1	Electrolytic capacitor 16v/50v	10 μ F	1
C2	Ceramic or tantalum capacitor	100nF	1
IC1	Hex inverters	74LS04	1
LB1, LB2	Led Indicator Bar 8 segments, DIP 16		2
P1	Pin Header, pitch 2.54mm, Dual Row, Vertical	16p	1
P2	Pin Header, pitch 2.54mm, Dual Row, Vertical	32p	1
P3	Socket Header, pitch 2.54mm, Dual Row, Vertical	24p	1
R1	Resistor axial 1/4 W	330 Ω	1
RN1, RN2	Resistor array 8 elements,9 pins	220 Ω	2
RN3, RN4	Resistor array 8 elements,9 pins	330 Ω	2
SW2, SW3	DIP switch 8 positions		2
SW4	Tactile press button 6 mm		1



Assembly List

Desig.	Description	Value
C1	Electrolytic capacitor 16v/50v	10 μ F
C2	Ceramic or tantalum capacitor	100nF
IC1	Hex inverters	74LS04
LB1	Led Indicator Bar 8 segments, DIP 16	
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P1	Pin Header, pitch 2.54mm, Dual Row, Vertical	16p
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R1	Resistor axial 1/4 W	330 Ω
RN1	Resistor array 8 elements,9 pins	220 Ω
RN2	Resistor array 8 elements,9 pins	220 Ω
RN3	Resistor array 8 elements,9 pins	330 Ω
RN4	Resistor array 8 elements,9 pins	330 Ω
SW2	DIP switch 8 positions	
SW3	DIP switch 8 positions	
SW4	Tactile press button 6 mm	