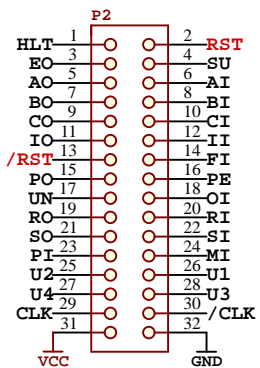
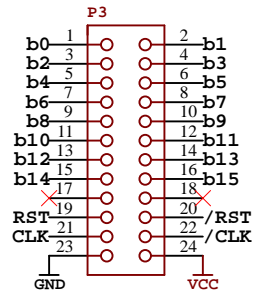




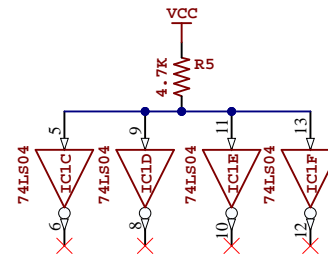
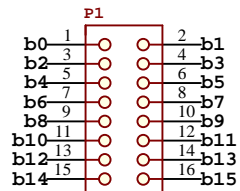
## Control BUS Connector



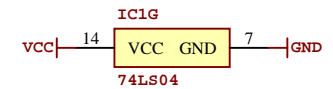
## OUTPUT Connector



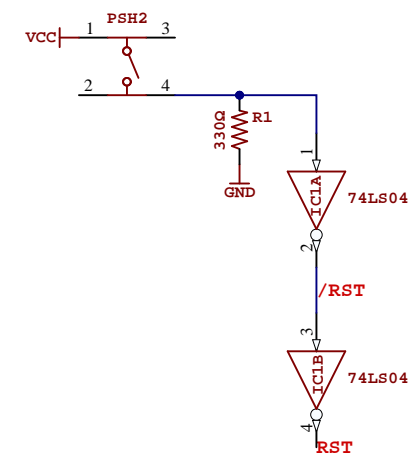
## Data BUS Connector



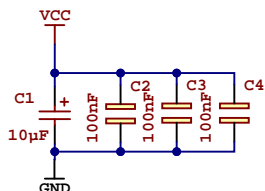
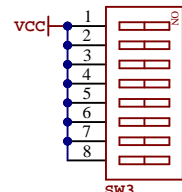
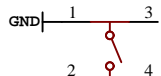
## Unused TTL Inputs



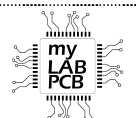
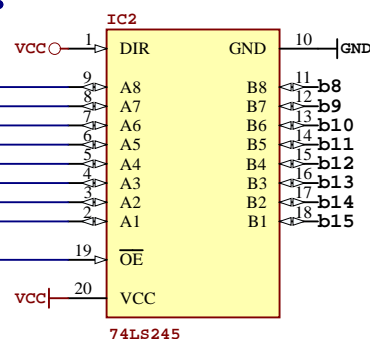
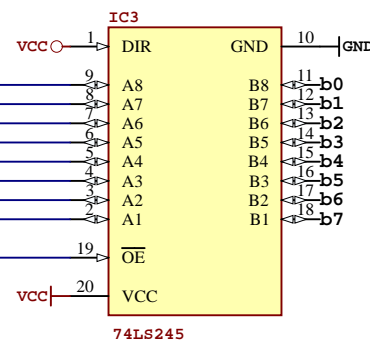
## RESET Signals



PSH1



## Decoupling Capacitors

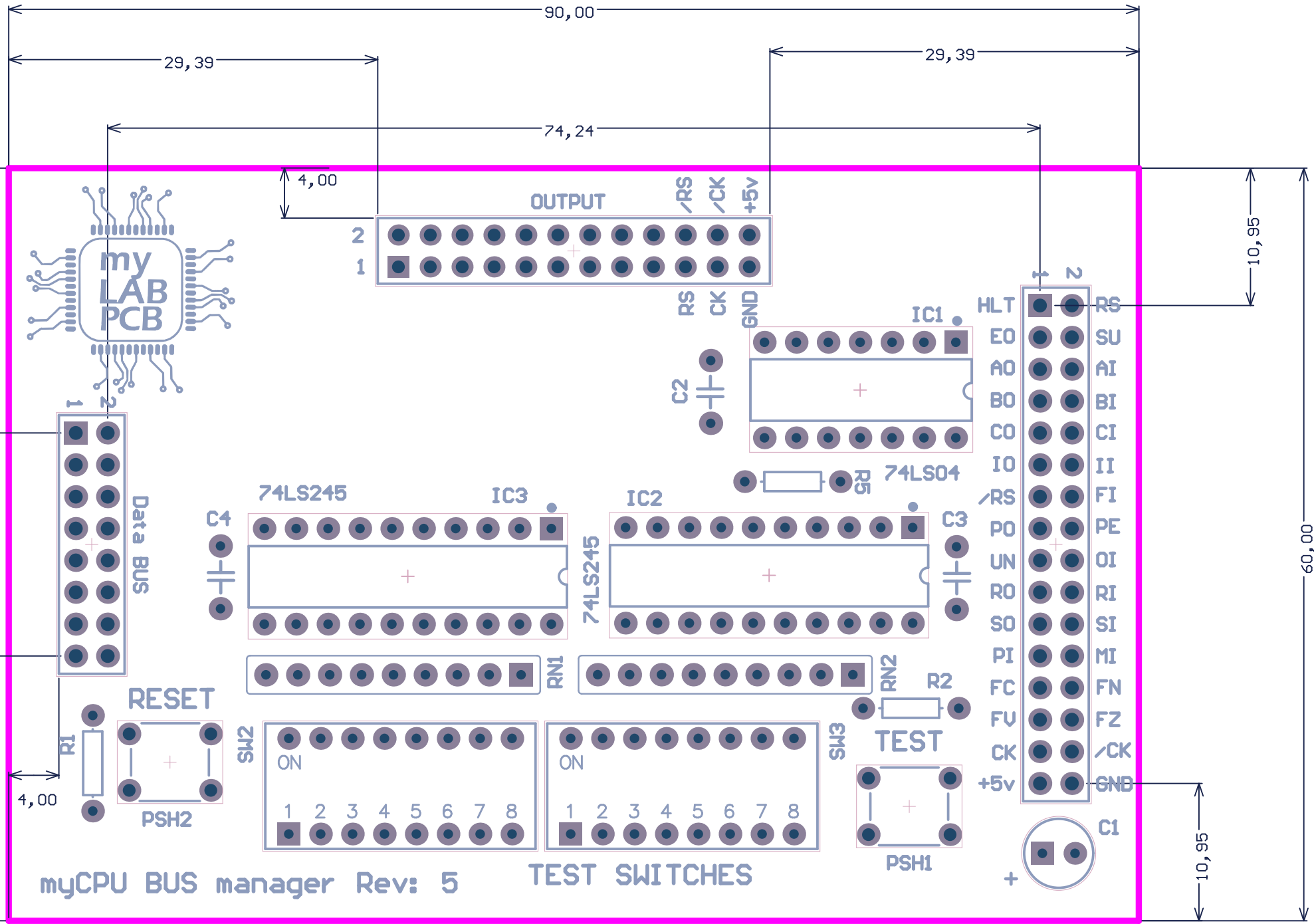


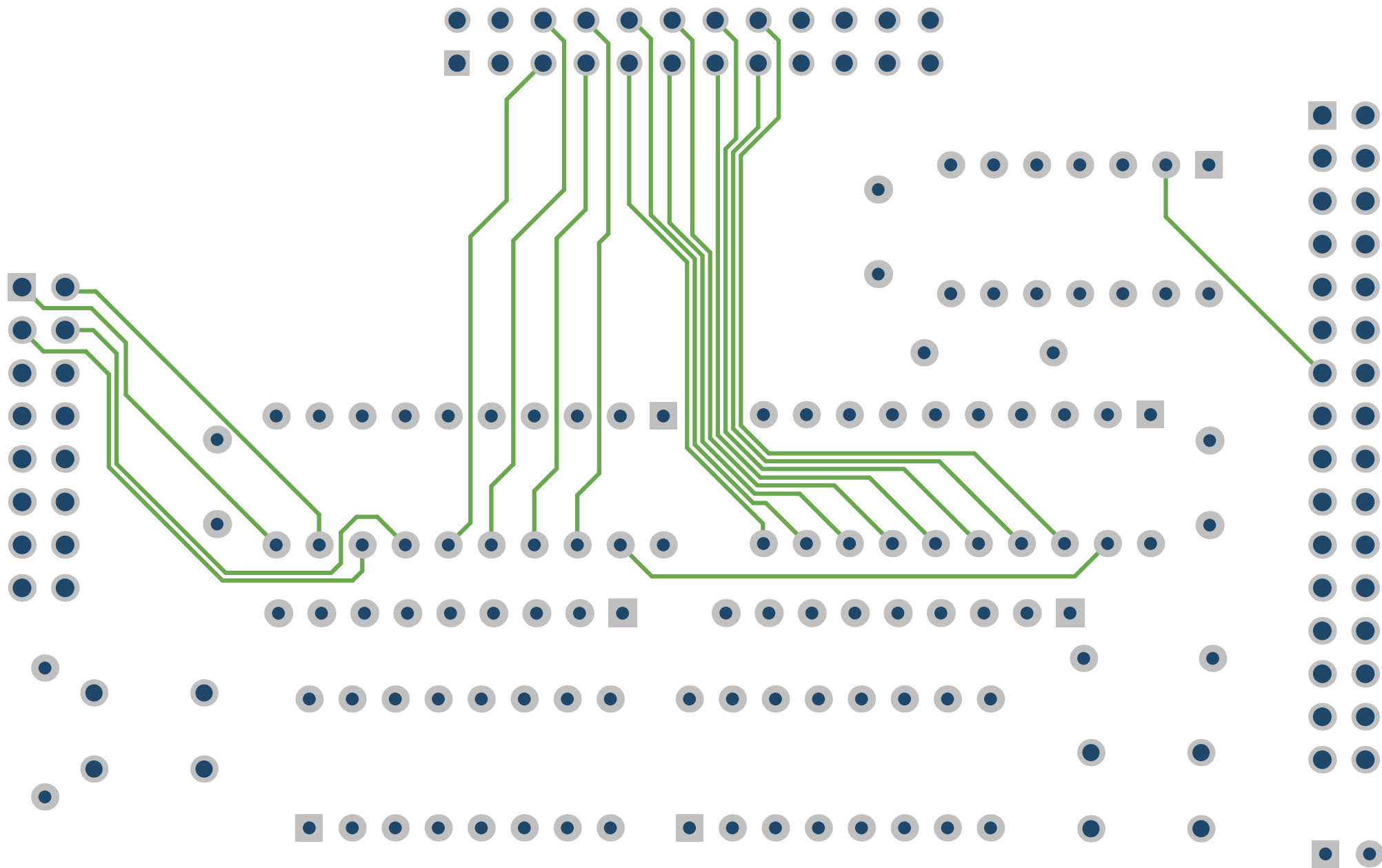
Project: myCPU BUS Manager

Revision: 5

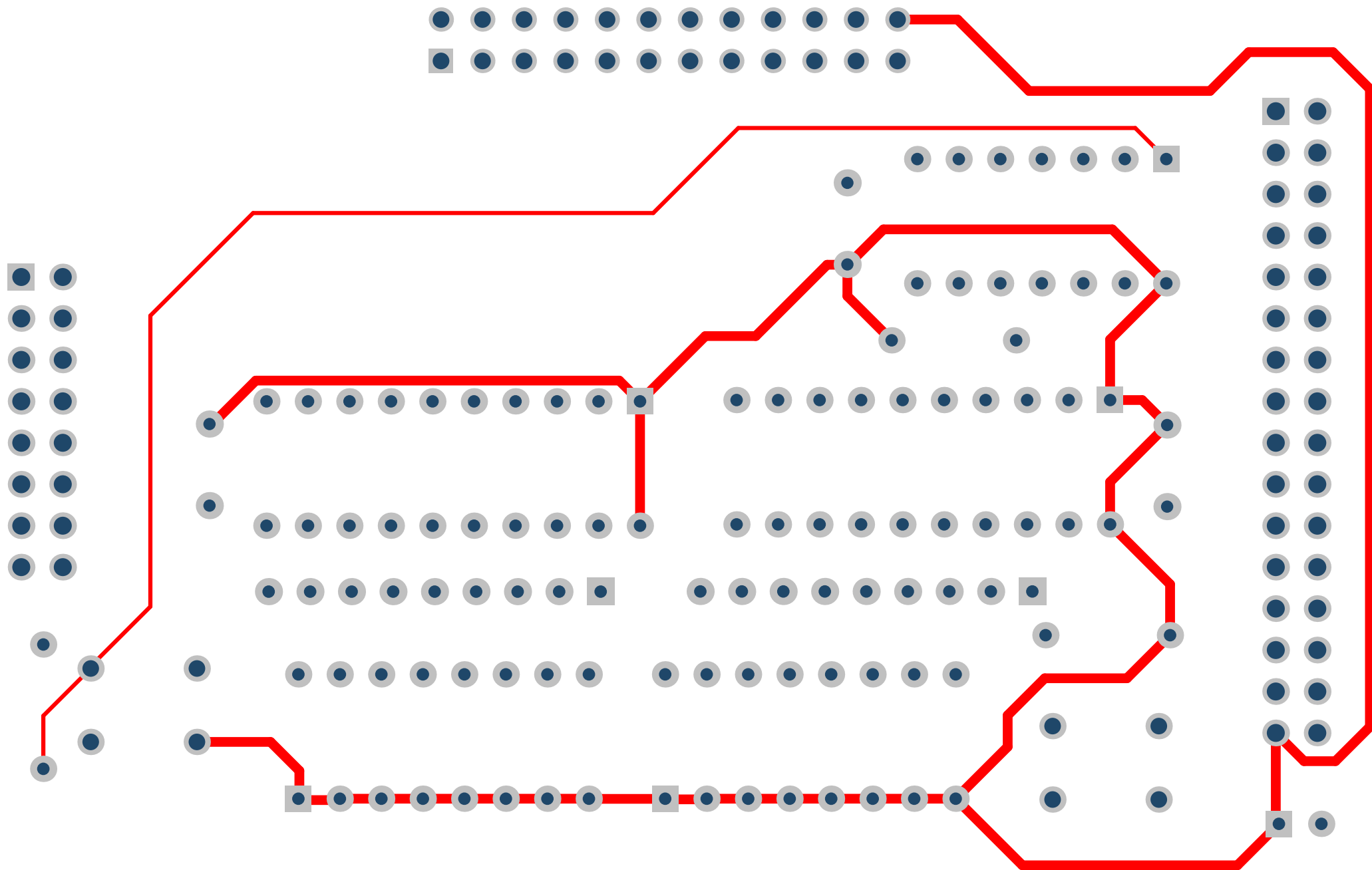
Date: 03/11/2022

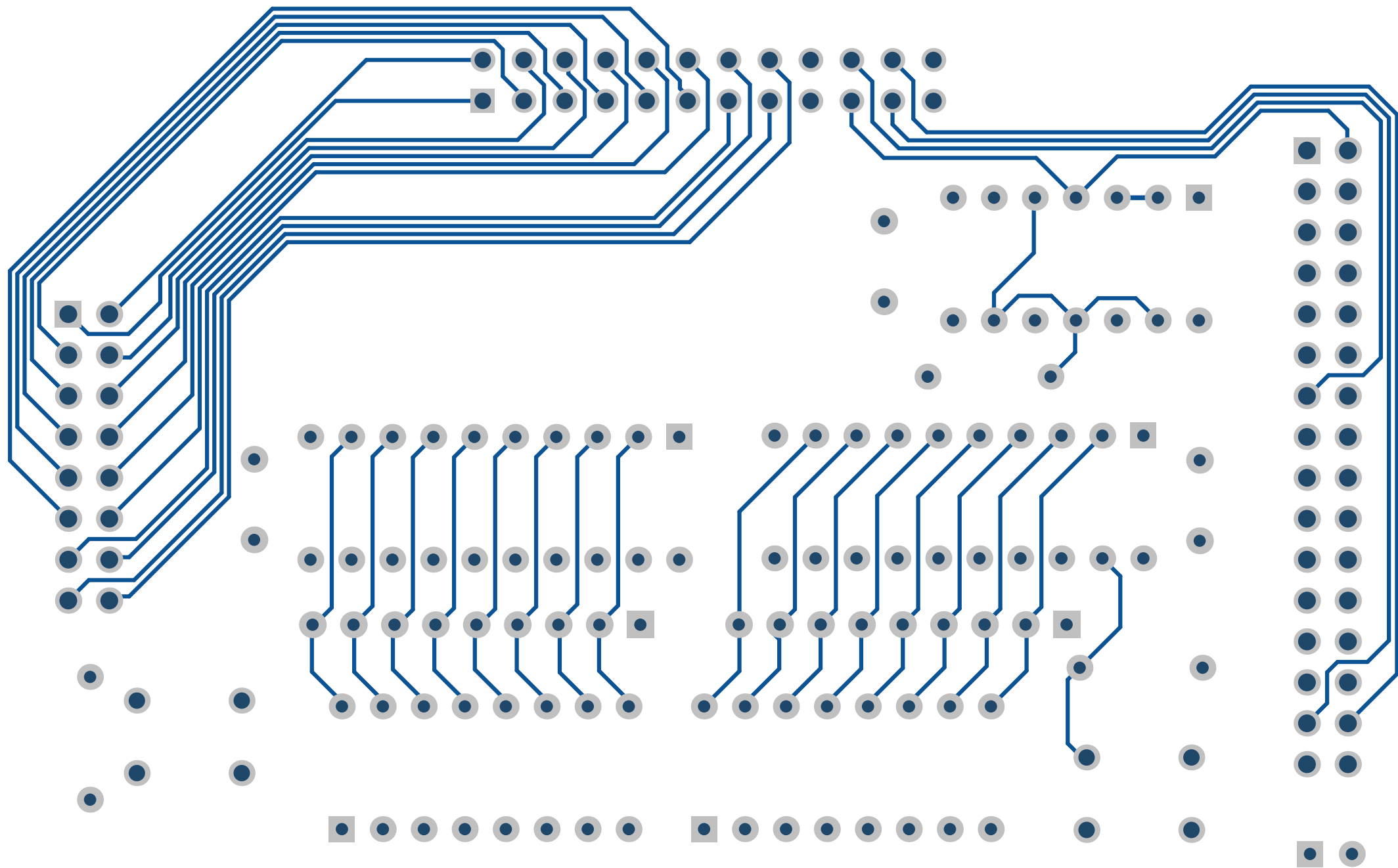
Author: Rafa Hernández















# Bill of Materials

Designator	Description	Value	Q
C1	Electrolytic capacitor 16v/50v	10μF	1
C2, C3, C4	Ceramic or tantalum capacitor	100nF	3
IC1	Hex inverters	74LS04	1
IC2, IC3	Non inverting bus transceiver	74LS245	2
P1	Pin Header, pitch 2.54mm, Dual Row, Vertical	16p	1
P2	Pin Header, pitch 2.54mm, Dual Row, Vertical	32p	1
P3	Socket Header, pitch 2.54mm, Dual Row, Vertical	24p	1
PSH1, PSH2	Tactile button 6 mm		2
R1	Resistor Axial	330Ω	1
R2	Resistor Axial	1K	1
R5	Resistor Axial	4.7K	1
RN1, RN2	Resistor array 8 elements, 9 pins	330Ω	2
SW2, SW3	DIP switch 8 positions		2



# Assembly List

Designator	Description	Value
C1	Electrolytic capacitor 16v/50v	10 $\mu$ F
C2	Ceramic or tantalum capacitor	100nF
C3	Ceramic or tantalum capacitor	100nF
C4	Ceramic or tantalum capacitor	100nF
IC1	Hex inverters	74LS04
IC2	Non inverting bus transceiver	74LS245
IC3	Non inverting bus transceiver	74LS245
P1	Pin Header, pitch 2.54mm, Dual Row, Vertical	16p
P2	Pin Header, pitch 2.54mm, Dual Row, Vertical	32p
P3	Socket Header, pitch 2.54mm, Dual Row, Vertical	24p
PSH1	Tactile button 6 mm	
PSH2	Tactile button 6 mm	
R1	Resistor Axial	330 $\Omega$
R2	Resistor Axial	1K
R5	Resistor Axial	4.7K
RN1	Resistor array 8 elements,9 pins	330 $\Omega$
RN2	Resistor array 8 elements,9 pins	330 $\Omega$
SW2	DIP switch 8 positions	
SW3	DIP switch 8 positions	