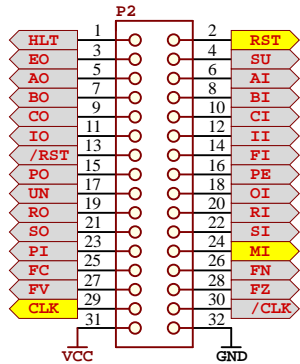
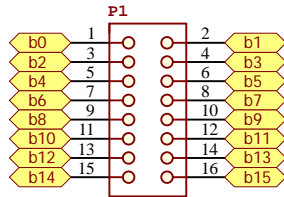


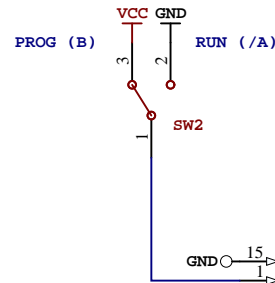
Control BUS Connector



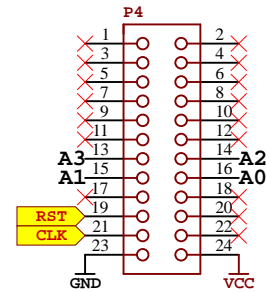
Data BUS Connector



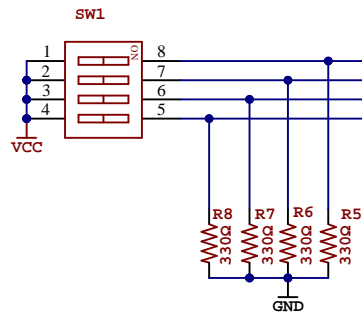
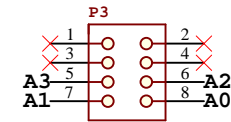
Programming Mode Switch



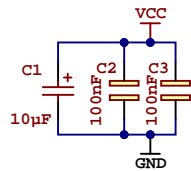
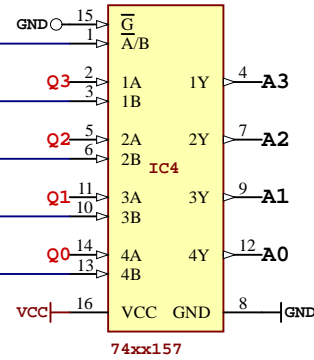
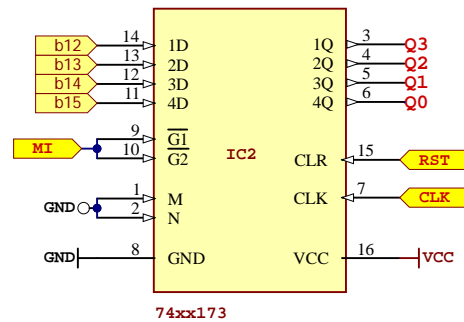
Output Connector



Address OUTPUT Connector

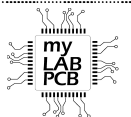


4 bits MAR version uses only less significative 4 bits of the data exposed on the data BUS.



Decoupling Capacitors

"74xx" Indicates the use LS (TTL) or HC(CMOS) ICs as your prefer for your build.

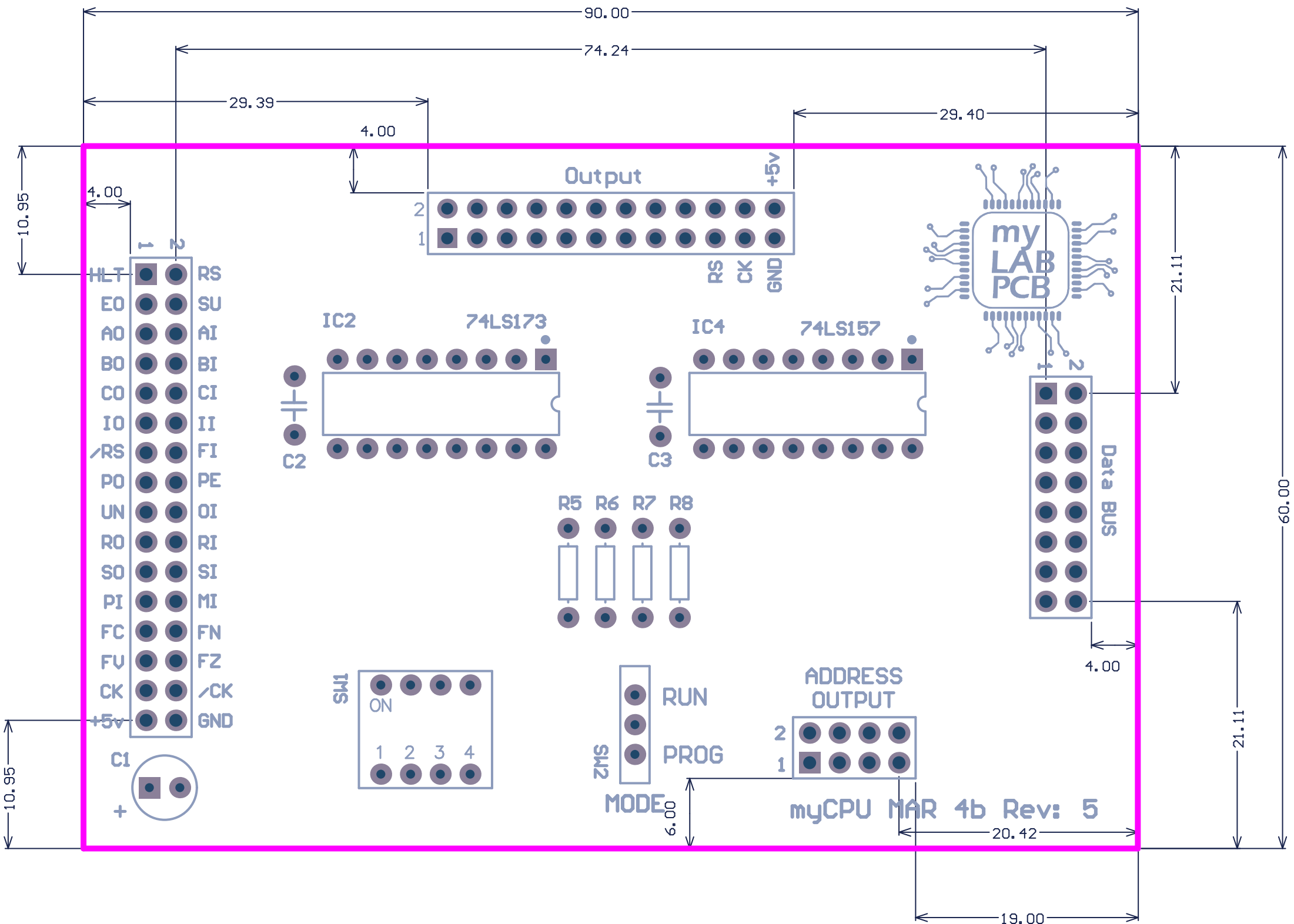


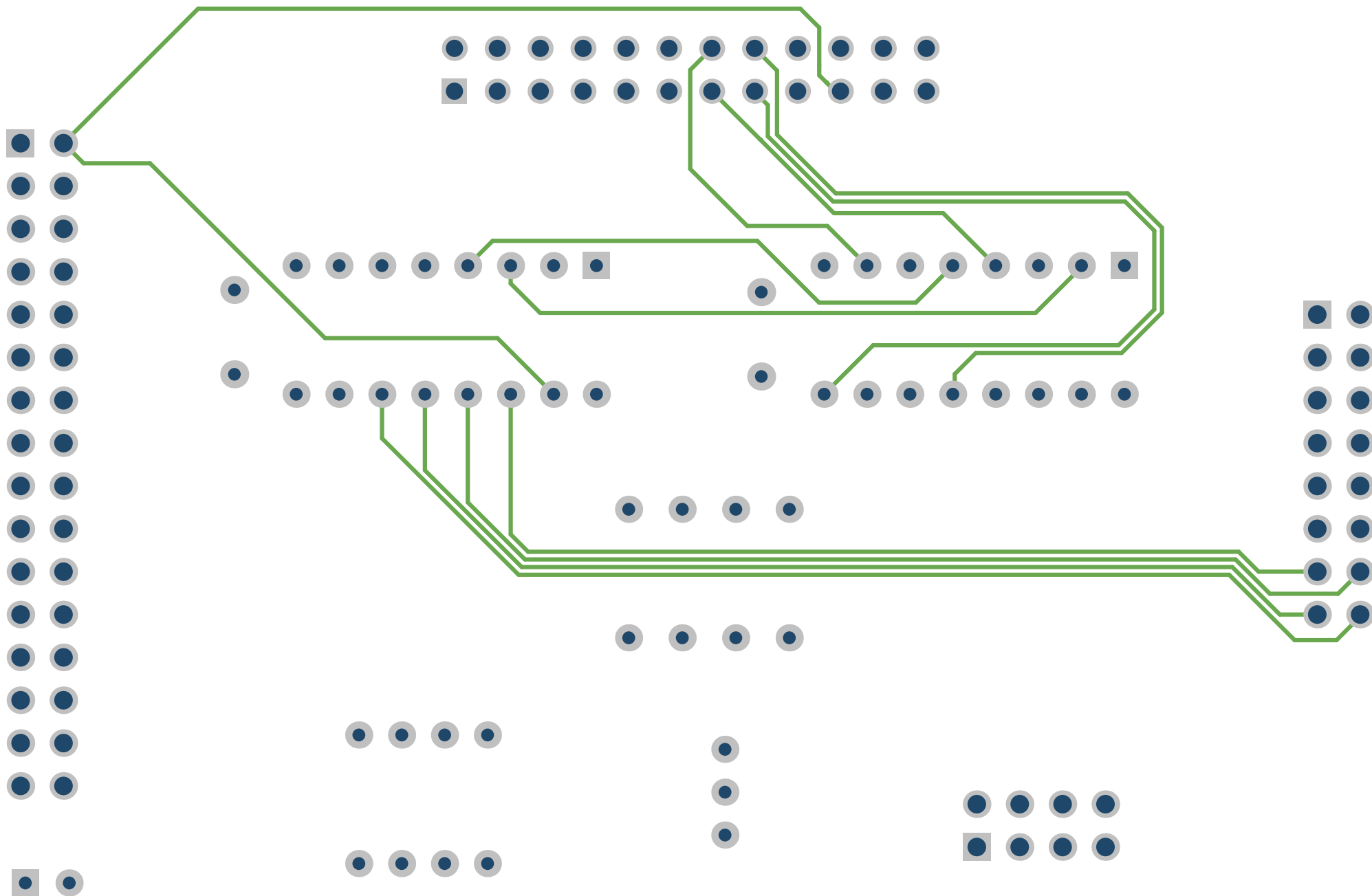
Project: myCPU MAR Module 4 bits

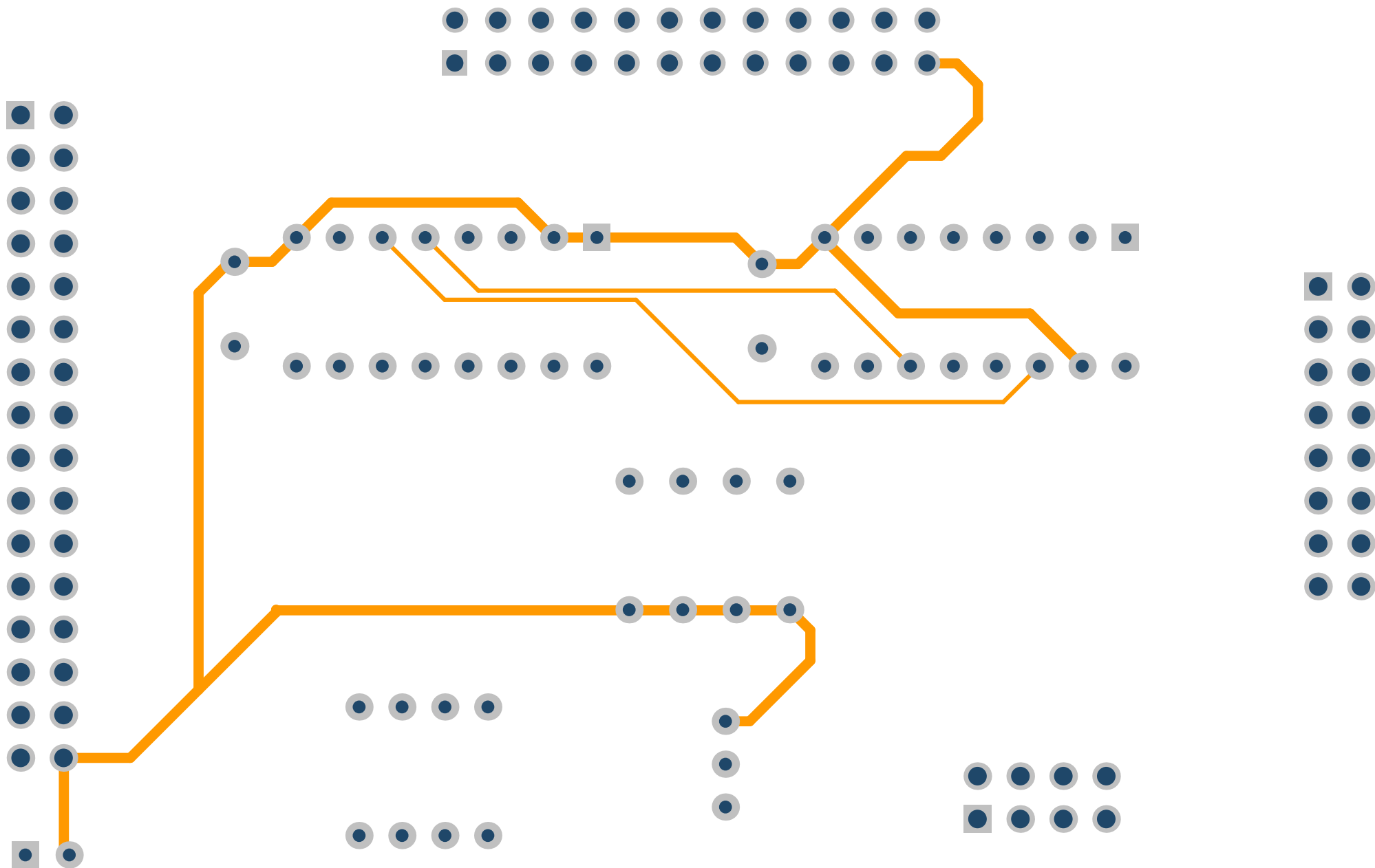
Revision: 5

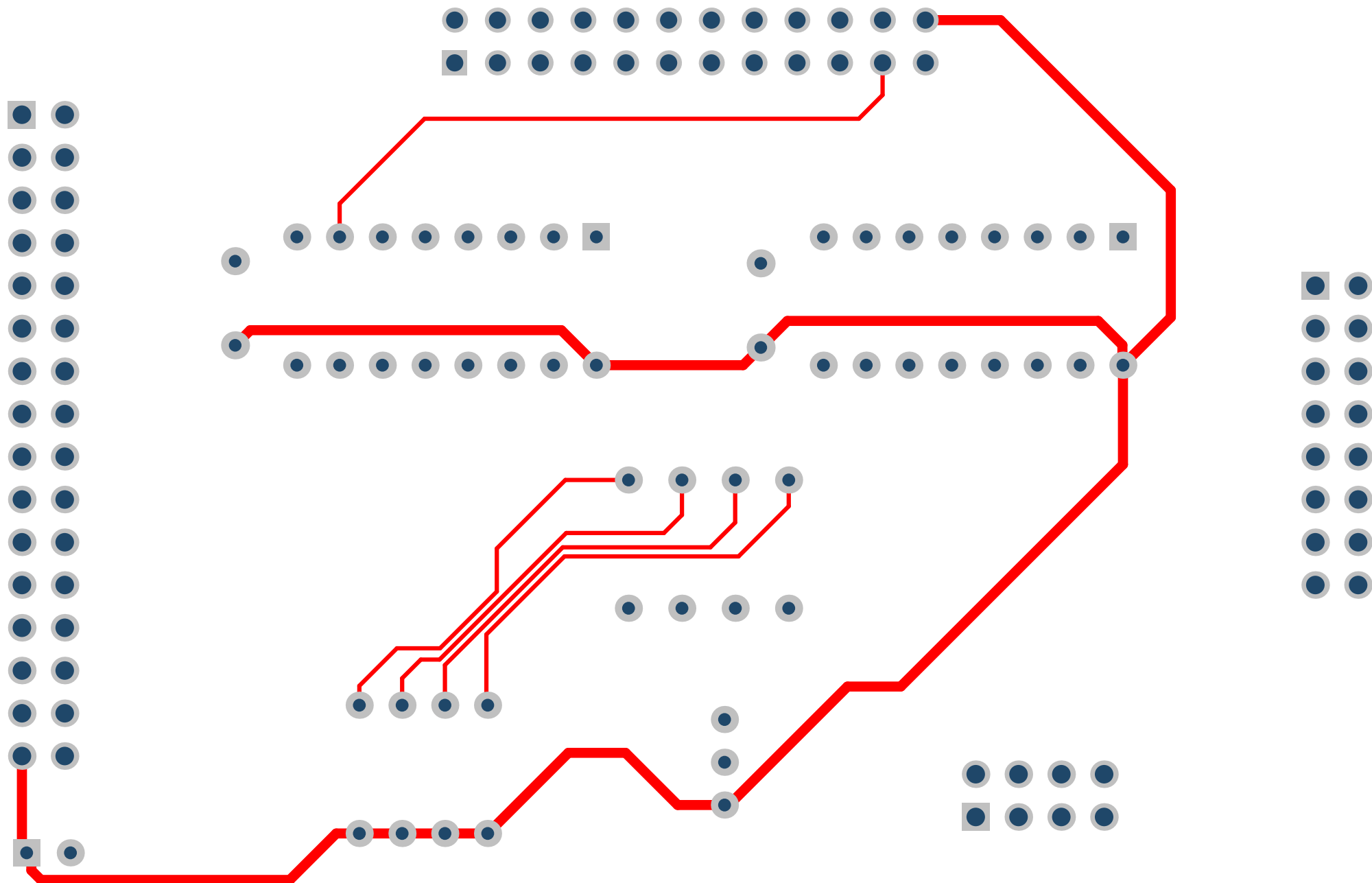
Date: 03-Jan-24

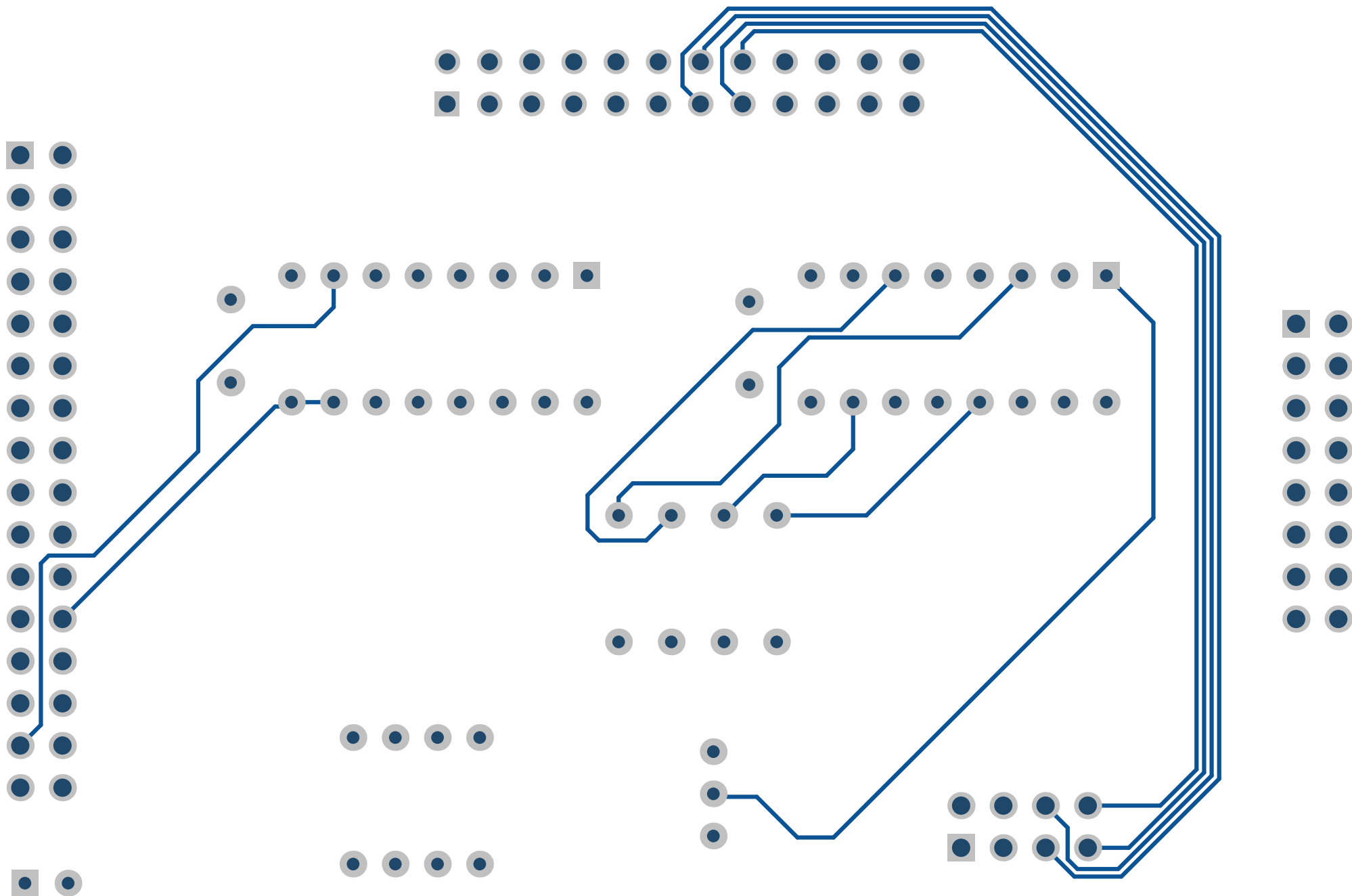
Author: Rafa Hernández













Bill of Materials

myCPU MAR 4 bits

Description	Value	Q
Electrolytic capacitor 16v/50v	10 μ F	1
Ceramic or tantalum capacitor	100nF	2
4-bit D-Type Register with 3 state outputs	74xx173	1
4-Bits 2-Line to 1 Line data selector	74xx157	1
Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 16p	16p	1
Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 32p	32p	1
Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 8p	8p	1
Socket Header, THT, pitch 2.54mm, Dual Row, Vertical, 24p	24p	1
Resistor Axial	330 Ω	4
DIP switch 4 positions	4 pos	1
Mini slide switch 2 pos, 3 pins		1



Assembly List

myCPU MAR 4 bits

Designator	Description	Value
C1	Electrolytic capacitor 16v/50v	10 μ F
C2	Ceramic or tantalum capacitor	100nF
C3	Ceramic or tantalum capacitor	100nF
IC2	4-bit D-Type Register with 3 state outputs	74xx173
IC4	4-Bits 2-Line to 1 Line data selector	74xx157
P1	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 16p	16p
P2	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 32p	32p
P3	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 8p	8p
P4	Socket Header, THT, pitch 2.54mm, Dual Row, Vertical, 24p	24p
R5	Resistor Axial	330 Ω
R6	Resistor Axial	330 Ω
R7	Resistor Axial	330 Ω
R8	Resistor Axial	330 Ω
SW1	DIP switch 4 positions	4 pos
SW2	Mini slide switch 2 pos, 3 pins	