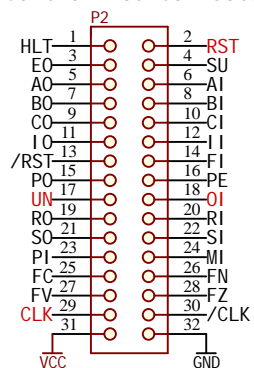
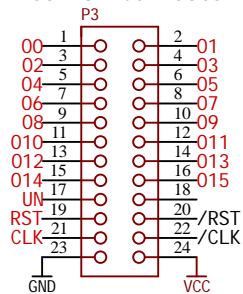


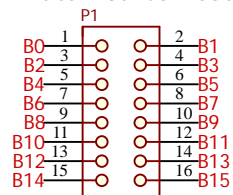
Control BUS Connector



OUTPUT Connector

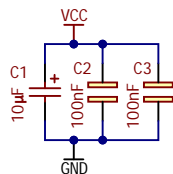


Data BUS Connector

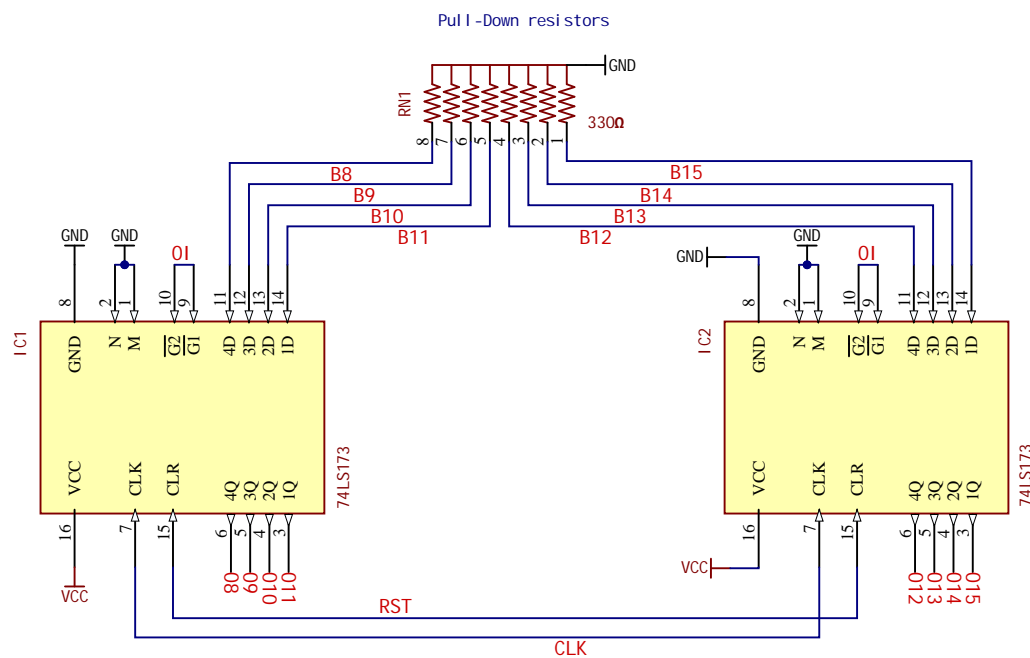


G1, G2 input control. Set both to LOW to load all bit on inputs in flip-flops

M,N output control. Set all to LOW allow driving all outputs to 245.



Decoupling Capacitors



Title		
Output Register 8 Bits (RIGHT)		
Size	Number	Revision
A4		4
Date:	12/21/2021	Sheet of 8
File:	E:\OneDrive - UNED\...\OutputRegister 8 Bits (RIGHT)	By: S. Schob

