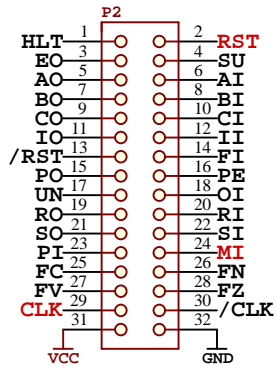
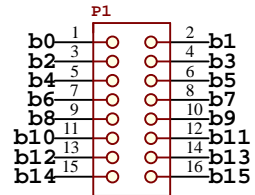


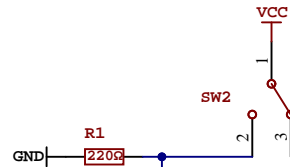
Control BUS Connector



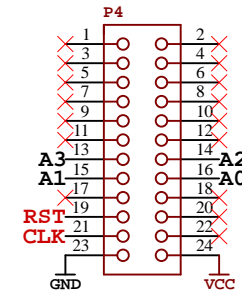
Data BUS Connector



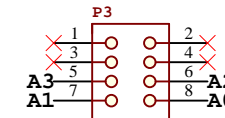
Programming Mode Switch



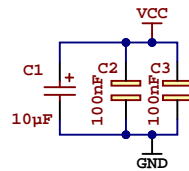
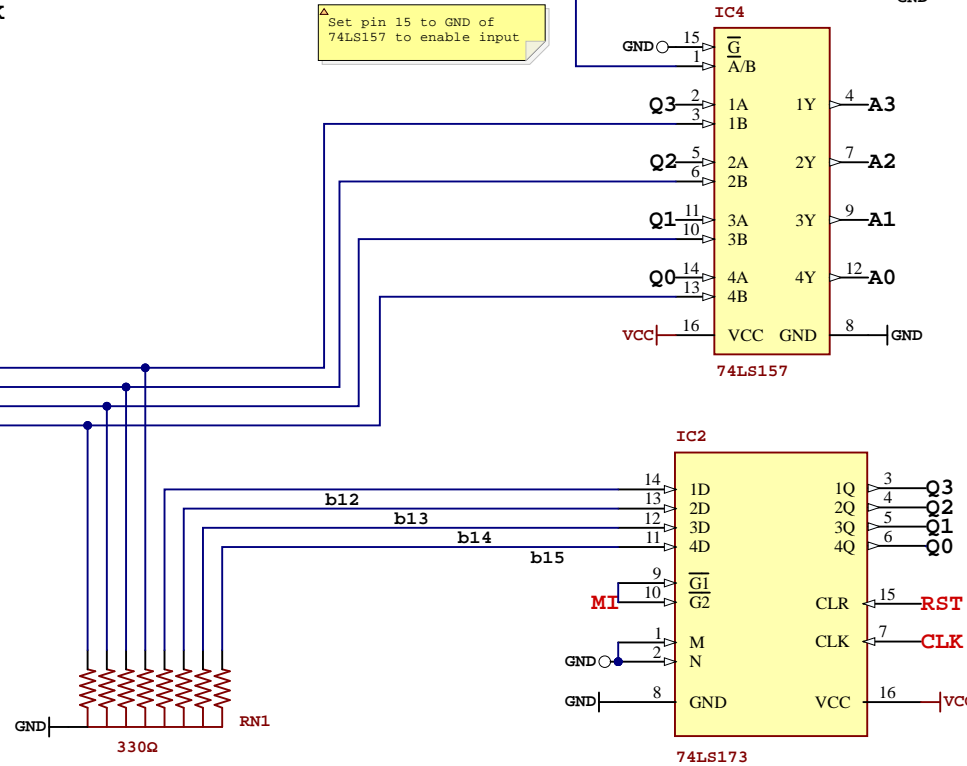
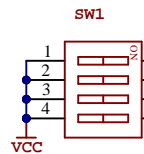
Output Connector



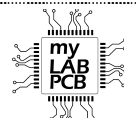
Address OUTPUT Connector



A 4 bits MAR version uses only less significative 4 bits of the data exposed on the data BUS.



Decoupling Capacitors

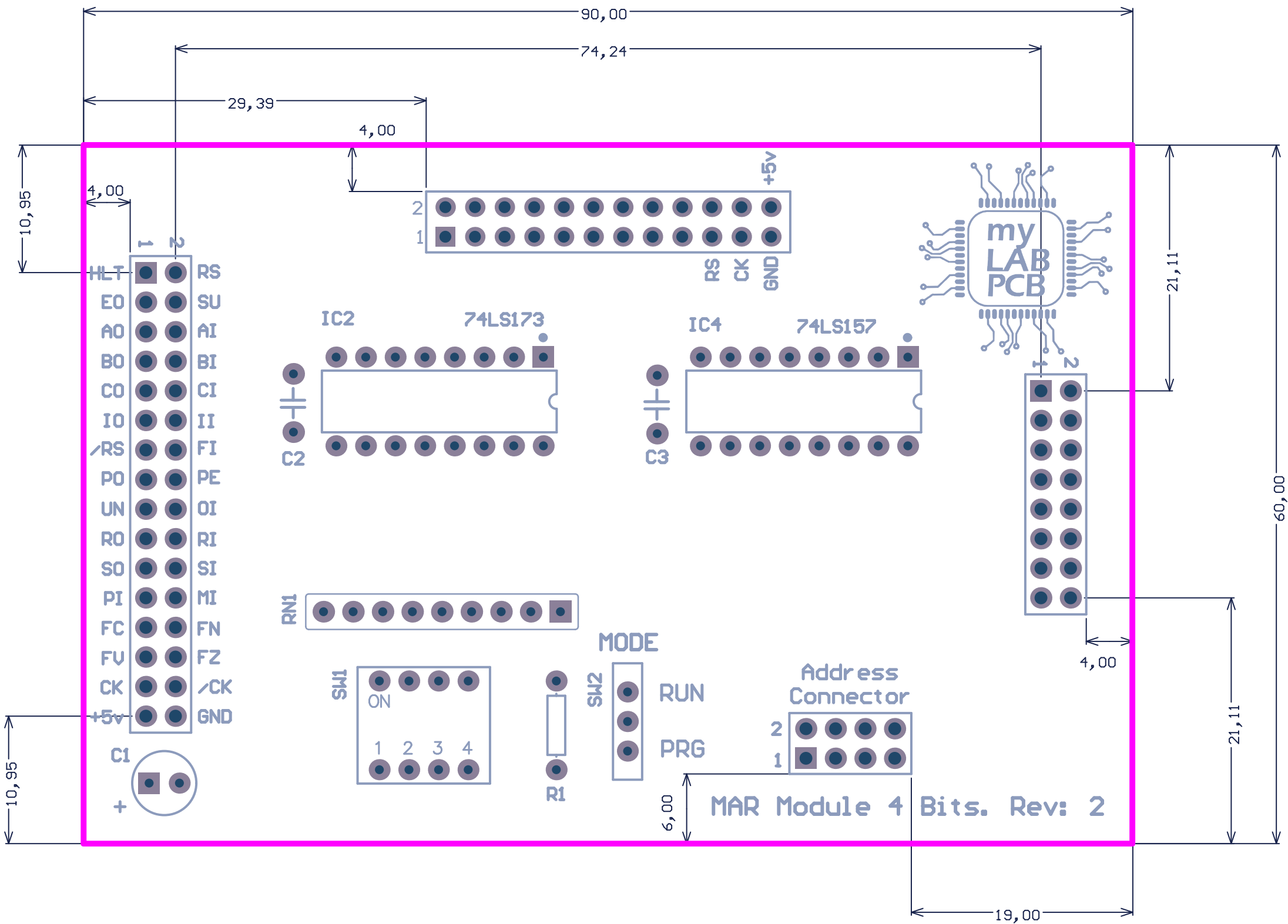


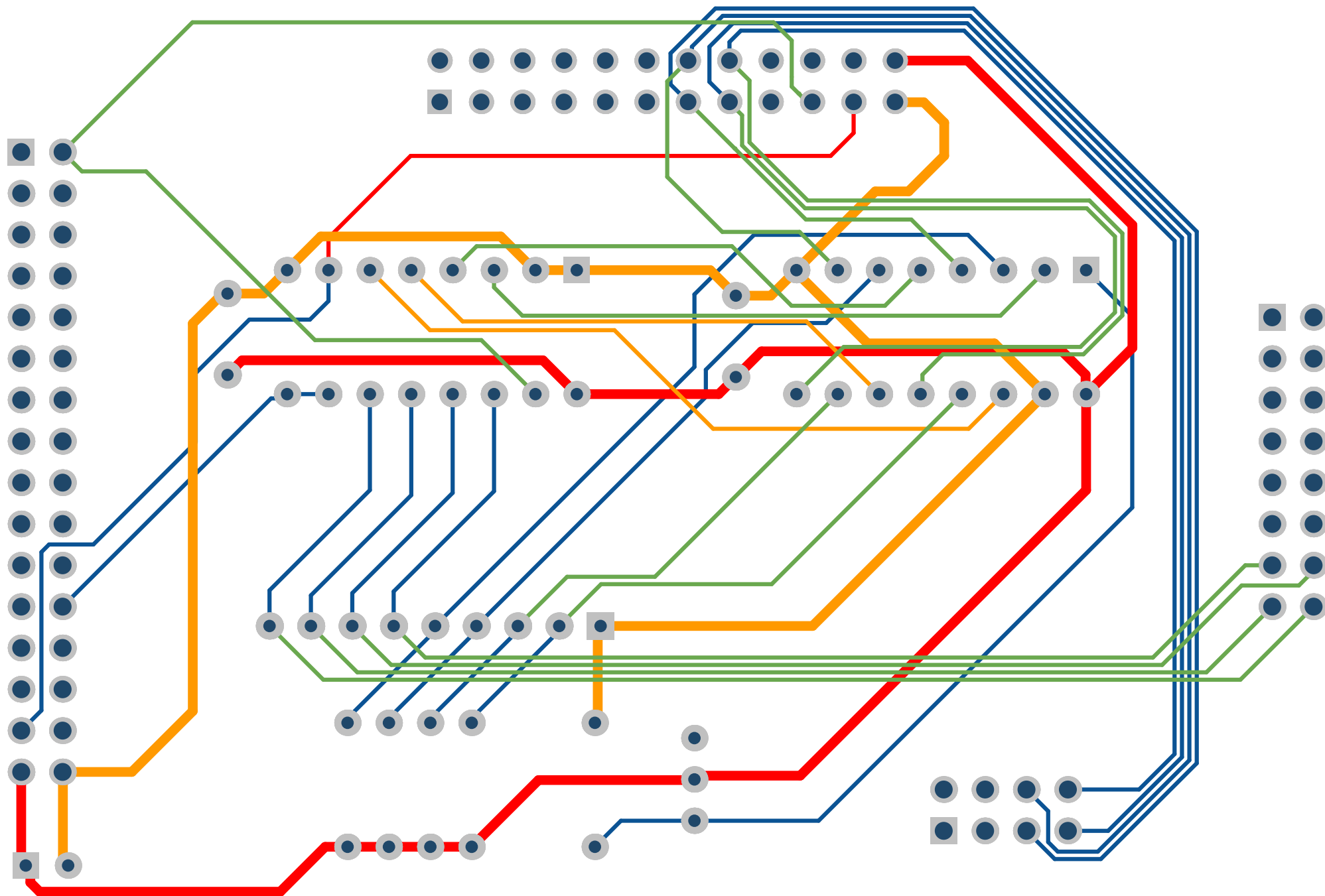
Project: myCPU MAR Module 4 bits

Revision: 2

Date: 18/10/2021

Author: Rafa Hernández







Bill of Materials

Designator	Description	Value	Q
C1	Electrolytic capacitor 16v/50v	10 μ F	1
C2, C3	Ceramic or tantalum capacitor	100nF	2
IC2	4-bit D-Type Register with 3 state outputs	74LS173	1
IC4	4-Bits 2-Line to 1 Line data selector	74LS157	1
P1	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 16p	16p	1
P2	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 32p	32p	1
P3	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 8p	8p	1
P4	Socket Header, THT, pitch 2.54mm, Dual Row, Vertical, 24p	24p	1
R1	Resistor axial	220 Ω	1
RN1	Resistor array 8 elements,9 pins	330 Ω	1
SW1	DIP switch 4 positions	4 pos	1
SW2	Mini slide switch 2 pos, 3 pins		1



Assembly List

Desig.	Description	Value
C1	Electrolytic capacitor 16v/50v	10 μ F
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C3	Ceramic or tantalum capacitor	100nF
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