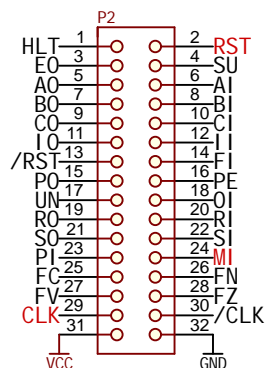
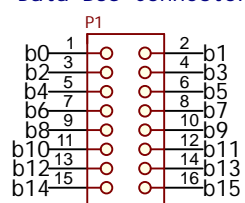




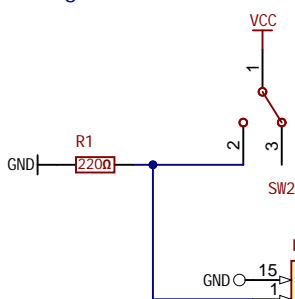
## Control BUS Connector



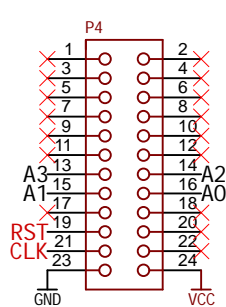
## Data BUS Connector



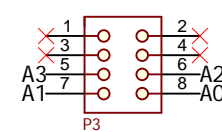
## Program Mode Switch



## Output Connector

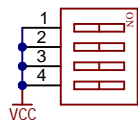


## Address OUTPUT Connector

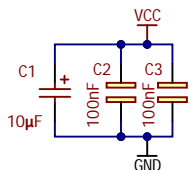


4 bits MAR version uses only less significant word of the 8bits data exposed on the data BUS.

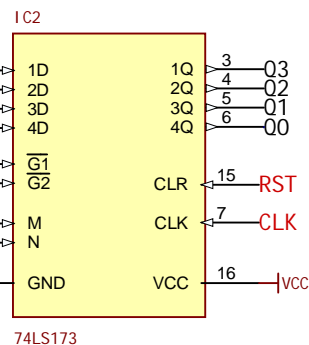
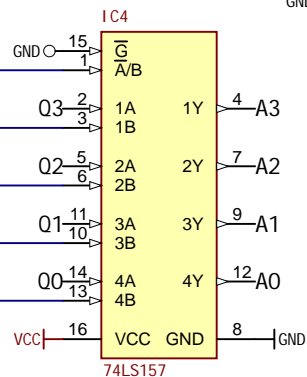
Set pin 15 to GND of 74LS157 enable input



SW1



Decoupling Capacitors



Title MAR Module (4 Bits)		
Size A4	Number	Revision 2
Date: 12/21/2021	Sheet of	
File: E:\OneDrive - UNED\..MAR_4b.SchDoc	Drawn By:	

