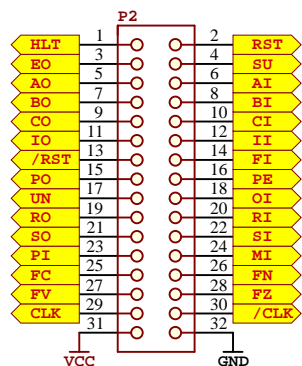
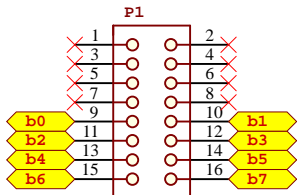


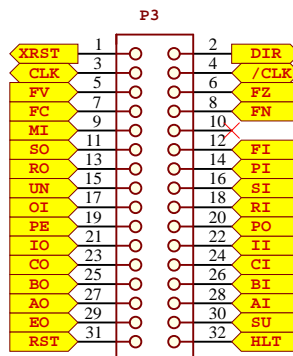
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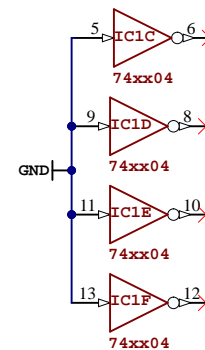
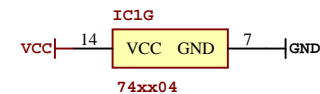
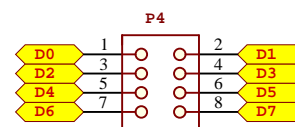
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3

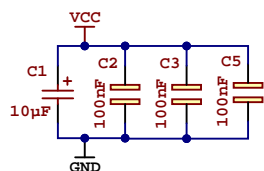
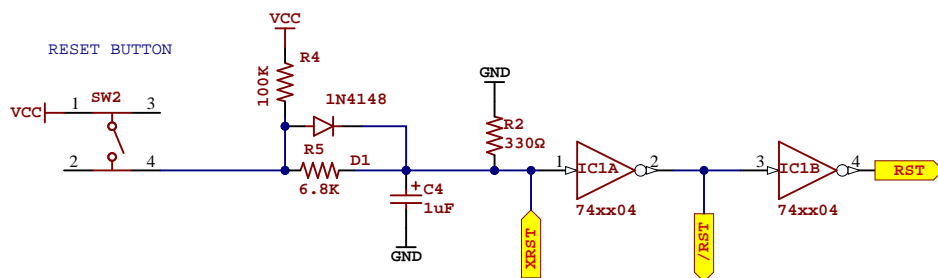


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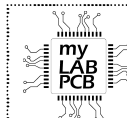
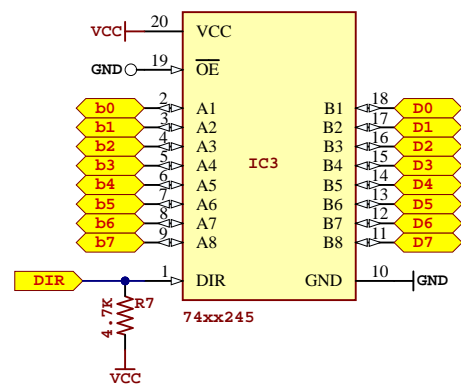


Default direction is OUTPUT from A to B, to avoid conflicts when no controller connected.

B

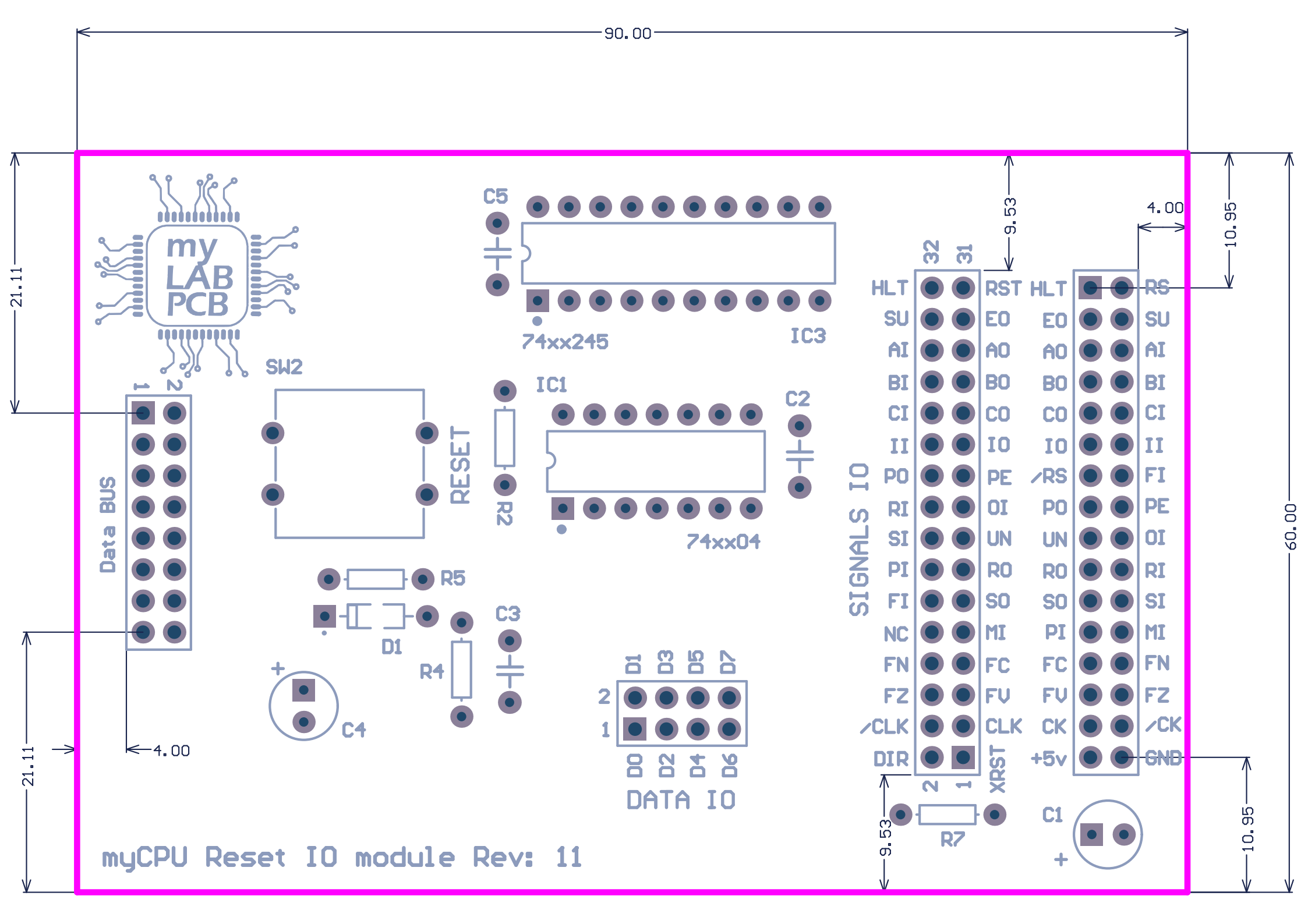


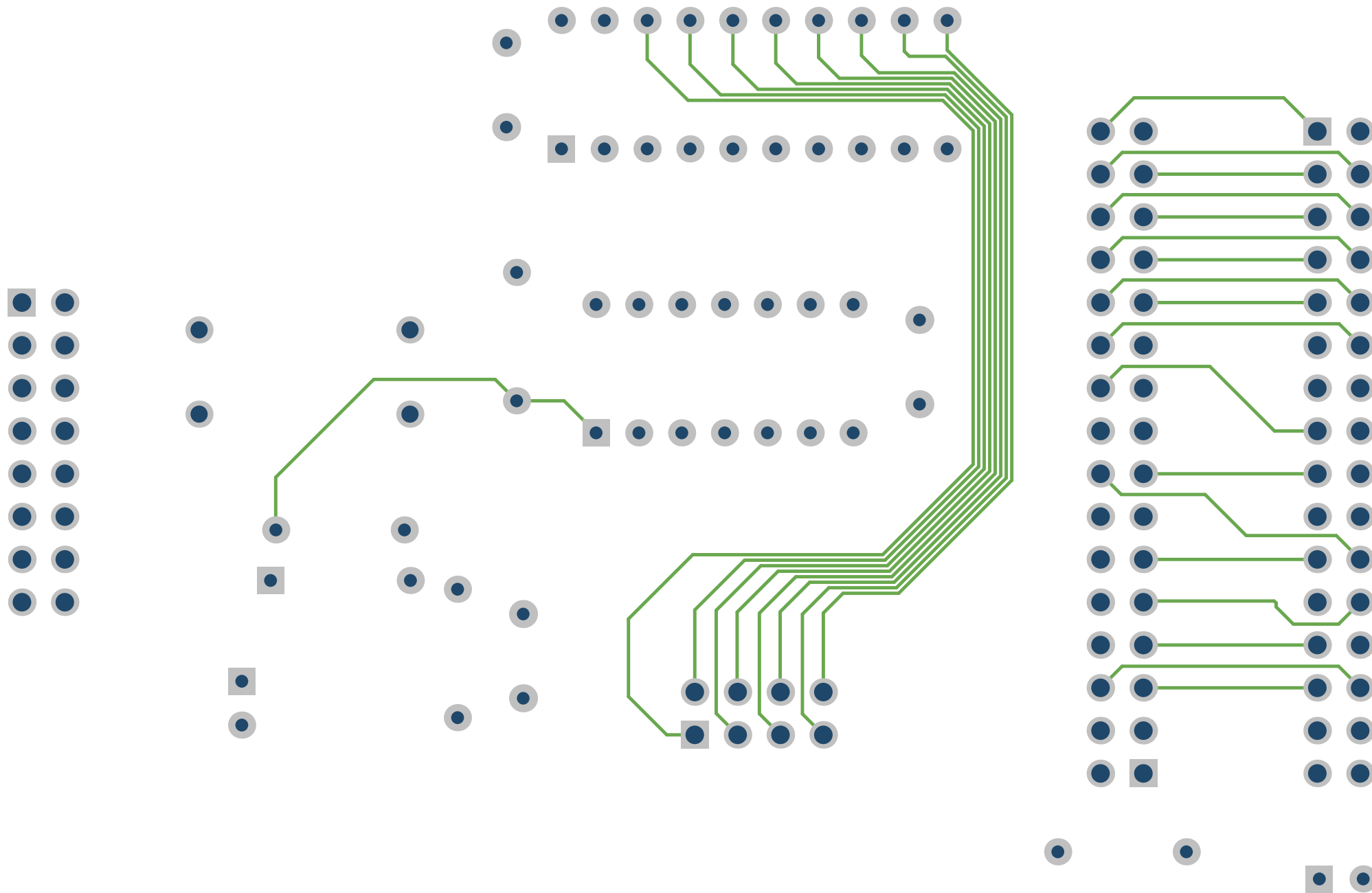
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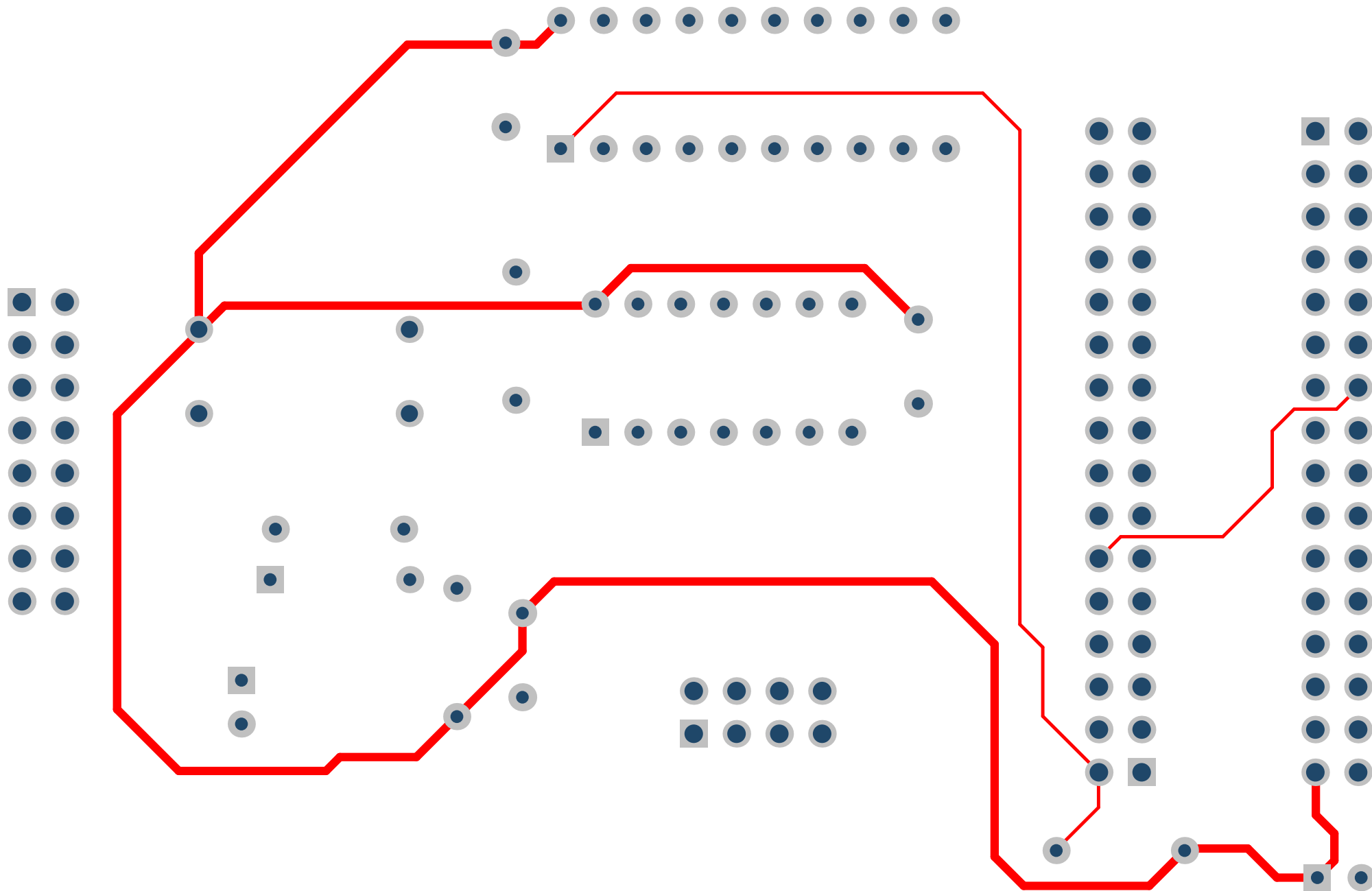
Date: 06-Jul-24

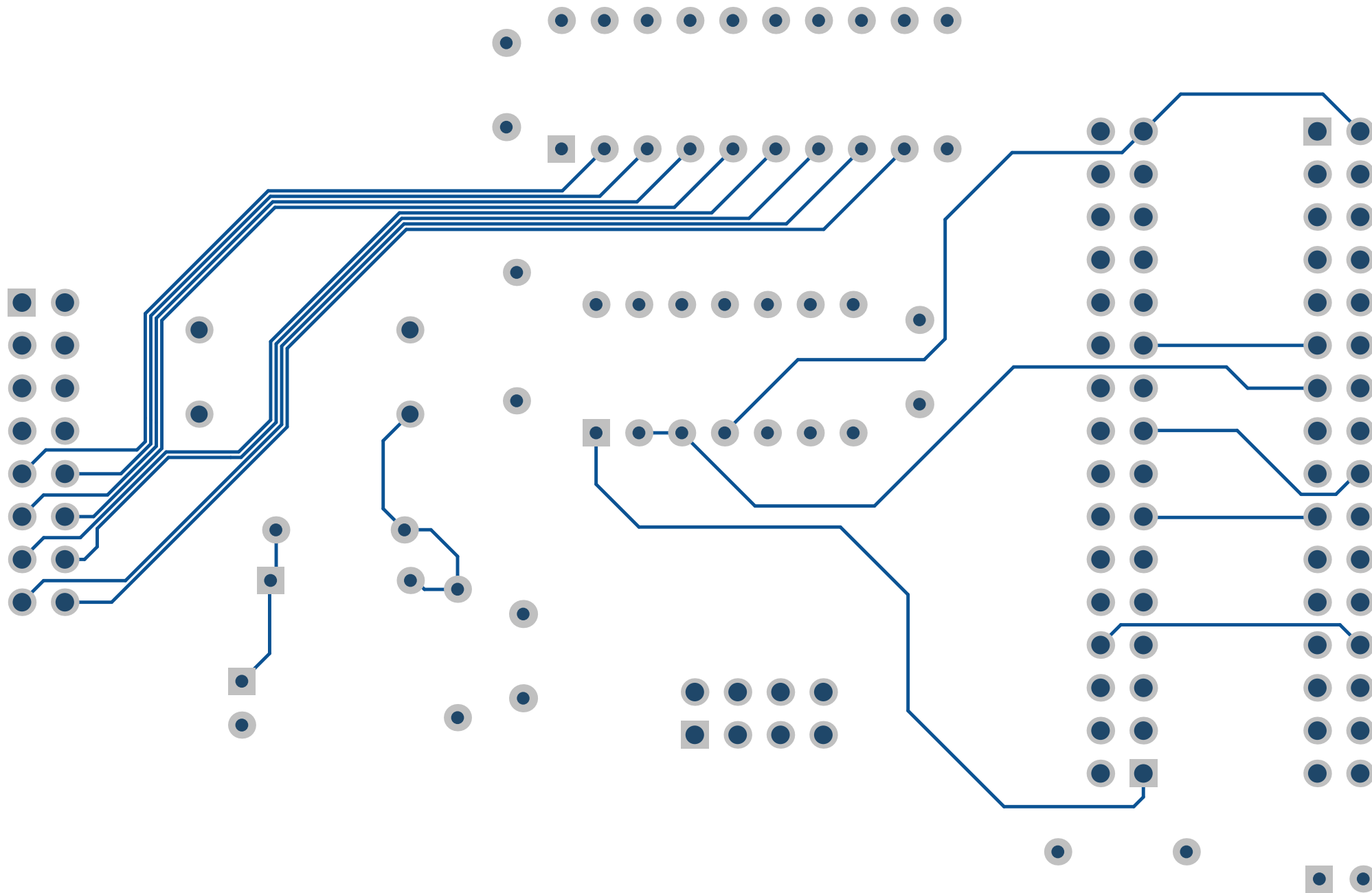
Author: Rafa Hernández

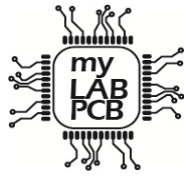












Bill of Materials

myCPU Reset IO module

Description	Value	Q
Electrolytic capacitor 16v/50v	10 μ F	1
Ceramic capacitor	100nF	3
Electrolytic capacitor 16v/50v	1 μ F	1
Switching Signal Diode	1N4148	1
Hex inverters	74xx04	1
Non inverting bus transceiver	74xx245	1
Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 16p	16p	1
Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 32p	32p	1
WR-PHD Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 32p	32p	1
WR-PHD Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 8p	8p	1
Resistor Axial	330 Ω	1
Resistor Axial	100K	1
Resistor Axial	6.8K	1
Resistor Axial	4.7K	1
Tactile button 12 mm		1



Assembly List

myCPU Reset IO module

Designator	Description	Value
C1	Electrolytic capacitor 16v/50v	10 μ F
C2	Ceramic capacitor	100nF
C3	Ceramic capacitor	100nF
C4	Electrolytic capacitor 16v/50v	1 μ F
C5	Ceramic capacitor	100nF
D1	Switching Signal Diode	1N4148
IC1	Hex inverters	74xx04
IC3	Non inverting bus transceiver	74xx245
P1	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 16p	16p
P2	Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 32p	32p
P3	WR-PHD Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 32p	32p
P4	WR-PHD Pin Header, THT, pitch 2.54mm, Dual Row, Vertical, 8p	8p
R2	Resistor Axial	330 Ω
R4	Resistor Axial	100K
R5	Resistor Axial	6.8K
R7	Resistor Axial	4.7K
SW2	Tactile button 12 mm	