



# MUHAMMED YILDIRIM

## CONTACT:

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### Address:

Ankara, Turkey

## ENGLISH :

- IELTS 7.0

## TECHNICAL SKILLS:

- SoC Architecture
- Risc-V Architecture
- ARM Architecture
- VHDL
- Verilog
- FPGA(Xilinx, Microchip)
- C/C++
- MATLAB
- AutoCAD

## EDUCATION

**Bilkent University Computer Engineering Master of Science**  
2023 – (Continue)

**Gazi University Electrical Electronics Engineering(%100 Eng.)**  
2017 - 2022 (With Preparatory Class)  
3.17 /4

**Muradiye Sevgi College Science High School (100% Scholarship)**  
2013 - 2017  
91.67 / 100

## WORK AND INTERNSHIP EXPERIENCE

**Bilkent University – Teaching Assistant**  
Fall 2023 | CS 223: Digital Design  
Spring 2023 | CS 223: Digital Design

**ROKETSAN - Candidate Engineer**  
December 2021 – July 2022

**TUSAŞ - Intern**  
August 2021 – September 2021

**MILMAST - Intern**  
August 2020 – September 2020

**Adaçal Endüstriyel Mineraller A.Ş - Candidate Engineer**  
July 2019 – September 2019

## AWARDS:

TÜBİTAK 2209-A "UNIVERSITY STUDENTS RESEARCH PROJECTS SUPPORT PROGRAM"

Human Detection and Encryption on FPGA with Infrared Camera

- Support Acceptance

CERTIFICATE OF PARTICIPATION  
SSB Roboik 2022 - Unmanned Surface Vehicles Prototype Competition

- Last 10 Team | Armada Team

CERTIFICATE OF PARTICIPATION  
Teknofest 2021 Agricultural Unmanned Ground Vehicle

- Finalist | Patika Team

CERTIFICATE OF PARTICIPATION

- Teknofest 2022 Integrated Circuit Design Competition
- Finalist | Mystic Team

## EXAM SCORES:

**ALES**

Puan: 95,59

## DRIVING LICENSE:

B Class

## PROJECTS

- Gazi Eem Graduation Project, Evaluation Of Performance Parameters Of Cryptology Algorithms Implemented On FPGA, Supported By Tübitak 2209-A
  - Team Captain
  - Designing And Implementation Aes,Des,Rsa Cryptology Algorithm
  - Used Vivado Software and Zedboard Soc
  - Used Uart Protocol
  - Developed Presentation Interface Using C#
- Teknofest 2022 Chip Design Competition, Mystic Team
  - Designing A Customized Processor with RISC-V Instruction Set Architecture (Isa)
  - Designing Uart and SPI Peripherals and Bus Structure
  - Used Openlane Streaming and Open Source Pdk of Skywater 130 Nm Technology
  - Used Verilog HDL
- SSB Roboik 2022 - Unmanned Surface Vehicles Prototype Competition Designing, Armada Team
  - Electronic System Manager
  - Used Uart, Can Bus, I2c And SPI Protocols for Mems Sensors, RF-Transceivers and GPS
  - Used Ros for Autonomous Swim Algorithms
  - Used Motor Drivers and Dc Motors
- Teknofest 2021 - Agricultural Unmanned Ground Vehicle Competition, Patika Team
  - Mechanical System Manager
  - Mechanical Design with SolidWorks and AutoCAD
  - Testing And Simulations with Ansys
  - Production Process
  - Designed Electronic System
  - Used Motor Drivers and DC Motors