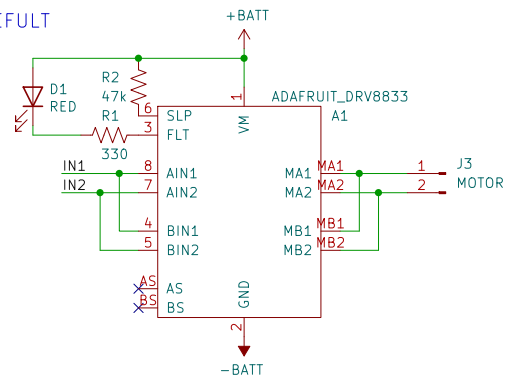


SLEEP PIN DISABLED BY DEFULT
LOGIC HIGH TO ENABLE

FAULT PIN WILL GO LOW
WHEN THERMAL SHUTDOWN
OR OVERCURRENT



CONTROL

INT 1 – INPUT 1, CONTROLS STATE OF M1
INT 2 – INPUT 2, CONTROLS STATE OF M2

PWM SPEED CONTROL

xIN1	xIN2	FUNCTION
PWM	0	FORWARD PWM, FAST DECAY
1	PWM	FORWARD PWM, SLOW DECAY
0	PWM	REVERSE PWM, FAST DECAY
PWM	1	REVERSE PWM, SLOW DECAY

OPERATING CONDITIONS

VBATT	2.7 to 10.8 V
DIGITAL INPUTS	0.0 to 5.75 V
CONTINUOUS CURRENT	2 A
PEAK CURRENT	4 A

Sheet: 1 of 1



University of California,
Santa Barbara

Department of Mechanical Engineering

Project:

TITLE:

DC Motor Speed Controller

DWG.No.:
21 05 24 01

REV
A

DESIGNED BY:
T. Marks

DATE:
2021-05-24