M480 CPU Support Package Guide

Version: 4.0

Contents

M480 Support Package

This guide describes the following features of the M480 CPU support package:

How to create M480 projects

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Creating M480 Projects

Creating an M480 C/C++ executable project

To create a new minimal C/C++ executable project:

> Select the **File > New > New Project** menu item.
>
> Select the **A C/C++ executable for Nuvoton M480** project template.
>
> Set the required project name and location directory.
>
> Click **Next**.
>
> If required, change any of the default project settings.
>
> Click **Finish** to create the project.

Creating an M480 library project

To create a new library project:

> Select the **File > New > New Project** menu item.
>
> Select the **A library for Nuvoton M480** project template.
>
> Set the required project name and location directory.
>
> Click **Next**.
>
> If required, change any of the default project settings.
>
> Click **Finish** to create the project.

Creating an M480 externally built executable project

To create a new project that will allow you to debug an existing externally built executable file:

> Select the **File > New > New Project** menu item.
>
> Select the **An externally built executable for Nuvoton M480** project template.
>
> Set the required project name and location directory.
>
> Click **Next**.
>
> Set the **Load File** project property to point to the executable file you want to download and debug.
>
> If required, change any of the other default project settings.
>
> Click **Finish** to create the project.

Creating an M480 CrossWorks Tasking Library executable project

To create a new C/C++ executable project configured to use the CrossWorks Tasking Library:

> Select the **File > New > New Project** menu item.
>
> Select the **A CrossWorks Tasking Library executable for Nuvoton M480** project template.
>
> Set the required project name and location directory.
>
> Click **Next**.

If required, change any of the other default project settings.

Click **Finish** to create the project.

Creating an M480 assembly code only executable project

Please note, this template does not add any C/C++ startup code or libraries and is therefore not suitable for creating projects that include C/C++ code.

To create a new assembly code only executable project without:

Select the **File > New > New Project** menu item.

Select the **An assembly code only executable for Nuvoton M480** project template.

Set the required project name and location directory.

Click **Next**.

If required, change any of the other default project settings.

Click **Finish** to create the project.

Opening M480 Sample Solutions

M480 Samples Solution

This solution contains general sample projects that run on M480 devices. To open the M480 Samples Solution:

Select the **Tools** > **Show Installed Packages** menu item.
Select the **Nuvoton M480 CPU Support Package** link.
Select the **Samples Solutions** > **M480 Samples Solution** link.

M480 CMSIS-DSP Samples Solution

This solution contains sample projects that use the CMSIS-DSP library running on M480 devices. To open the M480 CMSIS-DSP Samples Solution:

Select the **Tools** > **Show Installed Packages** menu item.
Select the **Nuvoton M480 CPU Support Package** link.
Select the **Sample Solutions** > **M480 CMSIS-DSP Samples Solution** link.

M480 Project Properties

Projects creating using the project templates in this support package have the following device specific project properties:

Heap Size

The heap size is set to be 256 bytes when a project is created. The heap size can be modified using the **Heap Size** project property.

Section Placement

You can select the memory configuration you require using the **Section Placement** project property.

For M480 projects, the set of placements are:

> **Flash** - The application runs in internal Flash memory *(default)*.
> **Flash Vectors In RAM** - The application runs in internal Flash memory and exception vectors are copied to RAM memory.
> **Flash Copy To RAM** - The application starts in internal flash and copies itself to run from internal RAM memory.
> **RAM** - The application runs from internal RAM memory only.

Stack Sizes

The main stack size is set to be 256 bytes when a project is created.

The process stack size is set to be 0 bytes when a project is created.

The main and process stack sizes can be modified using the **Main Stack Size** and **Process Stack Size** project properties.

To change the location of the stacks, edit the section placement file and place the *.stack* and *.stack_process* sections as required.

Startup From Reset

By default, the application will only startup from power-on/reset in *Release* configuration. This acts as a safety net in case you accidently download a program in FLASH during development that crashes and prevents the debugger from taking control of the target over the debug interface thus rendering the device unusable.

For M480 projects, the **Startup From Reset** project property can be set to one of the following:

> **No** - The application will not startup from reset.

Release Only - The application will only startup from reset when built in *Release* configuration *(default)*.

Yes - The application will always startup from reset.

Target Processor

Once a project has been created you can target different devices by modifying the **Target Processor** project property. See the **M480 Devices** section for details on the files, preprocessor definitions and macro definitions used when a device is selected.

M480 Project Templates

The project template system simplifies the creation of new projects with the IDE, it also system makes it easy to create new projects with a text editor or script. All that needs to be specified is the project name, the support packages that the project is dependent on, the target processor and the source files you want to add to the project. For example, create a file called *example.hzp* with the following contents:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="M480_EXE">
    <configuration Name="Common" package_dependencies="M480" Target="M483KGCAE2A" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

You can also add any other property settings that the project requires such as preprocessor definitions or include paths using the property save name, for example:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="M480_EXE">
    <configuration Name="Common" package_dependencies="M480" Target="M483KGCAE2A"
 c_preprocessor_definitions="MYDEF1=1;MYDEF2=TWO" c_user_include_directories="$(ProjectDir)/
include1;$(ProjectDir)/include2" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

Available M480 project templates

Template Name	Template Description
M480_ASM_EXE	M480 Assembly Code Only Executable
M480_CTL_EXE	M480 CTL Executable
M480_EXE	M480 C/C++ Executable
M480_EXT_EXE	M480 Externally Built Executable
M480_LIB	M480 Library

M480 Devices

This package supports the following M480 devices:

M480 Family

M480 Family

M481ZGDAE

M481ZGAAE

M481ZIDAE

M481LGAAE

M481LIDAE

M481SGAAE

M481SIDAE

M481ZG8AE

M481ZGCAE

M481ZE8AE

M481LG8AE

M481LGCAE

M481LE8AE

M481SG8AE

M481SGCAE

M481SE8AE

M481SG8AE2A

M481SGCAE2A

M482ZIDAE

M482LGAAE

M482LIDAE

M482SGAAE

M482SIDAE

M482KGAAE

M482KIDAE

M482ZG8AE

M482ZGCAE

M482ZE8AE

M482LG8AE

M482LGCAE

M482LE8AE

M482SG8AE

M482SGCAE

M482SE8AE

M482KGCAE

M483SGAAE

M483SIDAE

M483KIDAE

M483SG8AE

M483SGCAE

M483SE8AE

M483SGCAE2A

M483SG8AE2A

M483KGCAE

M483KGCAE2A

M483KG8AE2A

M484SGAAE

M484SGAAE2U

M484SIDAE

M484SIDAE2U

M484KIDAE

M485ZIDAE

M485LIDAE

M485SIDAE

M485KIDAE

M487SGAAE

M487SIDAE

M487KIDAE

M487JIDAE

M487KMCAN

M481ZGDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481ZGDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481ZGDAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481ZGAAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481ZGAAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481ZGAAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481ZIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481ZIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481ZIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481LGAAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481LGAAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481LGAAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481LIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481LIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481LIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481SGAAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481SGAAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481SGAAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481SIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481SIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481SIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481ZG8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481ZG8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481ZG8AE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481ZGCAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481ZGCAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481ZGCAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481ZE8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481ZE8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481ZE8AE__

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481LG8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481LG8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481LG8AE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481LGCAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481LGCAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481LGCAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481LE8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481LE8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481LE8AE__

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481SG8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481SG8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481SG8AE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481SGCAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481SGCAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481SGCAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481SE8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481SE8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481SE8AE__

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481SG8AE2A

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481SG8AE2A_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481SG8AE2A__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M481SGCAE2A

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M481SGCAE2A_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M481SGCAE2A__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482ZIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482ZIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482ZIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482LGAAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482LGAAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482LGAAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482LIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482LIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482LIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482SGAAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482SGAAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482SGAAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482SIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482SIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482SIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482KGAAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482KGAAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482KGAAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482KIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482KIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482KIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482ZG8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482ZG8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482ZG8AE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482ZGCAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482ZGCAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482ZGCAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482ZE8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482ZE8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482ZE8AE__

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482LG8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482LG8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482LG8AE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482LGCAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482LGCAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482LGCAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482LE8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482LE8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482LE8AE__

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482SG8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482SG8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482SG8AE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482SGCAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482SGCAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482SGCAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482SE8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482SE8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482SE8AE__

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M482KGCAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M482KGCAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M482KGCAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M483SGAAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M483SGAAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M483SGAAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M483SIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M483SIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M483SIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M483KIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M483KIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M483KIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M483SG8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M483SG8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M483SG8AE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M483SGCAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M483SGCAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M483SGCAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M483SE8AE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M483SE8AE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M483SE8AE__

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_128_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M483SGCAE2A

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M483SGCAE2A_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M483SGCAE2A__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M483SG8AE2A

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M483SG8AE2A_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M483SG8AE2A__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M483KGCAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M483KGCAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M483KGCAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M483KGCAE2A

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M483KGCAE2A_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M483KGCAE2A__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M483KG8AE2A

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_LD_4_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M483KG8AE2A_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M483KG8AE2A__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_LD_4_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M484SGAAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M484SGAAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M484SGAAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M484SGAAE2U

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M484SGAAE2U_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M484SGAAE2U__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M484SIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M484SIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M484SIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M484SIDAE2U

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M484SIDAE2U_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M484SIDAE2U__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M484KIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M484KIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M484KIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M485ZIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M485ZIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M485ZIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M485LIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M485LIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M485LIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M485SIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M485SIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M485SIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M485KIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M485KIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M485KIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M487SGAAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M487SGAAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M487SGAAE__

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20017FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_256_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M487SIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M487SIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M487SIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M487KIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M487KIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M487KIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M487JIDAE

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M487JIDAE_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M487JIDAE__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480

M487KMCAN

Device Details	
CMSIS Header File	$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
CMSIS Include Path	$(TargetsDir)/M480/CMSIS/Device/M480/Include
CMSIS System File	$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
Family	M480
Sub Family	M480
Loader File	$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
Memory Map File	$(TargetsDir)/M480/XML/M487KMCAN_MemoryMap.xml
Register Definition File	$(TargetsDir)/M480/XML/M481_v1_Registers.xml
Vectors File	$(TargetsDir)/M480/Source/M481_v1_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
__M480_FAMILY
__M480_SUBFAMILY
__M487KMCAN__

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/M480/CMSIS/Device/M480/Include
DeviceHeaderFile=$(TargetsDir)/M480/CMSIS/Device/M480/Include/M480.h
DeviceLoaderFile=$(TargetsDir)/M480/Loader/M481_AP_512_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/M480/XML/M481_v1_Registers.xml
DeviceSystemFile=$(TargetsDir)/M480/CMSIS/Device/M480/Source/system_M480.c
DeviceVectorsFile=$(TargetsDir)/M480/Source/M481_v1_Vectors.s
DeviceFamily=M480
DeviceSubFamily=M480