



NGUYEN CANH MY

PHYSICAL DESIGN INTERN

OBJECTIVES

To improve skills in the major, especially in IC back-end designing and be familiar with working environment once graduate.

EDUCATION

Final-year student major in Electronics at Bach Khoa University (HCMUT).

CONTACT

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Ho Chi Minh city

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ACADEMIC KNOWLEDGE

- English (TOEIC 690)
- IC design with cadence (understand fast algorithms and perform in digital signal processing)
- Software skill in Linux Bash Shell
- FPGA design in Quartus tools (project implementation on Altera DE1 FPGA board)
- C and Verilog program language

WORK EXPERIENCE

HOUSEHOLD ELECTRICITY APPLIANCE MAINTENANCE

JUN 2017- SEP 2017

- Electrician's helper
- installation of electrical systems in the house

INTERNSHIP AT ICDREC COMPANY

JUL 2019 - SEP 2019

- Project implementation on Altera DE1 FPGA board

TECHNOLOGY LEADER AT FROGSLEAP FOUNDATION

MAR 2019 - MAR 2020

- Plan, divide works for members and keep track
- Frogsleap's assembling green technology project in remote areas
- Joining in an education camp with singaporean